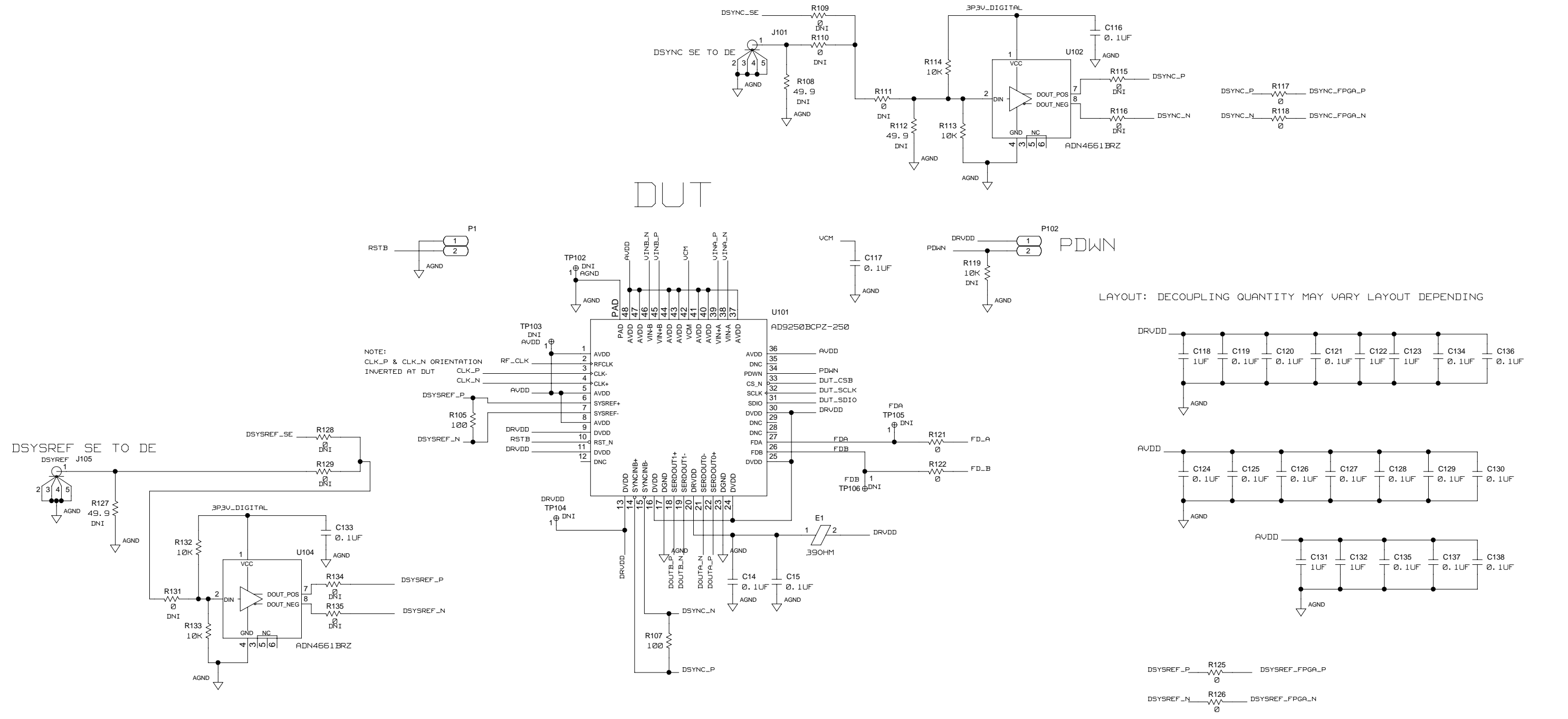


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



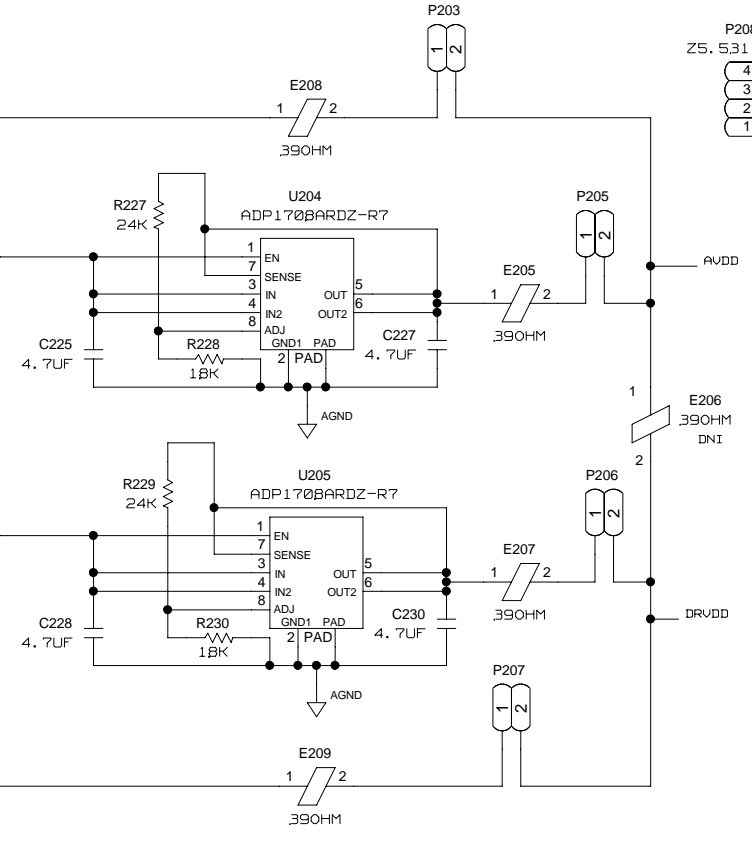
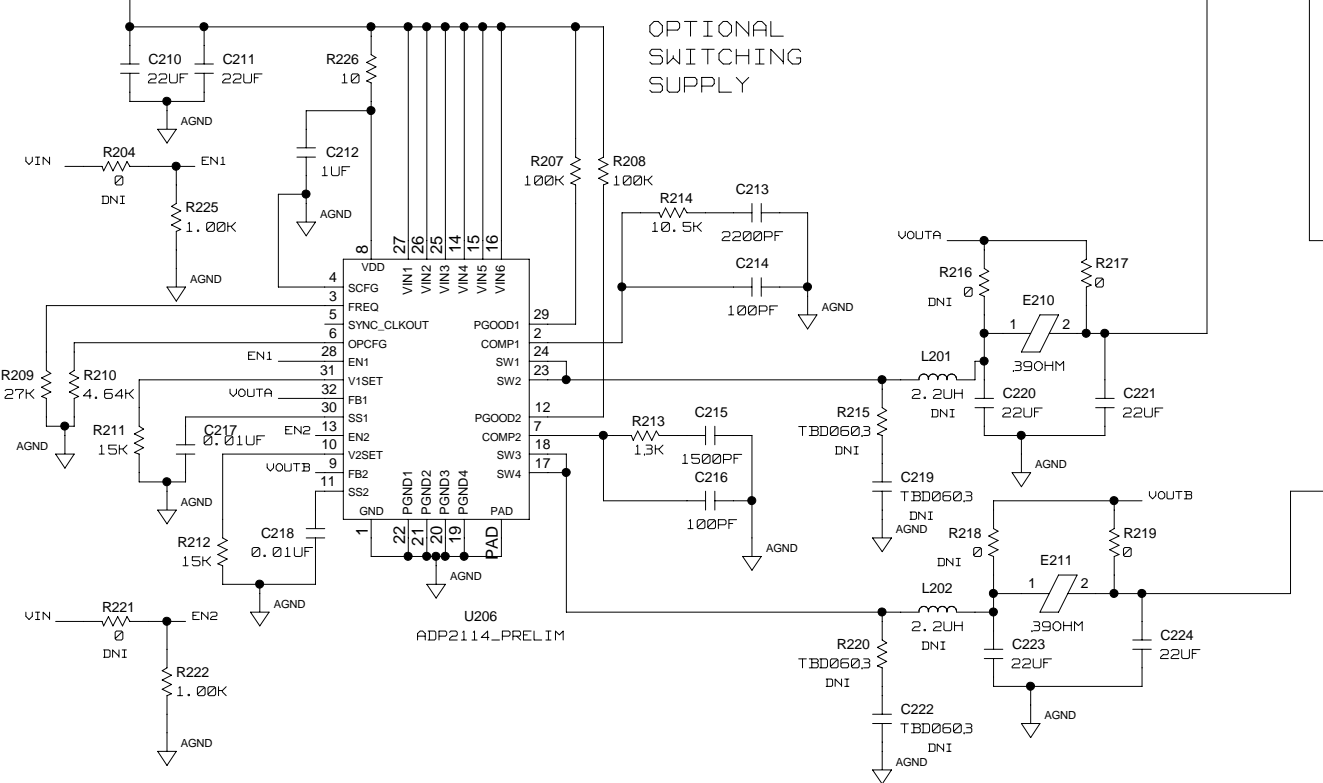
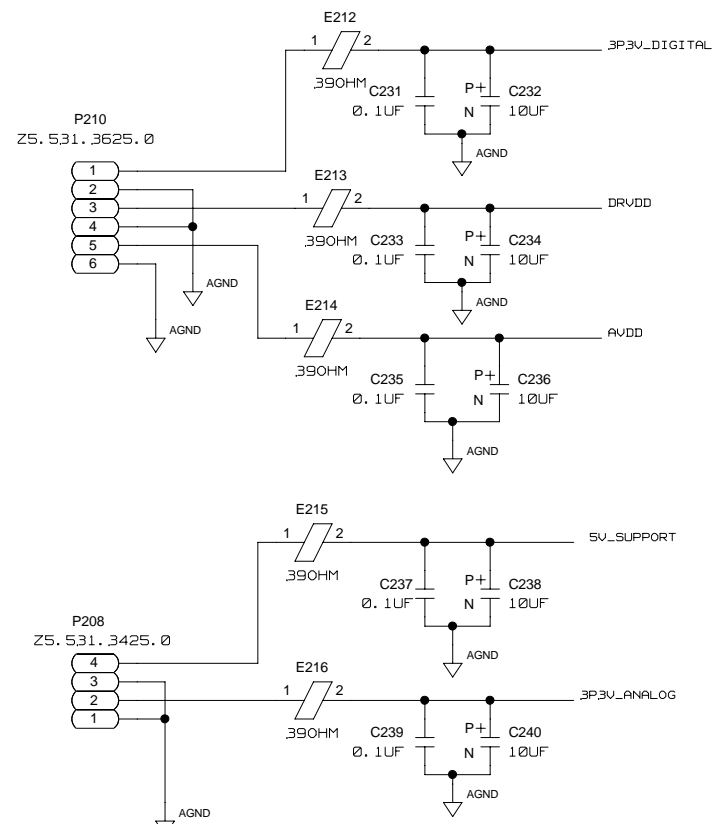
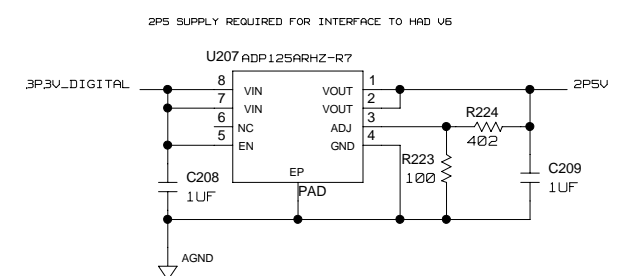
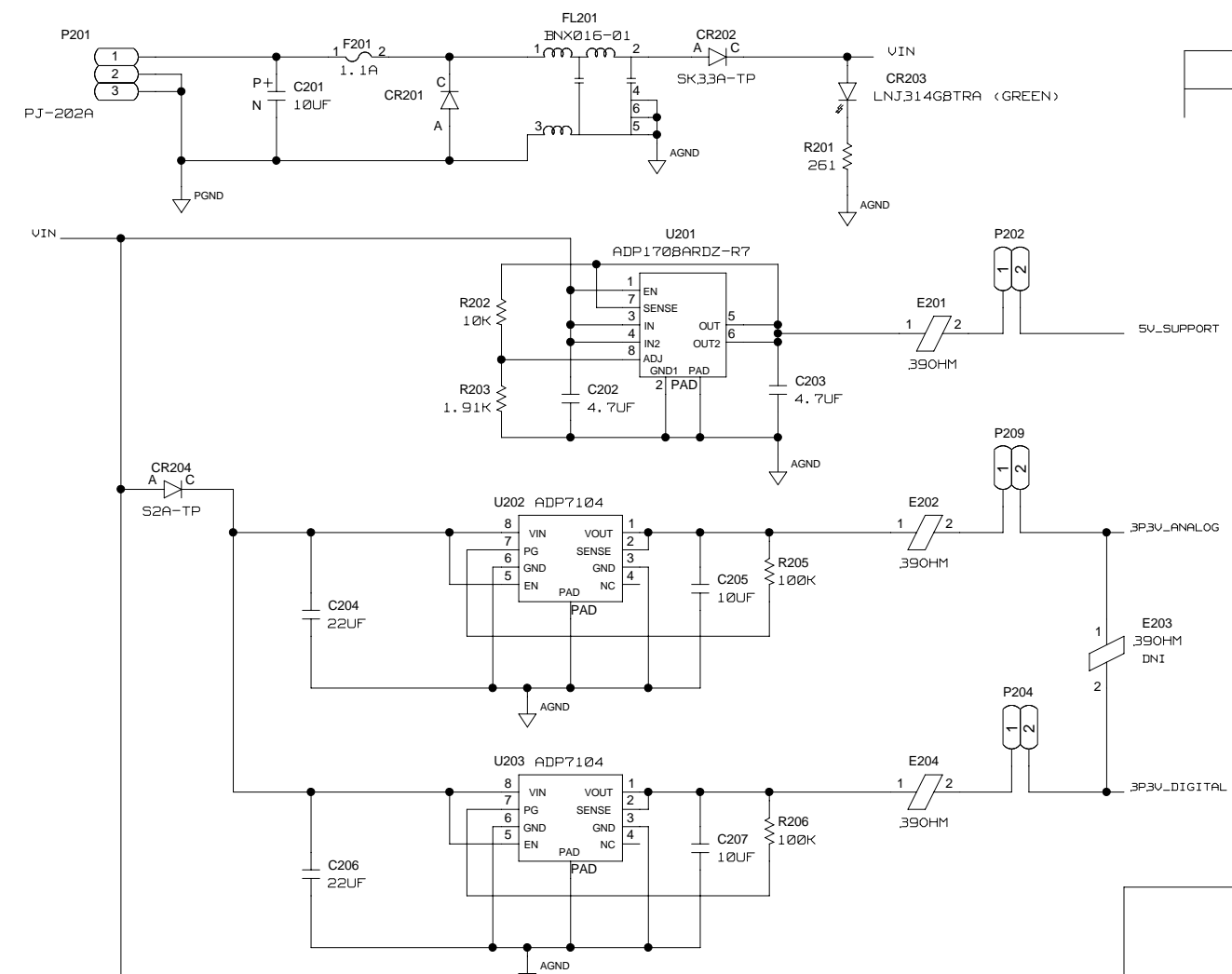
LAYOUT: DECOUPLING QUANTITY MAY VARY LAYOUT DEPENDING

DA_N	C110	0.001UF	DOUTA_N
DA_P	C111	0.001UF	DOUTA_P
DB_N	C112	0.001UF	DOUTB_N
DB_P	C113	0.001UF	DOUTB_P

	SCHEMATIC		
	AD9250 EVALUATION BOARD		
<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED BY ANY OTHER PARTY WITHOUT THE WRITTEN PERMISSION OF ANALOG DEVICES. THE EQUIPMENT DESIGN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.</small>	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. HSC 1200B	REV B1
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE <SCALE>	SHEET 2 OF 7

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

POWER



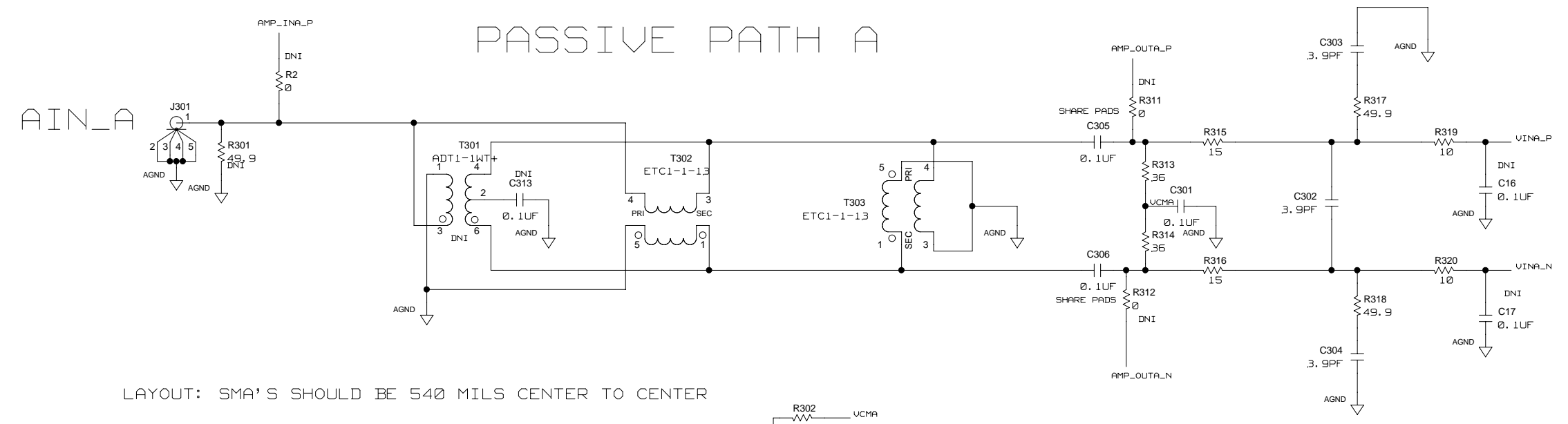
TO EVALUATE SHARING OF AVDD AND DVDD

	SCHEMATIC		
	AD9250 EVALUATION BOARD <PRODUCT> <PRODUCT_1>		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. HSC 12008	REV B1	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE <SCALE>	SHEET 3 OF 7

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

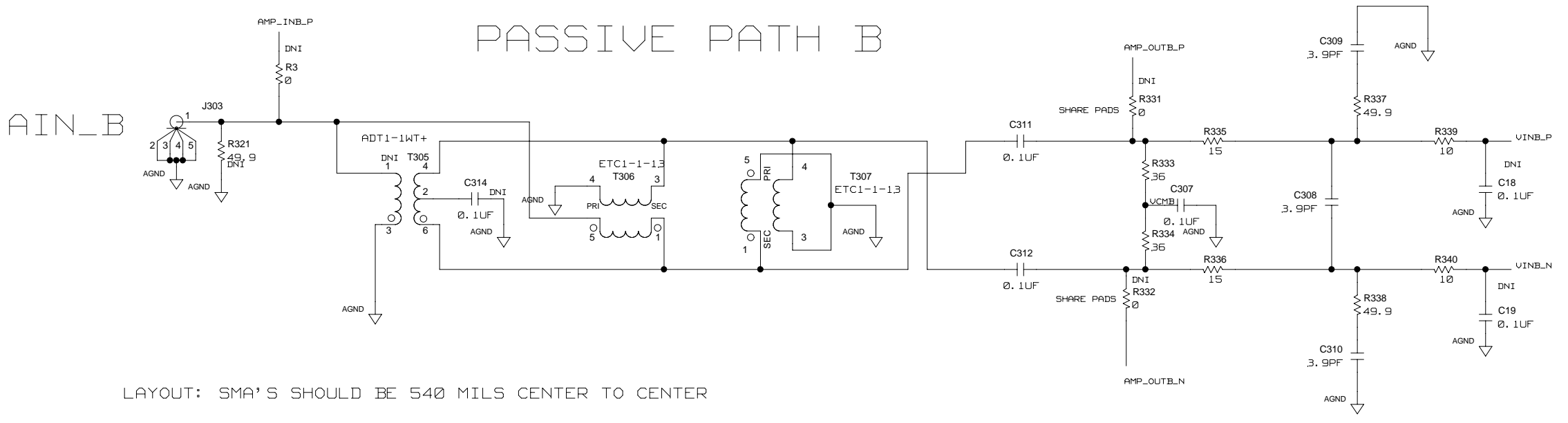
ANALOG INPUT

PASSIVE PATH A



LAYOUT: SMA'S SHOULD BE 540 MILS CENTER TO CENTER

PASSIVE PATH B

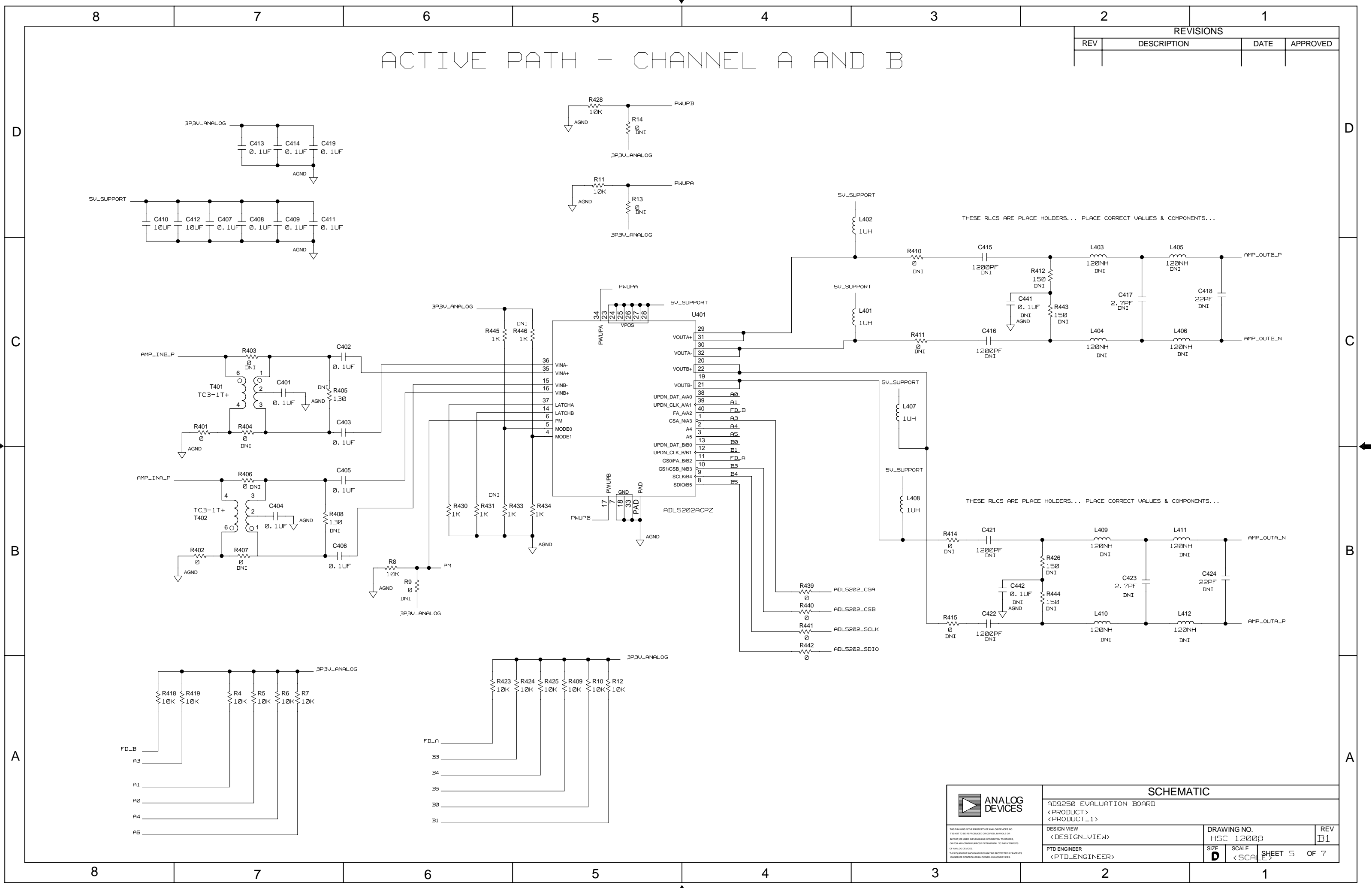


LAYOUT: SMA'S SHOULD BE 540 MILS CENTER TO CENTER

	SCHEMATIC		
	AD9250 EVALUATION BOARD <PRODUCT> <PRODUCT_1>		
<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED BY ANY OTHER PARTY WITHOUT THE WRITTEN PERMISSION OF ANALOG DEVICES. THE EQUIPMENT DESIGN HEREIN MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.</small>	DESIGN VIEW <DESIGN_VIEW> PTD ENGINEER <PTD_ENGINEER>	DRAWING NO. HSC 1200B SIZE D SCALE <SCALE>	REV B1 SHEET 4 OF 7

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

ACTIVE PATH - CHANNEL A AND B



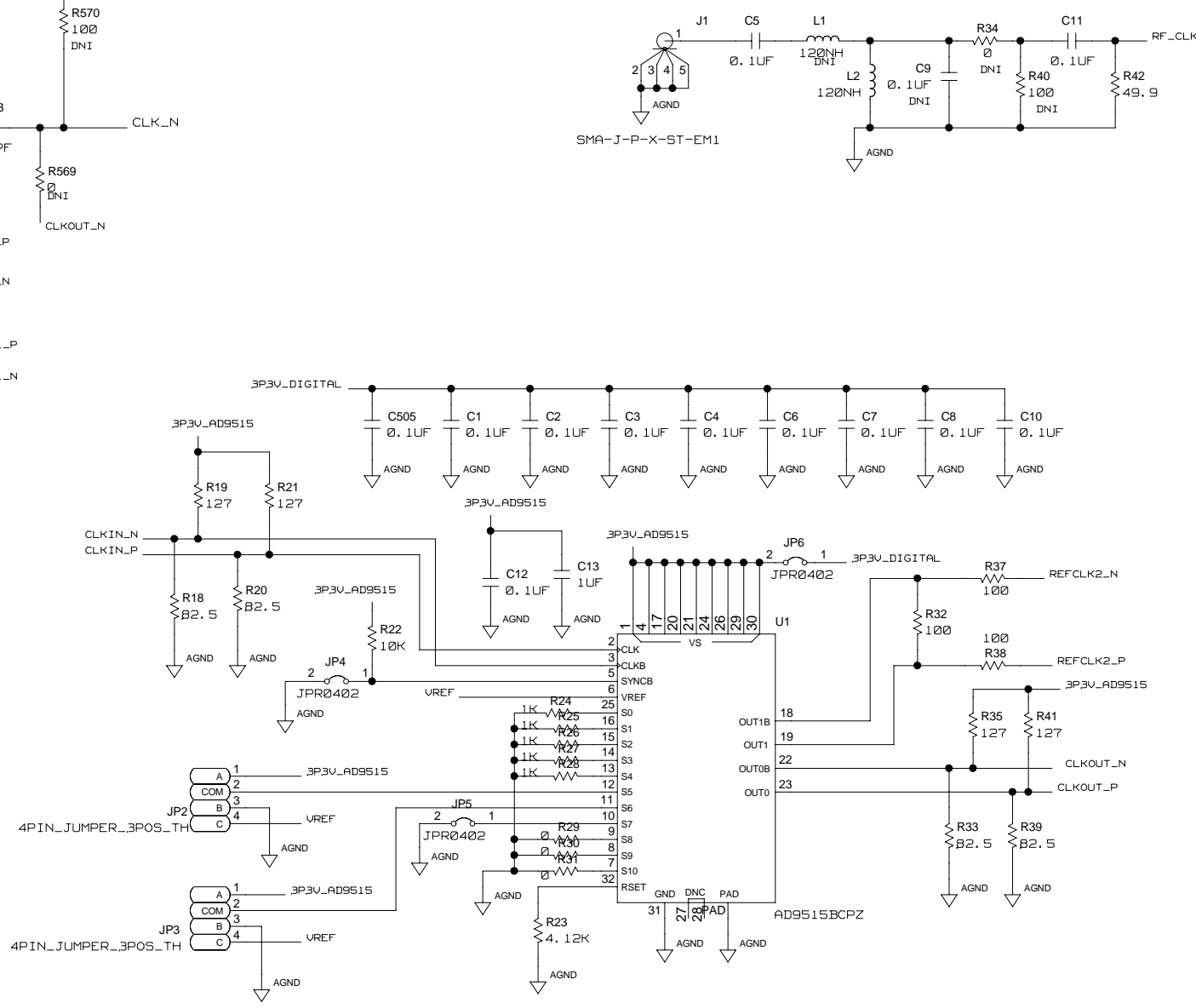
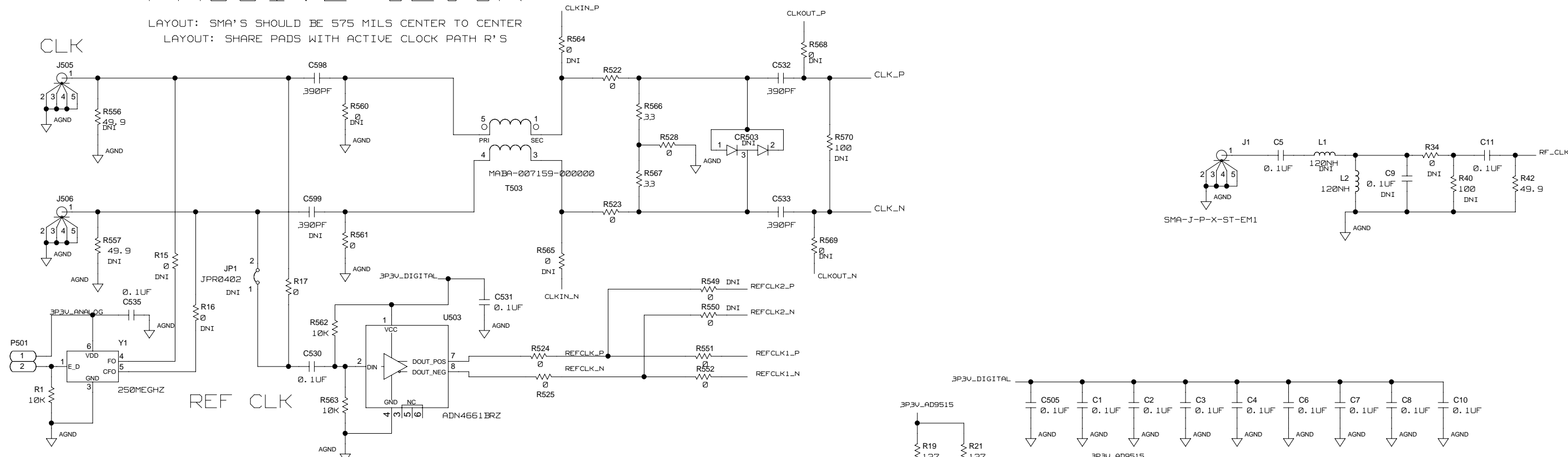
	SCHEMATIC		
	AD9250 EVALUATION BOARD		
<PRODUCT> <PRODUCT_1>		DRAWING NO.	REV
DESIGN VIEW		HSC 1200B	B1
<DESIGN_VIEW>		SIZE	SCALE
PTD ENGINEER		<SCALE>	SHEET 5 OF 7
<PTD_ENGINEER>			

ACTIVE CLOCK PATH

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

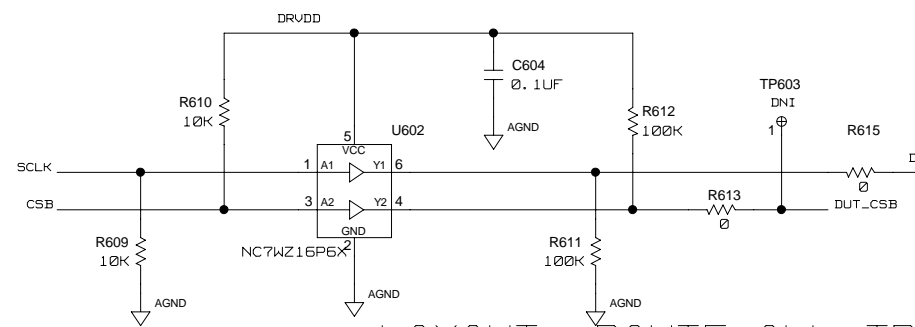
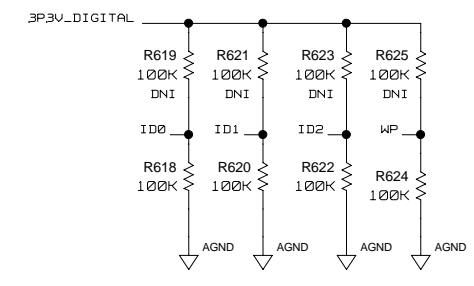
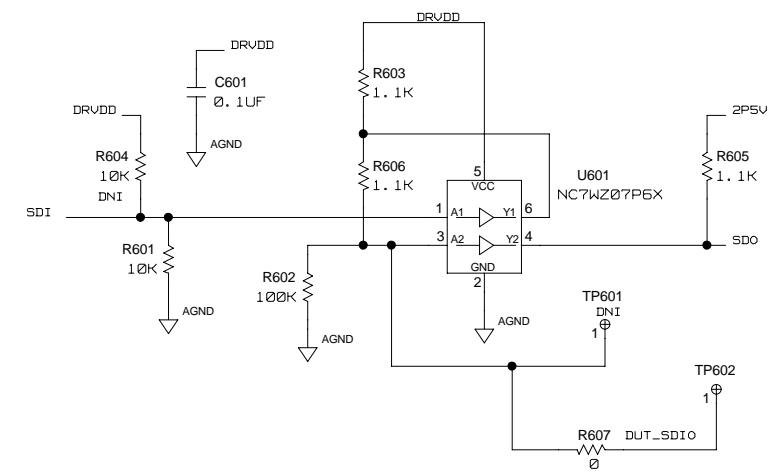
PASSIVE CLOCK

LAYOUT: SMA'S SHOULD BE 575 MILS CENTER TO CENTER
LAYOUT: SHARE PADS WITH ACTIVE CLOCK PATH R'S

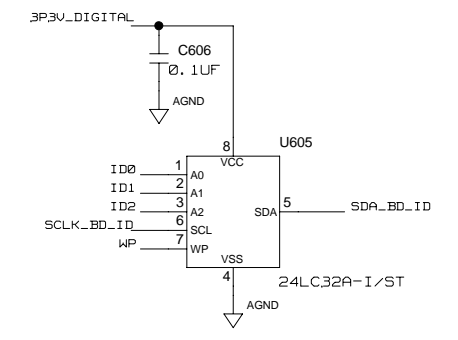
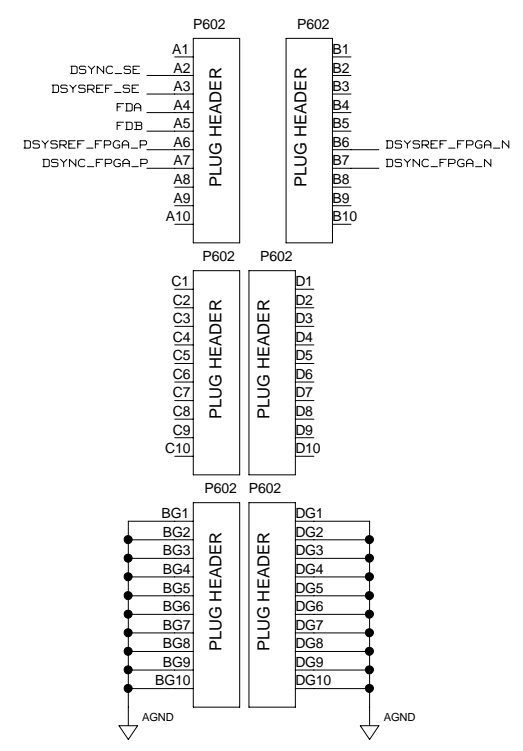
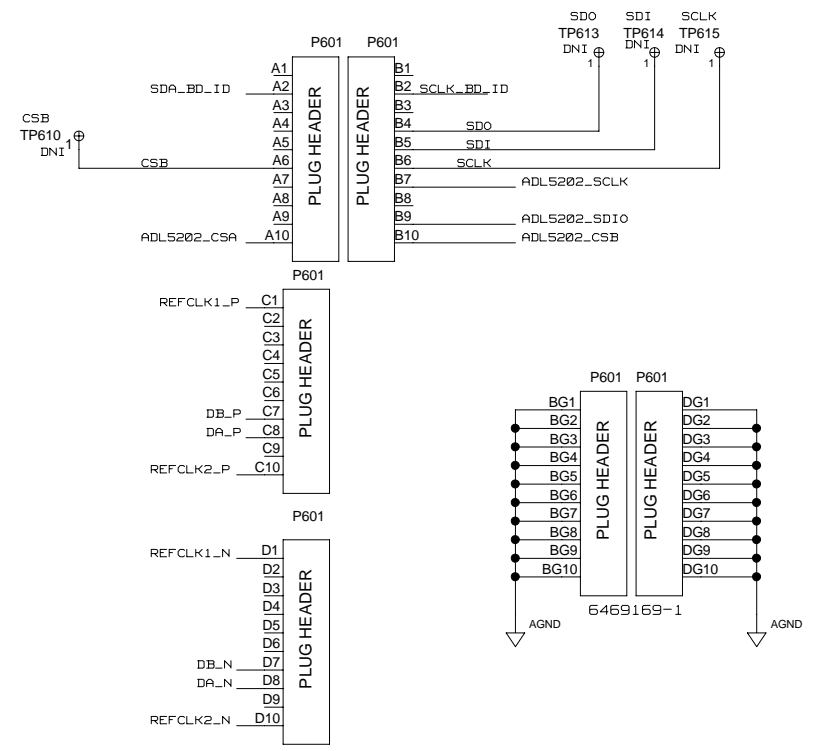


	SCHEMATIC		
	AD9250 EVALUATION BOARD <PRODUCT> <PRODUCT_1>		
<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN ANY MANNER WITHOUT THE WRITTEN PERMISSION OF ANALOG DEVICES. THE EQUIPMENT DESIGN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.</small>	DESIGN VIEW <DESIGN_VIEW> PTD ENGINEER <PTD_ENGINEER>	DRAWING NO. HSC 1200B SIZE D SCALE <SCALE>	REV B1 SHEET 6 OF 7

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



LAYOUT: ROUTE ALL TRACES TO THE TYCO CONN ON TOP OF BOARD USING HADV6



SCHEMATIC			
		AD9250 EVALUATION BOARD <PRODUCT> <PRODUCT_1>	
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. HSC 12008	REV B1	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE <SCALE>	SHEET 7 OF 7