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REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

EVAL-LTC2854/59-ARDZ SCHEMATIC REV (1)

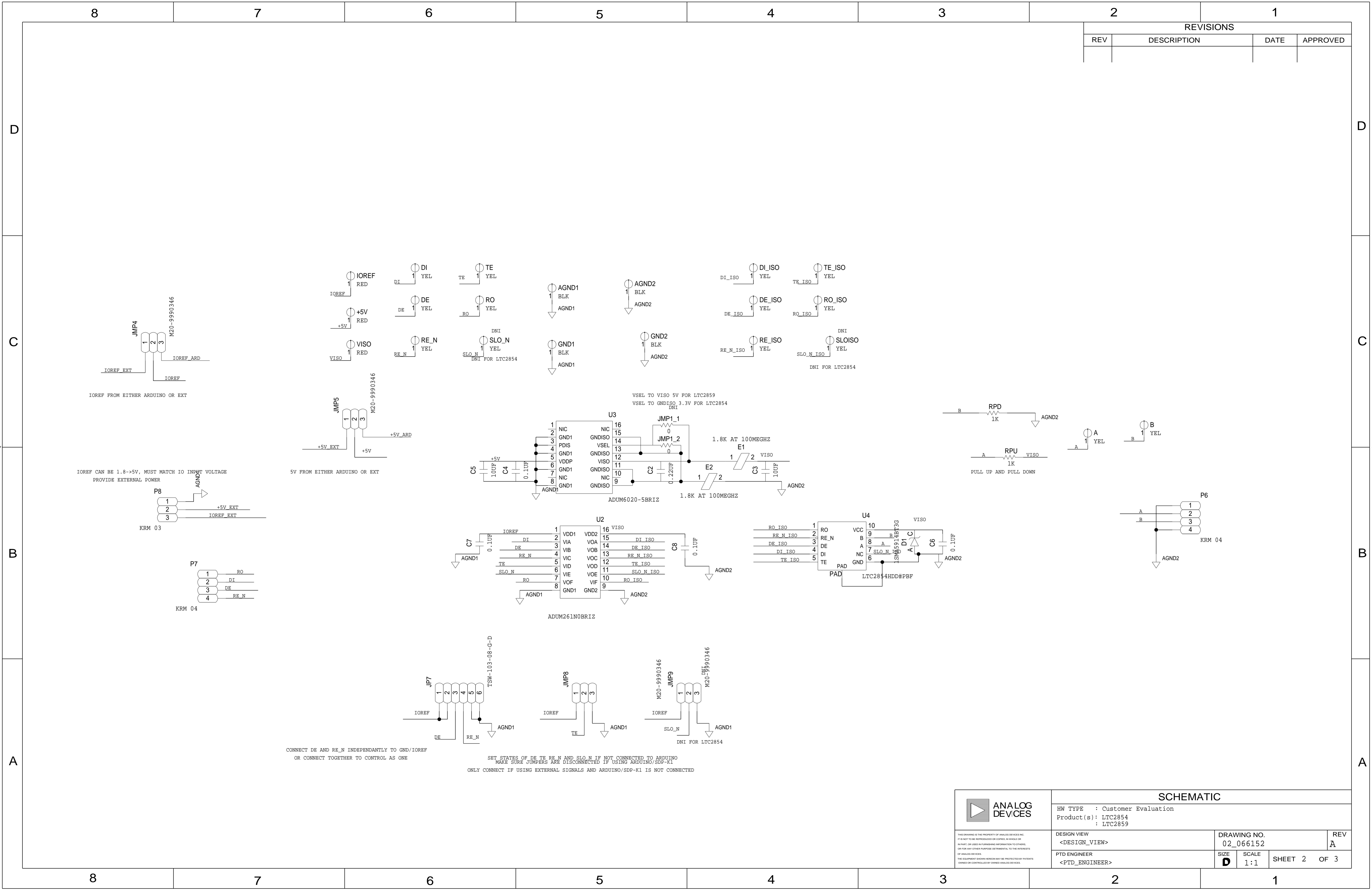
PAGE 2 - CIRCUIT

PAGE 3 - ARDUINO UNO HEADER

P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER

TEMPLATE ENGINEER -	DATE	SCHEMATIC		
HARDWARE SERVICES -		HW TYPE : Customer Evaluation		
HARDWARE SYSTEMS -		Product(s): LTC2854		
TEST ENGINEER -		: LTC2859		
COMPONENT ENGINEER -		PACKAGE : 100-lead 3X3_MM DFN-family		
TEST PROCESS -		: Pitch-pitch StyleVendor Style		
HARDWARE RELEASE -		<User Define>		
DESIGNER -		<User Define>		
PTD ENGINEER <PTD_ENGINEER>		<User Define>		
CHECKER -		MASTER PROJECT TEMPLATE TBD	TESTER TEMPLATE 064489_b	DRAWING NO. 02_066152
		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		REV. A
		TOLERANCES		
		DECIMALS X.XX +0.010 X.XXX +0.005	FRACTIONS +1/32	ANGLES +2
		SIZE D	SCALE 1:1	CODE ID NO. CodeID
				SHEET 1 OF 3

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



	SCHEMATIC		
	HW TYPE : Customer Evaluation Product(s) : LTC2854 : LTC2859		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_066152	REV A
	PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1
		SHEET 2 OF 3	

8 7 6 5 4 3 2 1

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

D

D

C

C

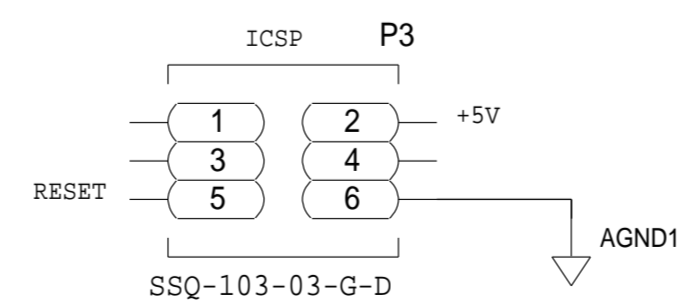
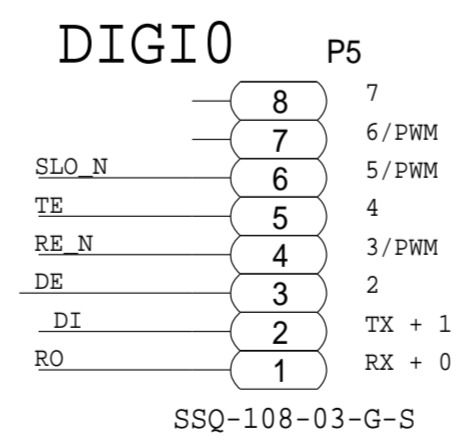
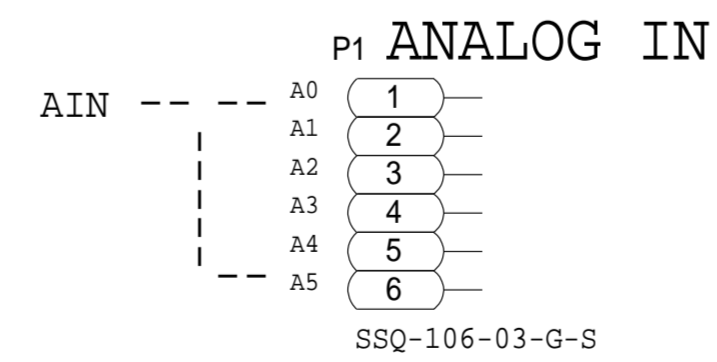
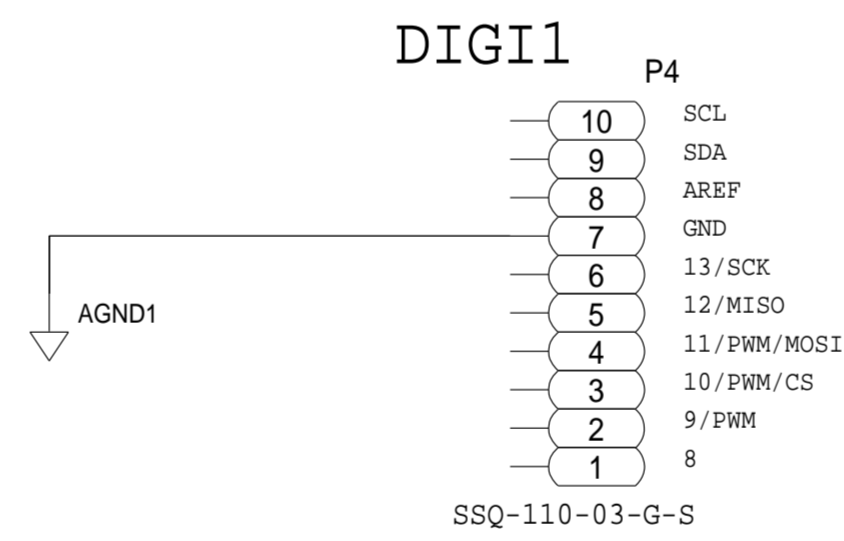
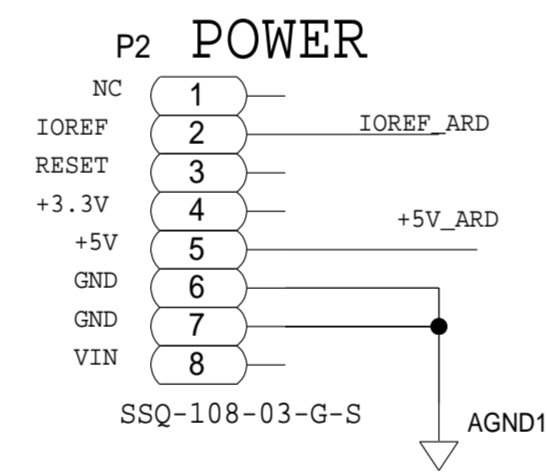
B

B

A

A

IOREF IS 1.8V OR 3.3V FOR SDP-K1 AND 5V FOR ARDUINO



MANDATORY: THE ICSP MUST BE PLACED TO ENSURE YOUR DESIGNED SHIELD CAN BE STACKED WITH OTHER ARDUINO SHIELDS.

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	HW TYPE : Customer Evaluation Product(s) : LTC2854 : LTC2859		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_066152	REV A
	PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1
		SHEET 3 OF 3	

8 7 6 5 4 3 2 1