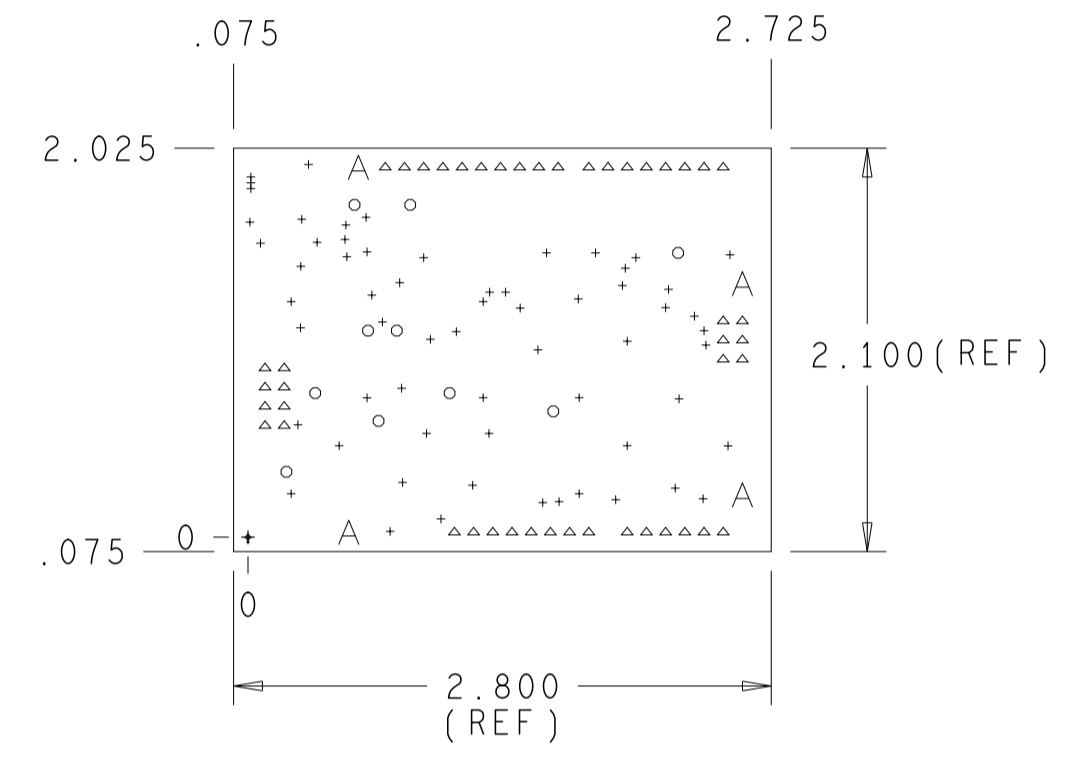



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	14FEB14	J.CHUA
B		02APR14	J.CHUA

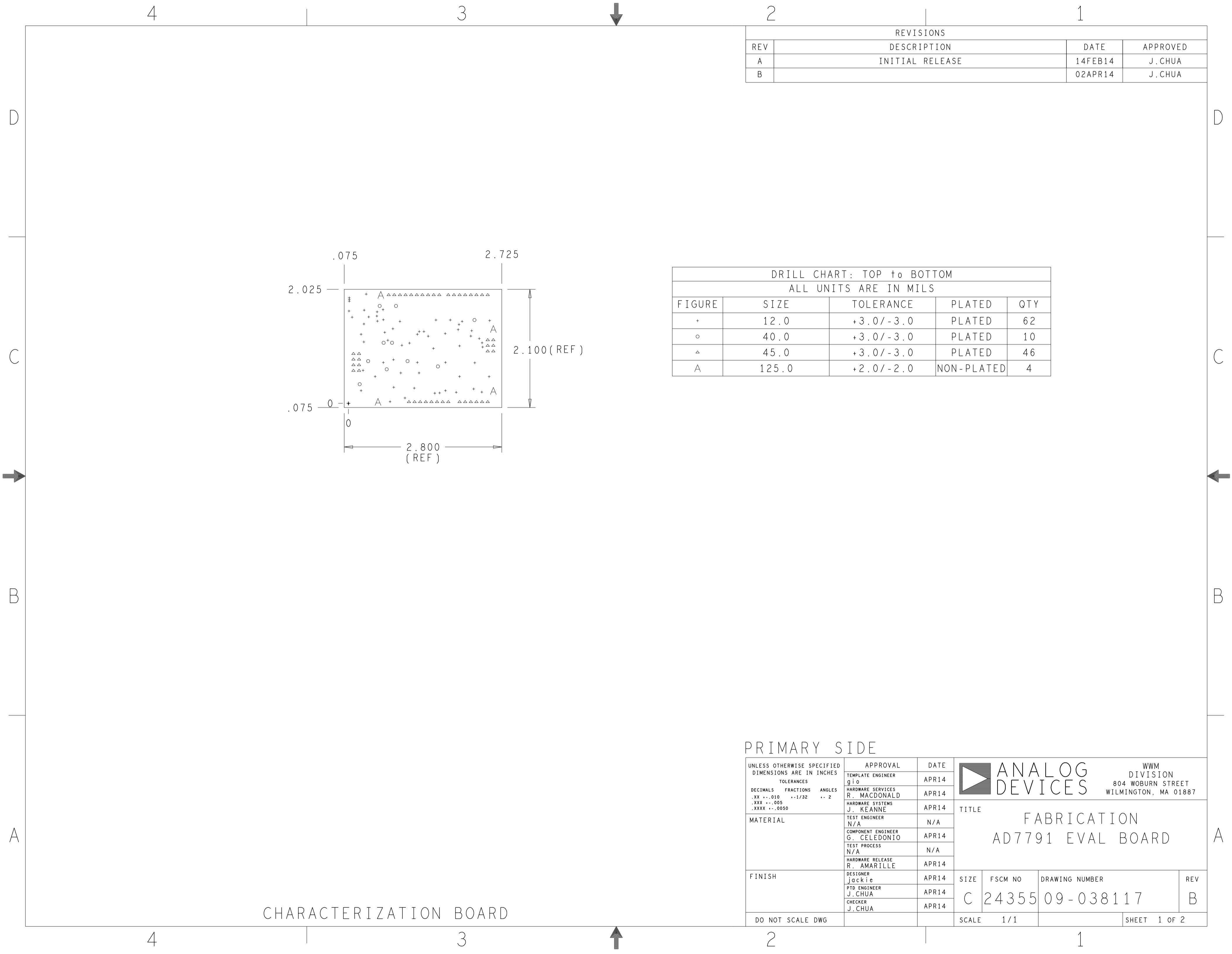


DRILL CHART: TOP to BOTTOM				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	TOLERANCE	PLATED	QTY
+	12.0	+3.0/-3.0	PLATED	62
o	40.0	+3.0/-3.0	PLATED	10
^	45.0	+3.0/-3.0	PLATED	46
A	125.0	+2.0/-2.0	NON-PLATED	4

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX -.010 --1/32 -- 2 .XXX -.005 .XXXX -.0050	APPROVAL	DATE	 WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887			
	TEMPLATE ENGINEER gio	APR14				
	HARDWARE SERVICES R. MACDONALD	APR14				
	HARDWARE SYSTEMS J. KEANNE	APR14				
MATERIAL	TEST ENGINEER N/A	N/A	TITLE FABRICATION AD7791 EVAL BOARD			
	COMPONENT ENGINEER G. CELEDONIO	APR14				
	TEST PROCESS N/A	N/A				
	HARDWARE RELEASE R. AMARILLE	APR14				
FINISH	DESIGNER jackie	APR14	SIZE	FSCM NO	DRAWING NUMBER	REV
	PTD ENGINEER J.CHUA	APR14	C	24355	09-038117	B
	CHECKER J.CHUA	APR14	SCALE 1/1		SHEET 1 OF 2	

CHARACTERIZATION BOARD



4

3

2

1

SPECIFICATIONS:

MATERIALS; ALL LAMINATES AND BONDING MATERIALS SHOULD BE SELECTED FROM IPC-4101 OR IPC-4103, MINIMUM Tg>170degC, Td>300degC, U.L. RATING OF 94 V-0

MATERIAL FAMILY; FR4

CLADDING; EXTERNAL LAYERS .5 OZ. COPPER, OVERPLATE TO 1.5 OZ.

NOTE: IF THE LAYER STACKUP CONFLICTS WITH THE ABOVE CLADDING SPECIFICATIONS THEN THE LAYER STACKUP SHALL TAKE PRECEDENCE.

SOLDER MASK; SHALL BE LIQUID PHOTOIMAGEABLE (LPI) APPLIED ON BOTH SIDES OVER BARE COPPER OR GOLD AND SHALL MEET IPC-SM-840 (LATEST REV.) CLASS 3. COLOR GREEN.

SILK SCREEN; SHALL BE PERMANENT NON-CONDUCTIVE EPOXY INK, COLOR: WHITE SYNTHETIC INKJET PRINTING ALLOWED FOR DENSE BOARDS, COLOR: WHITE

SURFACE FINISH; ENIG (Electroless Nickel/Immersion Gold) 100 MICRO INCHES NICKEL/ 3-5 MICRO INCHES GOLD

INTENTIONAL SHORTS; NO INTENTIONAL SHORTS FOR THIS DESIGN.

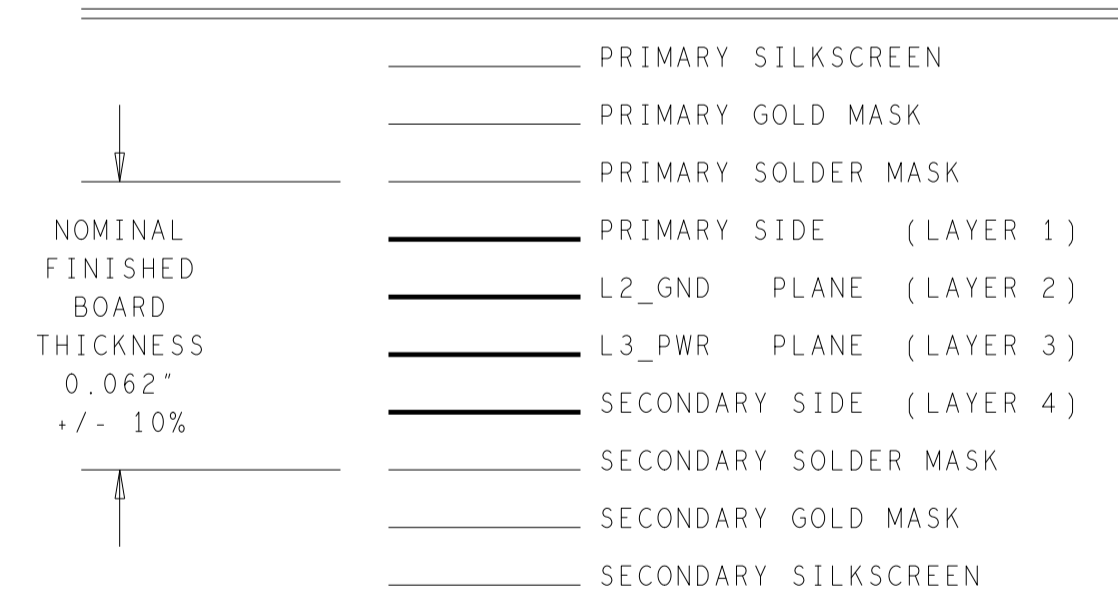
TEST REQUIREMENTS; +100% NETLIST ELECTRICAL VERIFICATION USING CUSTOMER SUPPLIED IPC-D-356 NETLIST FOR OPENS AND SHORTS WHEN "GERBER DATA" IS PROVIDED. THIS VERIFICATION ALSO REQUIRED FOR "ODB++" DATA PER EMBEDDED NETLIST.

ROHS COMPLIANCE NOTE: HOMOGENOUS MATERIALS IN THIS BOARD SHALL BE COMPLIANT THE EU RoHS DIRECTIVE 2002/95/EC

REQUIREMENTS:

- REFER TO IPC-6010 SERIES (LATEST REV.), CLASS 2 FOR FABRICATION UNLESS OTHERWISE SPECIFIED.
- ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00115, (LATEST REVISION.)
- MODIFICATIONS TO THE ARTWORK ARE NOT ALLOWED WITHOUT WRITTEN AUTHORIZATION.
- HOLE PATTERN TOLERANCES FOR UNDIMENSIONED HOLES SHALL BE A DIAMETER OF 0.005 INCHES FROM THEIR TRUE POSITION.
- PLATED HOLE WALL THICKNESS SHALL NOT BE LESS THAN 0.001 INCH MINIMUM AVERAGE, WITH NO READING LESS THAN .0008 BY CROSS SECTION.
- HOLE DIAMETERS APPLY AFTER PLATING.
- FINISHED CONDUCTOR WIDTHS SHALL NOT BE REDUCED FROM THE NOMINAL INDICATED ON THE MASTER PATTERN, BY MORE THAN THE CONDUCTOR THICKNESS.
- MINIMUM DESIGN LINE WIDTH IS .010 INCH.
- MINIMUM DESIGN SPACING IS .008 INCH.
- NON-FUNCTIONAL PAD REMOVAL FROM INNER SIGNAL LAYERS MAY BE PERFORMED AFTER CUSTOMER APPROVAL.
- IF PAD SIZES PROVIDED ARE NOT LARGE ENOUGH TO MAINTAIN ANNULAR RING REQUIREMENT, MFGR. MAY REQUEST APPROVAL TO TEAR DROP PADS TO MAINTAIN ANNULAR RING. (AT PAD TO TRACE INTERSECTION ONLY AND ELECTRICAL INTEGRITY MUST BE MAINTAINED.)
- THIEVING MAY BE ADDED TO COMPENSATE FOR LOW COPPER DENSITY AREAS ON THIS DESIGN ONLY AFTER REVIEW AND APPROVAL FROM THE CUSTOMER:
 - A. THIEVING TO CARD EDGE, FIDUCIALS, NON-PLATED THROUGH HOLES, ALL OTHER FEATURES TO BE 0.200 INCH MINIMUM.
 - B. THERE SHALL BE NO THIEVING IN ANY AREAS FREE OF SOLDER MASK OR INTERNAL COPPER PLANES.
- MFGR. TO LEGIBLY ETCH OR STAMP/SCREEN WITH PERMANENT NON-CONDUCTIVE INK ON SECONDARY SIDE IN A CLEAR AREA UNLESS OTHERWISE INDICATED;
 - A. U.L. CODE-FLAMMABILITY RATING
 - B. DATE CODE (STAMP).
 - C. LOT NUMBER
 - D. MFGR LOGO
 - E. SUCCESSFUL ELECTRICAL TEST.
- REPAIRS PER IPC-7711/21 (LATEST REV.) ARE ALLOWED.

4 LAYER STACKUP



CHARACTERISTIC IMPEDANCE = N/A

ARTWORK LINE WIDTH FOR IMPEDANCE CONTROLLED LINES = N/A

CHARACTERIZATION BOARD

PRIMARY SIDE

		WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887	
		SIZE C	FSCM NO 24355
SCALE 1/1		SHEET 2 OF 2	

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C

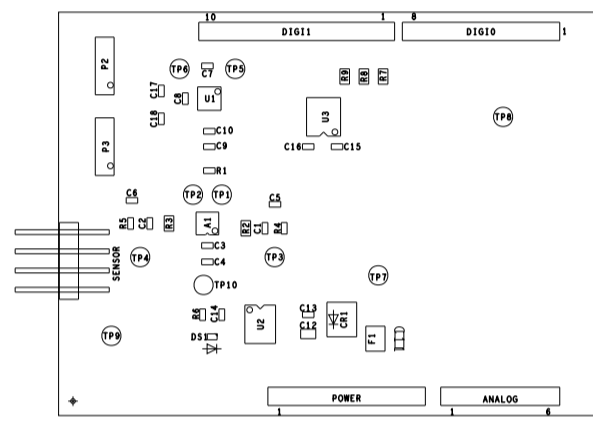
B

B

A


A

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	14FEB14	J.CHUA
B		02APR14	J.CHUA



Assembly Notes:
 Don't cut the excess pins of the following connectors:
 DIG11 DIG10 POWER ANALOG ICSP

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX -.010 --1/32 --2 .XXX -.005 .XXXX -.0050	APPROVAL	DATE	 WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887			
	TEMPLATE ENGINEER gio	APR14				
	HARDWARE SERVICES R. MACDONALD	APR14				
	HARDWARE SYSTEMS J. KEANNE	APR14				
MATERIAL	TEST ENGINEER N/A	N/A	TITLE ASSEMBLY AD7791 EVAL BOARD			
	COMPONENT ENGINEER G. CELEDONIO	APR14				
	TEST PROCESS N/A	N/A				
	HARDWARE RELEASE R. AMARILLE	APR14				
FINISH	DESIGNER jackie	APR14	SIZE	FSCM NO	DRAWING NUMBER	REV
	PTD ENGINEER J.CHUA	APR14	C	24355	01-038117	B
	CHECKER J.CHUA	APR14	SCALE 1/1		SHEET 1 OF 1	

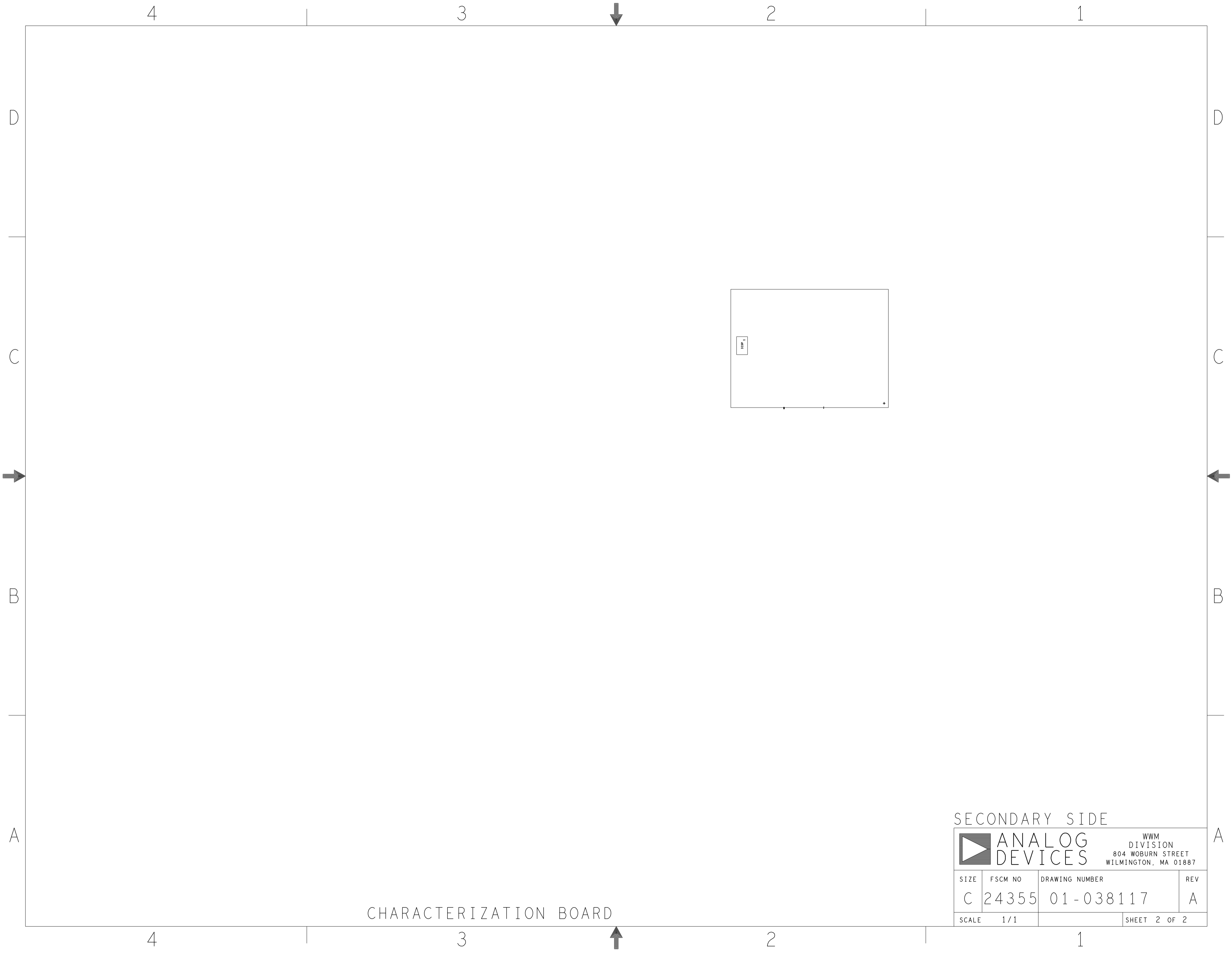
CHARACTERIZATION BOARD

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2

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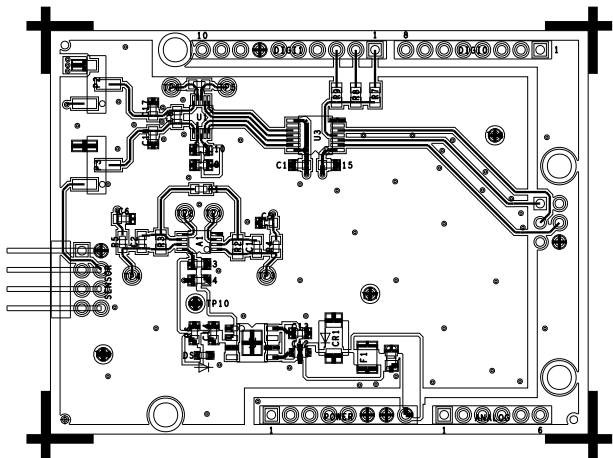


CHARACTERIZATION BOARD

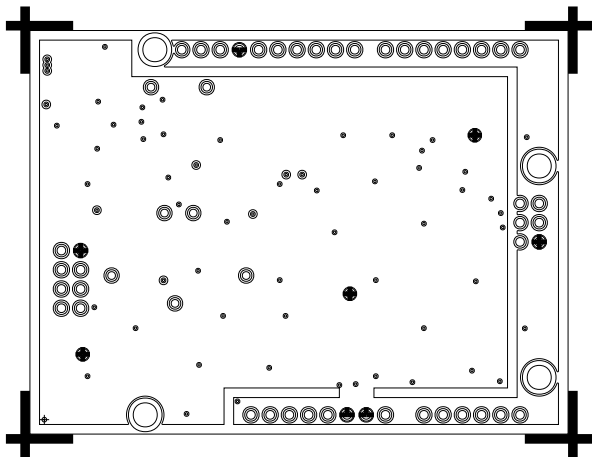
SECONDARY SIDE

		ANALOG DEVICES		WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887	
SIZE	FSCM NO	DRAWING NUMBER		REV	
C	24355	01-038117		A	
SCALE	1/1			SHEET 2 OF 2	

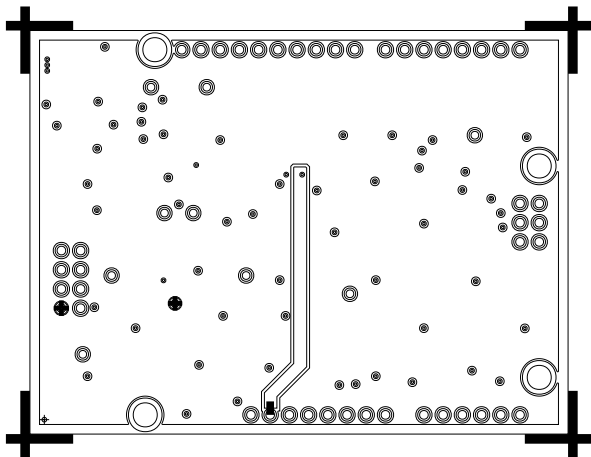
L1 PRIMARY
08-038117-01
REV B



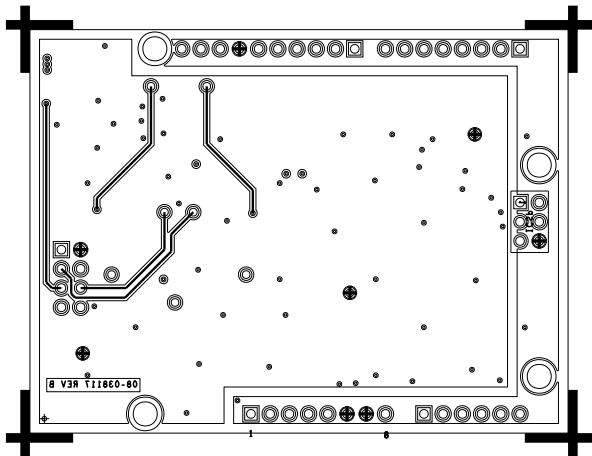
L2 GND
08-038117-07
REV B



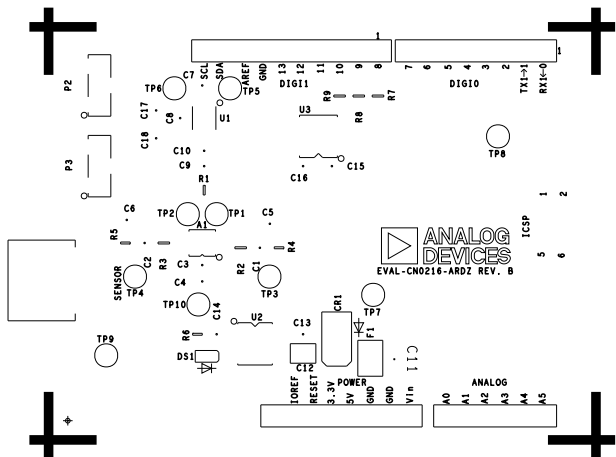
L3 PWR
08-038117-08
REV B



L4 SECONDARY
08-038117-02
REV B



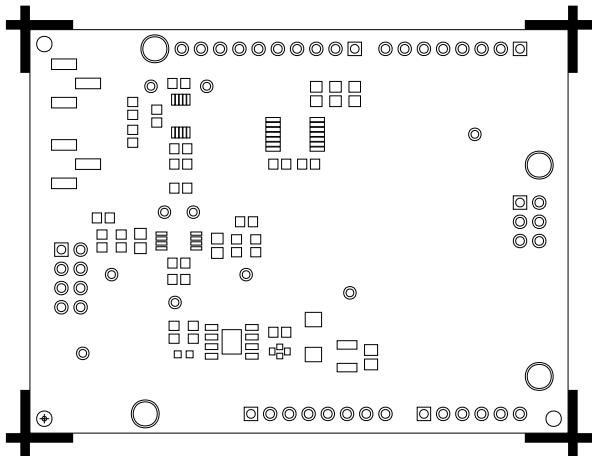
SILKSCREEN PRIMARY
08-038117-03
REV B



SOLDERMASK PRIMARY

08-038117-04

REV B



SILKSCREEN SECONDARY

08-038117-05
REV B



TP8
TP2
DIGI0
DIGI1
8 1
10

TP8

TP1
TP5

SENSOR

TP4

TP10

TP3



TP9

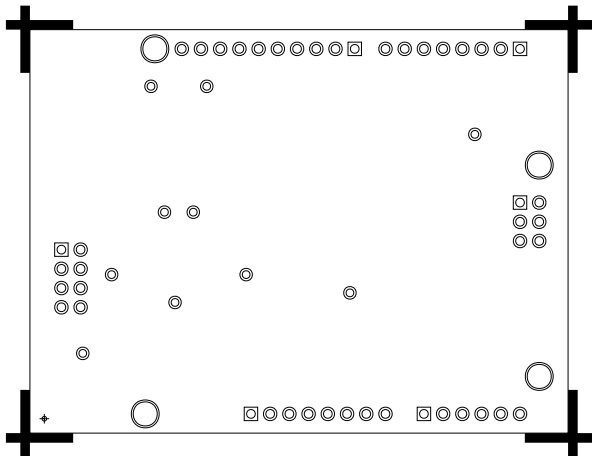


POWER
ANALOG
1
1

SOLDERMASK SECONDARY

08-038117-06

REV B



PASTEMASK PRIMARY

08-038117-07

REV B

