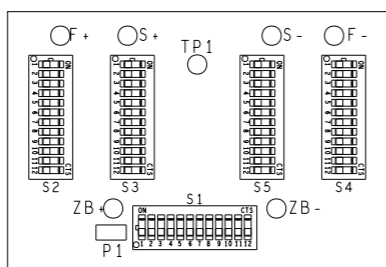
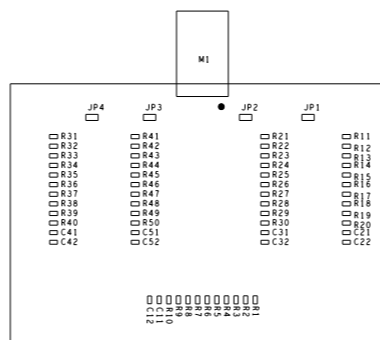


8 | 7 | 6 | 5 | 4 | 3 | 2 | 1

REVISION			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE		



PRIMARY SIDE




SECONDARY SIDE

D
C
B
A

D
C
B
A

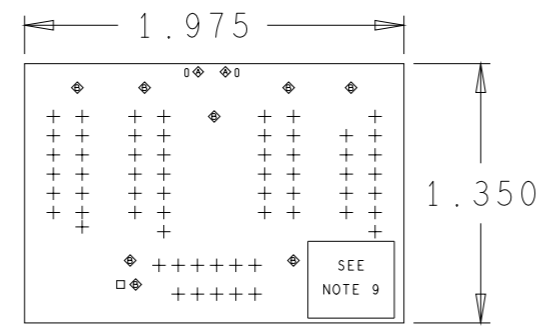
8 | 7 | 6 | 5 | 4 | 3 | 2 | 1

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MM TOLERANCES DECIMALS FRACTIONS ANGLES .XX ±0.25 ±.1/32 ±.1/2	ASSEMBLY		 ANALOG DEVICES INTERNATIONAL ERDC Building Raheen Business Park Raheen, LIMERICK, IRELAND	
	APPROVAL	DATE	TITLE	
	DESIGNED BY SHEAHAN	18APR18	AD5940 Impedance Test	
MATERIAL	CHECKED	APPROVED	DATE	
FINISH	WFO ENGINEER	SIZE	FSCM NO	DRAWING NUMBER
DO NOT SCALE DWG		A3		01-049393
		SCALE	1/1	SHEET 1 OF 1
				REV A

REVISION			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE		


HOLE TOLERANCES
 UNLESS SPECIFIED
 PLATED: +/- 0.003"
 NON PLATED: +/- 0.002"

FINISHED HOLES IN MILS				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	PLATED	QTY	TOLERANCE/NOTES
+	20.0	PLATED	61	
◇	35.0	PLATED	2	
□	40.0	PLATED	1	
◇	40.0	PLATED	8	
°	43.0x18.0	PLATED	2	

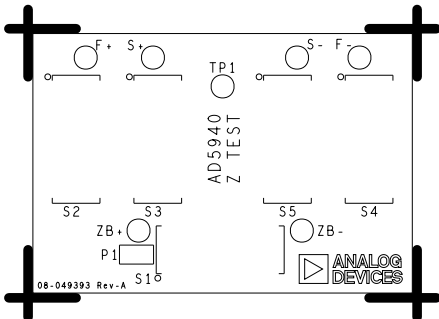


FABRICATION NOTES:

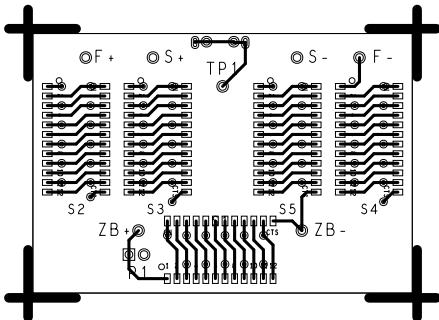
- Material: 2 Layer FR4 glass epoxy laminate 1.6mm +/- 10%. material to be RoHS compliant. Board to be fabricated per IPC-6012A, CLASS 2.
- Plated thru holes and conductive pattern electroplated with 0.255mm min. thick copper. Terminal areas and plated thru holes to be ENIG plated.
- Processing tolerance:
 - Conductive pattern front to back registration within 0.125mm total.
 - Minimum annular ring surrounding holes 0.05mm.
 - Finished conductive pattern to be within 0.05mm of true size.
 - Minimum feature size 0.2032mm
 - Minimum air gap 0.2032mm
- Warp and twist within 0.1875mm per 25mm (0.75%)
- Dimensions are for the finished part.
- SOLDERMASK: liquid Photo Imageable soldermask over bare copper (SMOBC), colour GREEN both sides using the pattern provided. No mask is permitted on the terminal areas. Soldermask to etch registration within 0.125mm total.
- SCREENING: Screen component outlines and nomenclature using indelible white ink on both sides. Nomenclature shall be legible. Screen to etch registration within 0.15mm total.
- Electrically test 100%.
- Manufacturer UL and Date codes on the bottom side silkscreen as indicated.

<small>UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MM</small> <small>TOLERANCES</small> <small>DECIMALS FRACTIONS ANGLES</small> <small>.XX +/-0.25 +/- 1/32 +/- .2</small> <small>.XXX +/-0.127</small>	FABRICATION		 <small>ANALOG DEVICES INTERNATIONAL</small> <small>ERDC Building</small> <small>Rohaan Business Park</small> <small>Rohaan, LIMERICK, IRELAND</small>	
	<small>APPROVAL</small> <small>DESIGNED</small> <small>CHECKED</small>	<small>DATE</small> <small>18APR18</small>	<small>TITLE</small> AD5940 Impedance Test	
<small>MATERIAL</small>	<small>APPROVED</small> <small>MFG ENGINEER</small>	<small>SIZE</small> A3	<small>FSCM NO</small>	<small>DRAWING NUMBER</small> 09-049393
<small>DO NOT SCALE DWG</small>	<small>SCALE</small> 1/1	<small>REV</small> A	<small>SHEET 1 OF 1</small>	

SILKSCREEN PRIMARY
08-049393-03
AD5940 IMPEDANCE TEST Rev-A



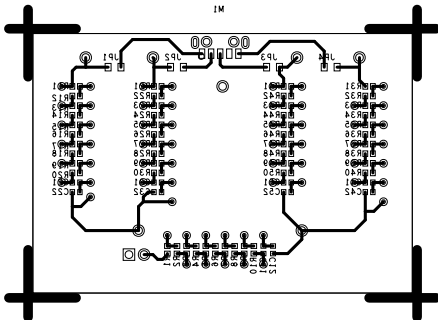
L1 PRIMARY
08-049393-01
AD5940 IMPEDANCE TEST Rev-A



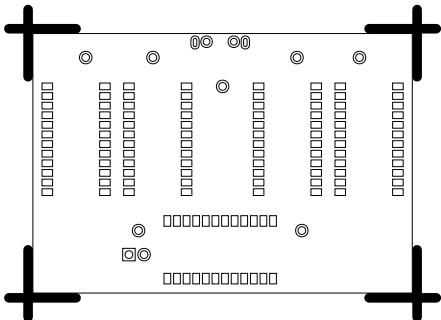
L2 SECONDARY

08-049393-02

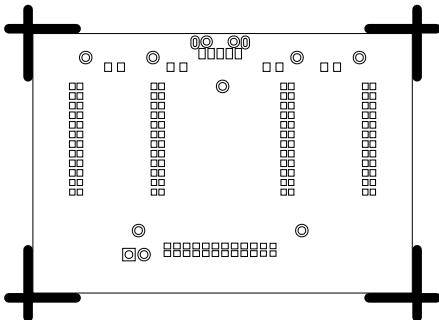
AD5940 IMPEDANCE TEST Rev-A



SOLDERMASK PRIMARY
08-049393-04
AD5940 IMPEDANCE TEST Rev-A



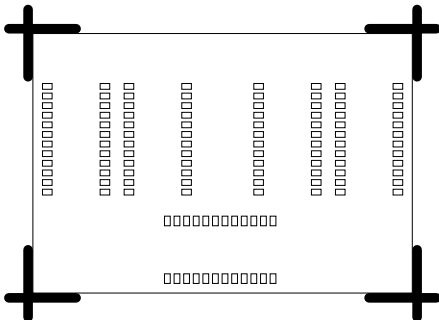
SOLDERMASK SECONDARY
08-049393-06
AD5940 IMPEDANCE TEST Rev-A



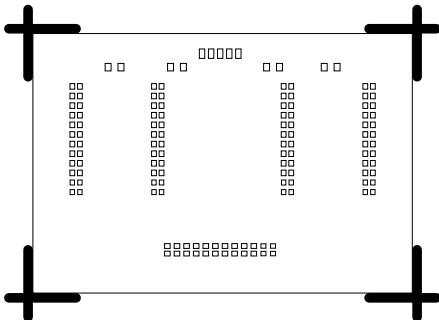
PASTEMASK PRIMARY

049393

AD5940 IMPEDANCE TEST Rev - A



PASTEMASK SECONDARY
08-049393-14
AD5940 IMPEDANCE TEST Rev-A



SILKSCREEN PRIMARY
 08-049393-03
 AD5940 IMPEDANCE TEST Rev-A

