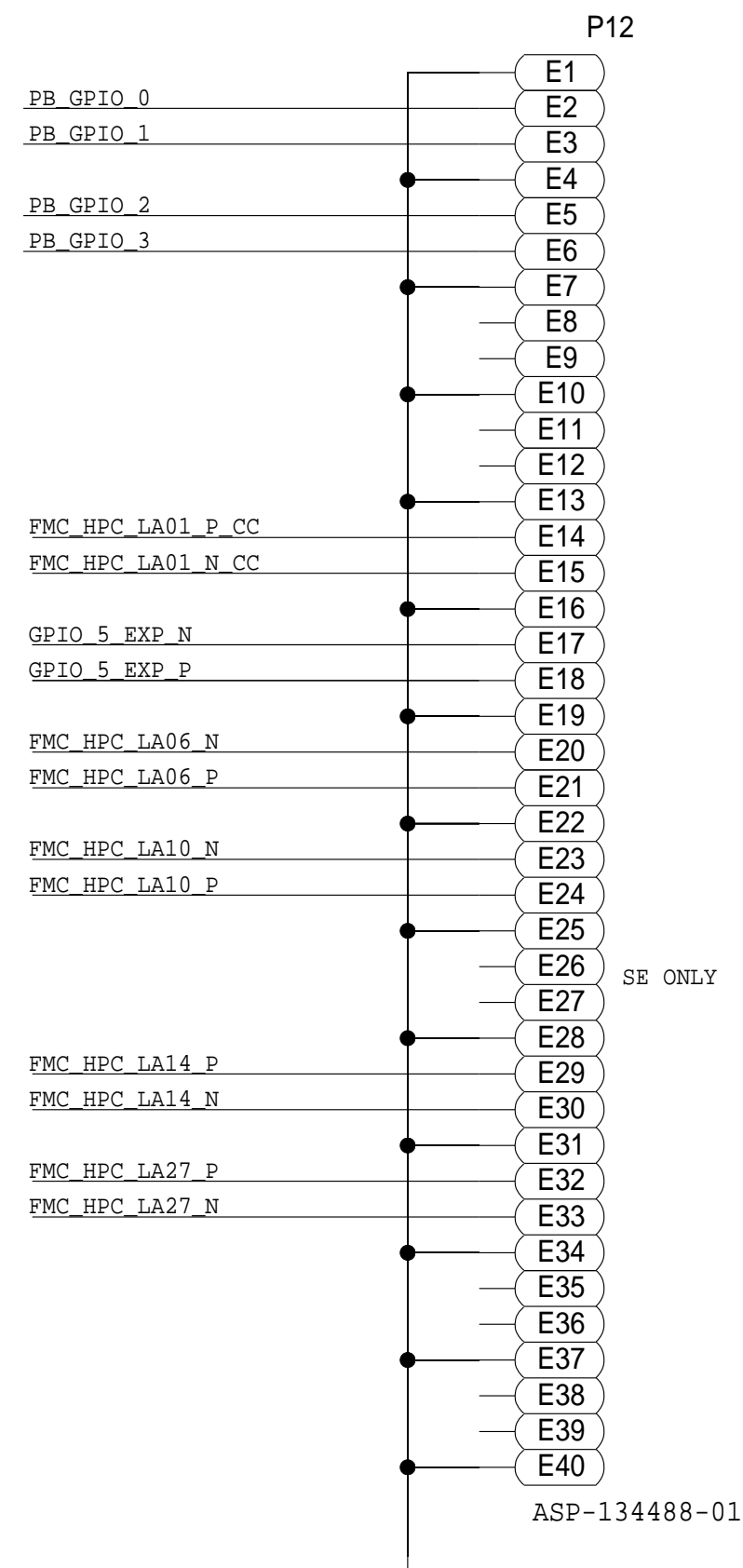
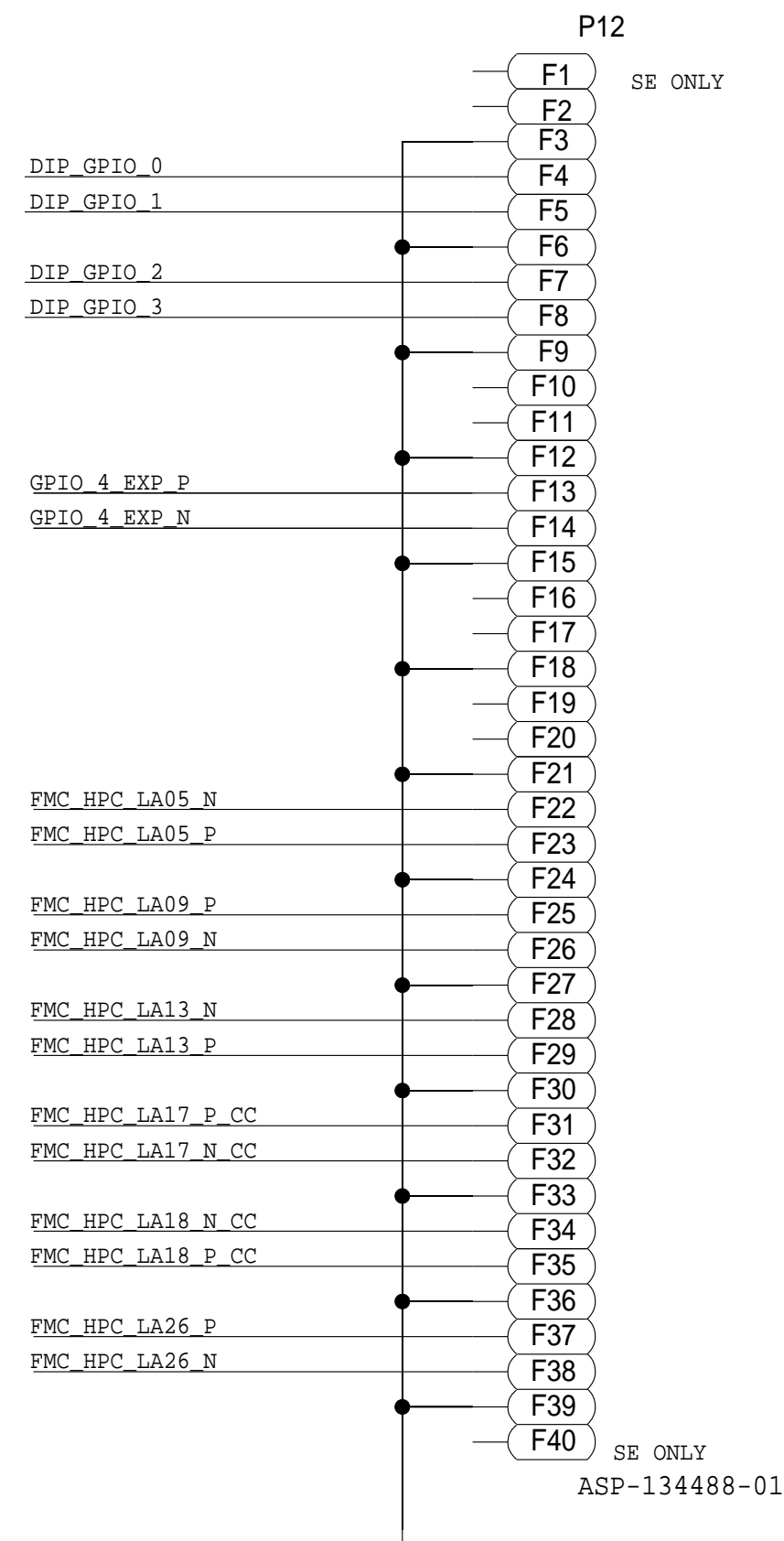
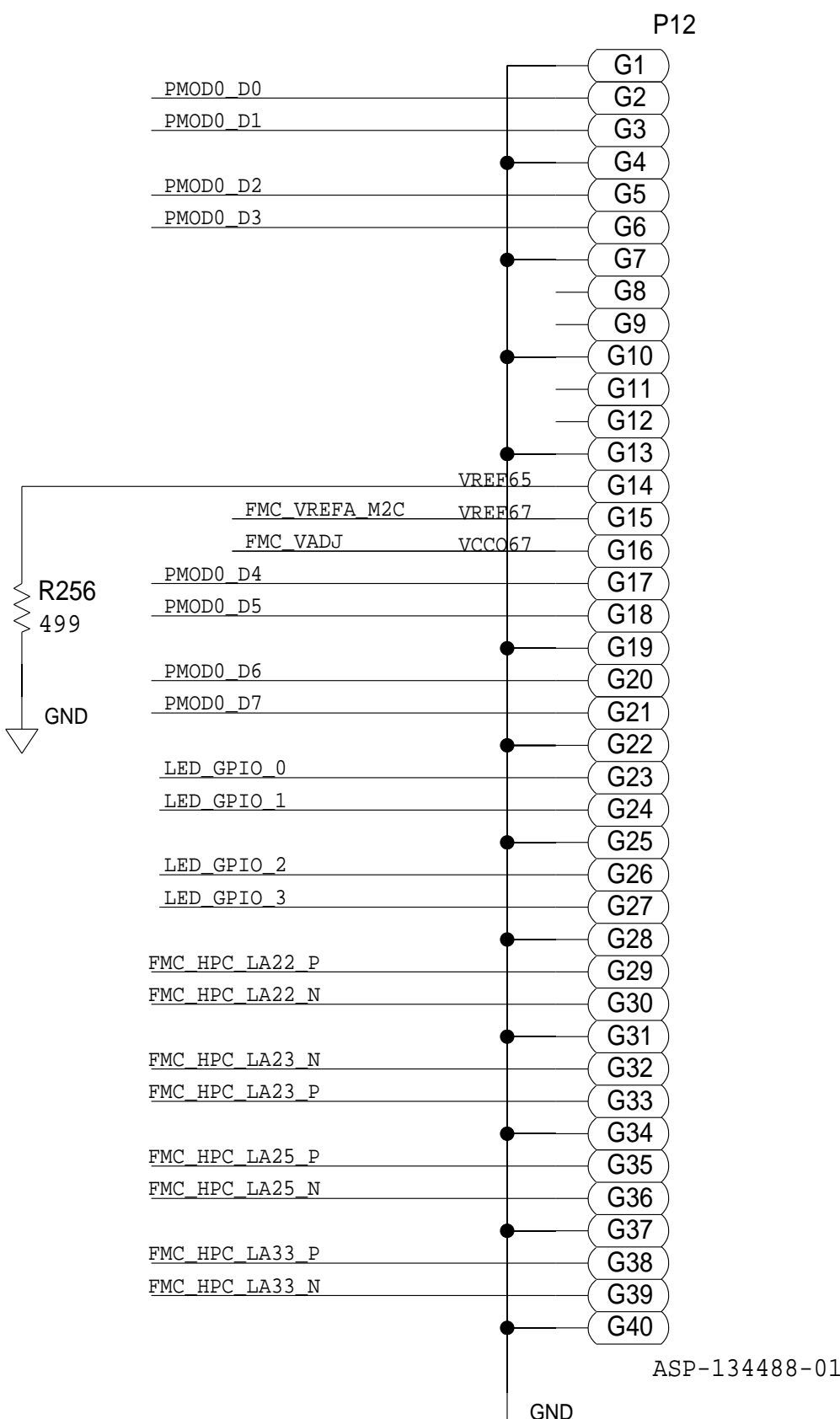
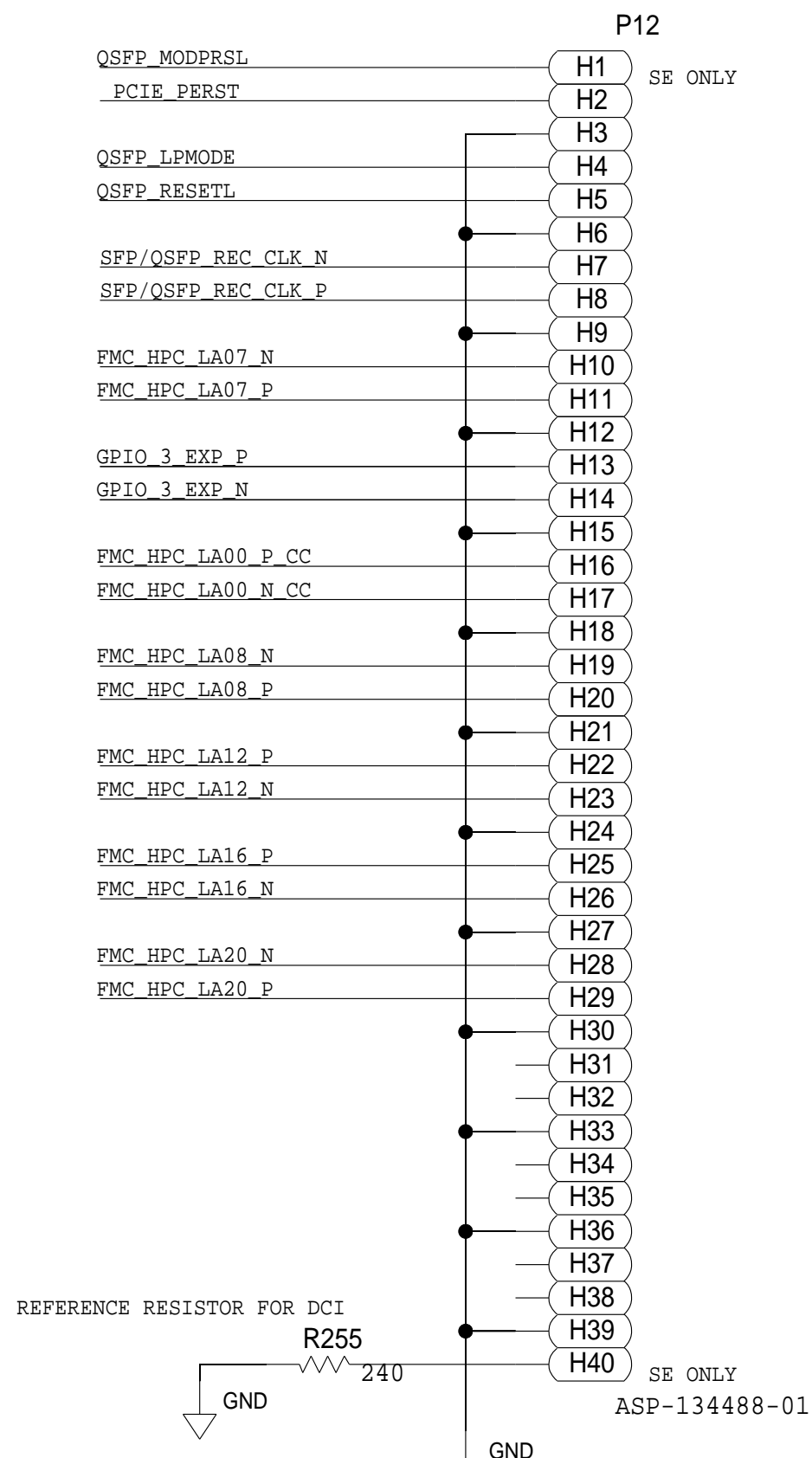
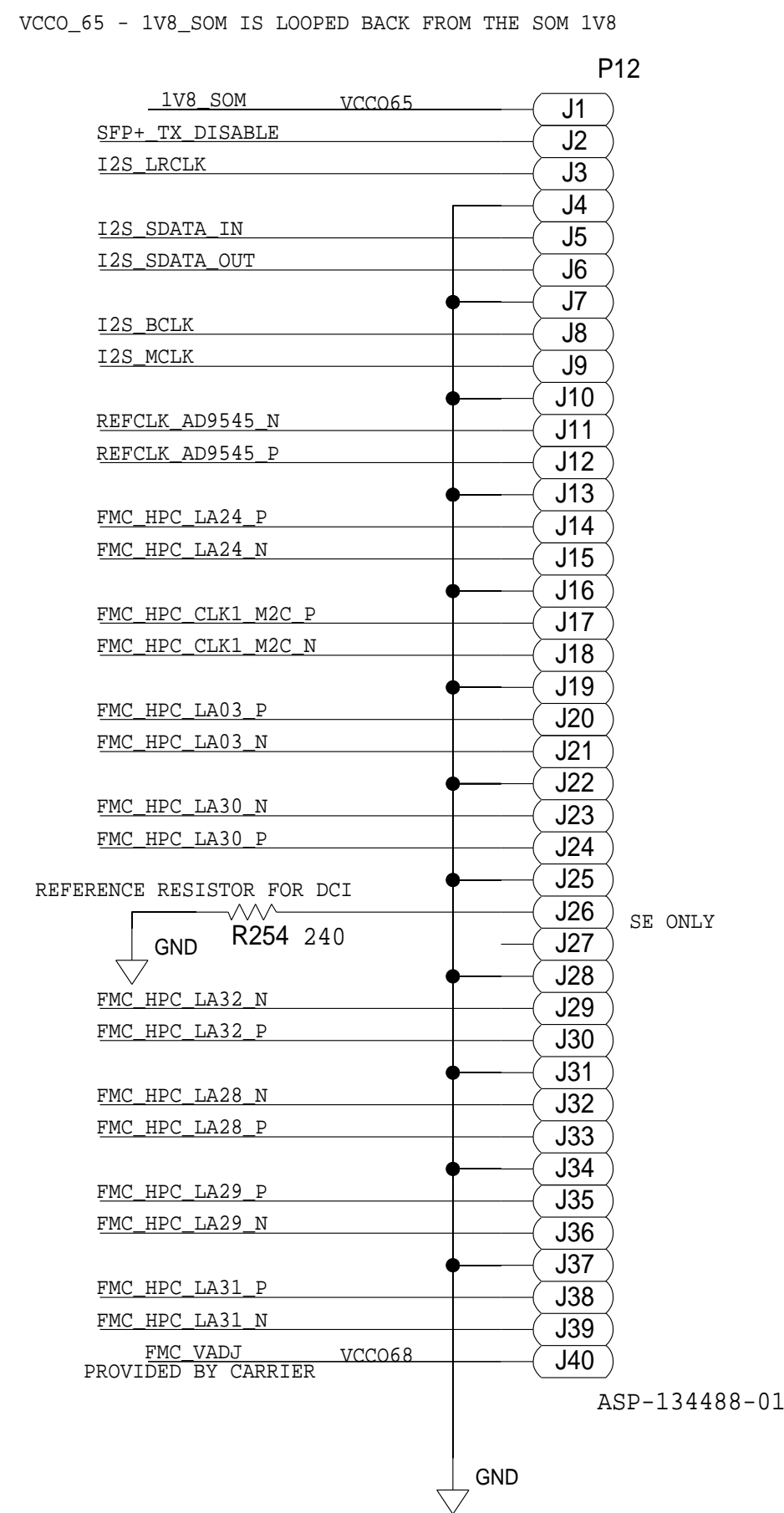
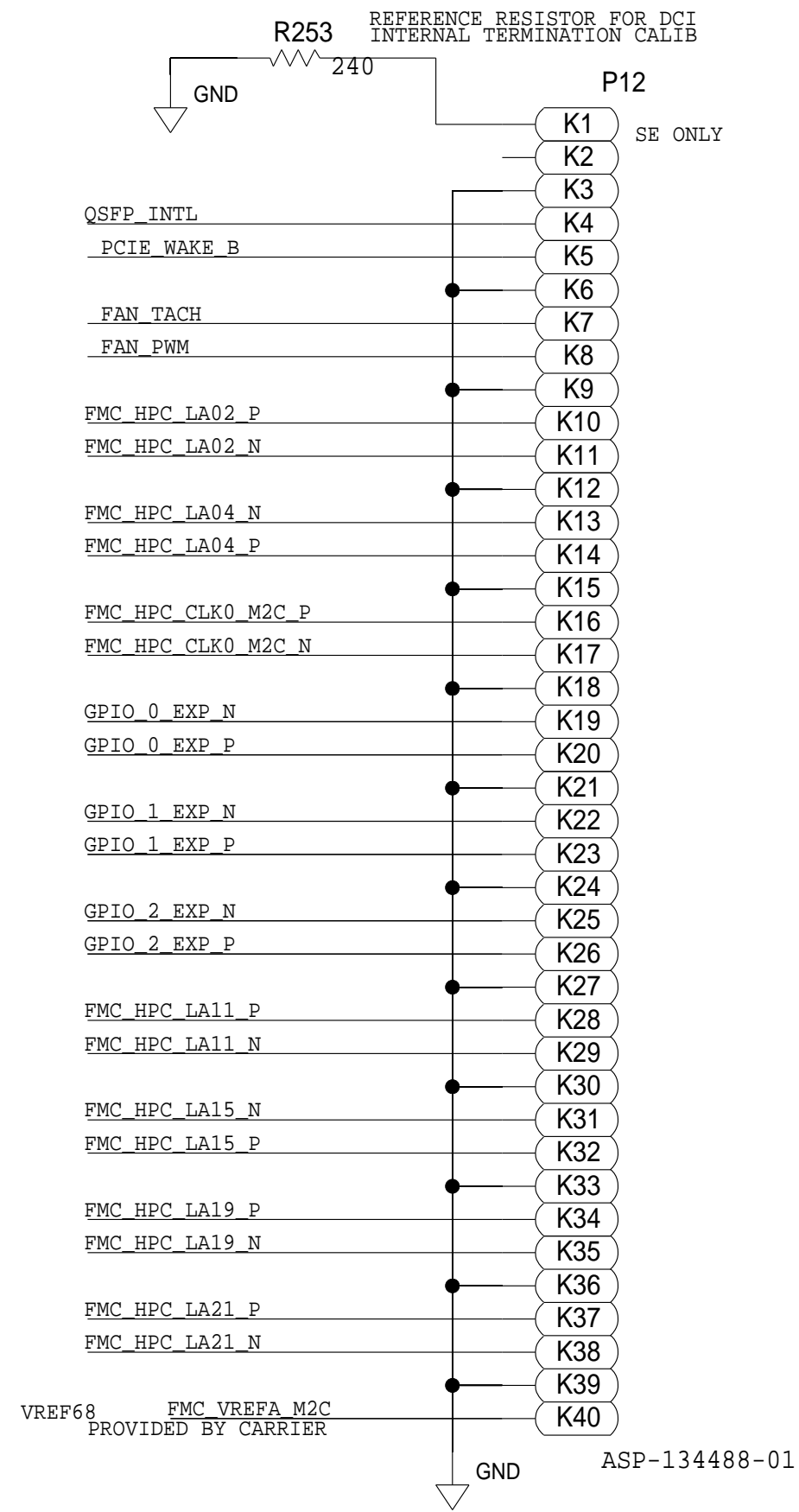
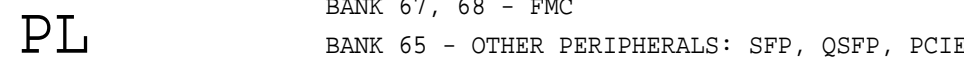


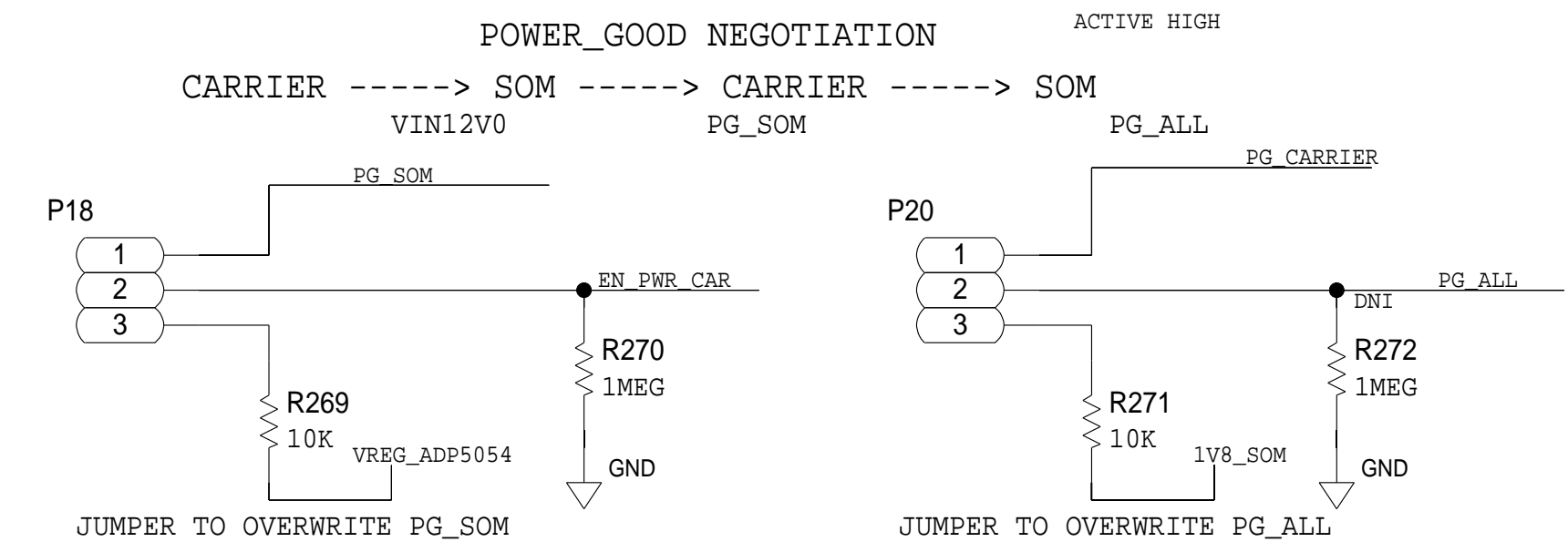
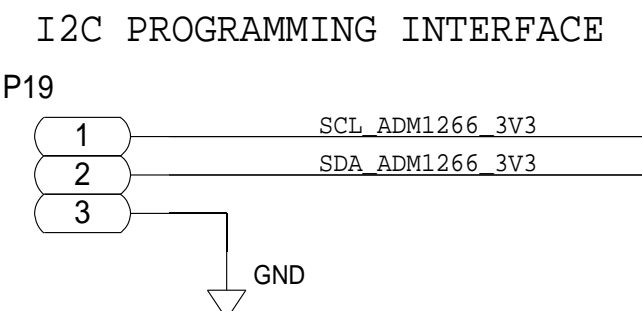
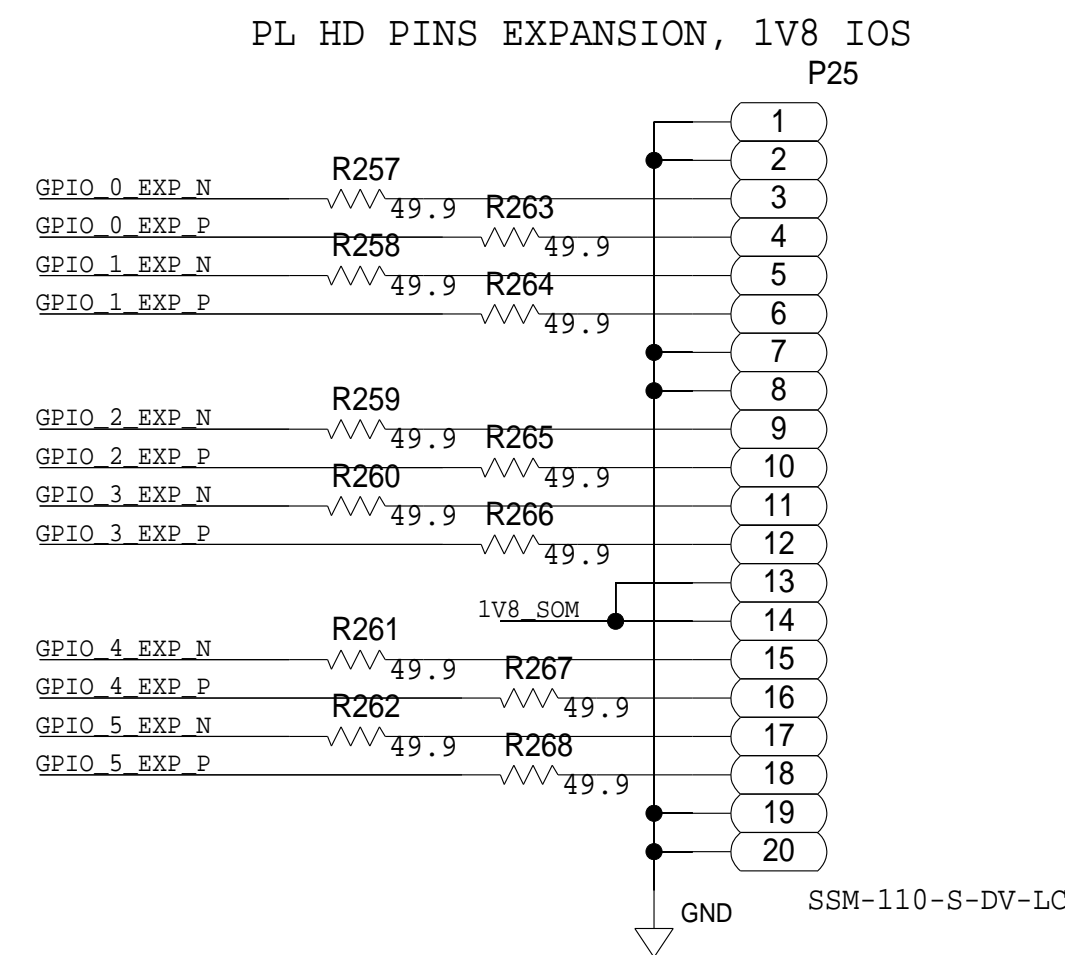
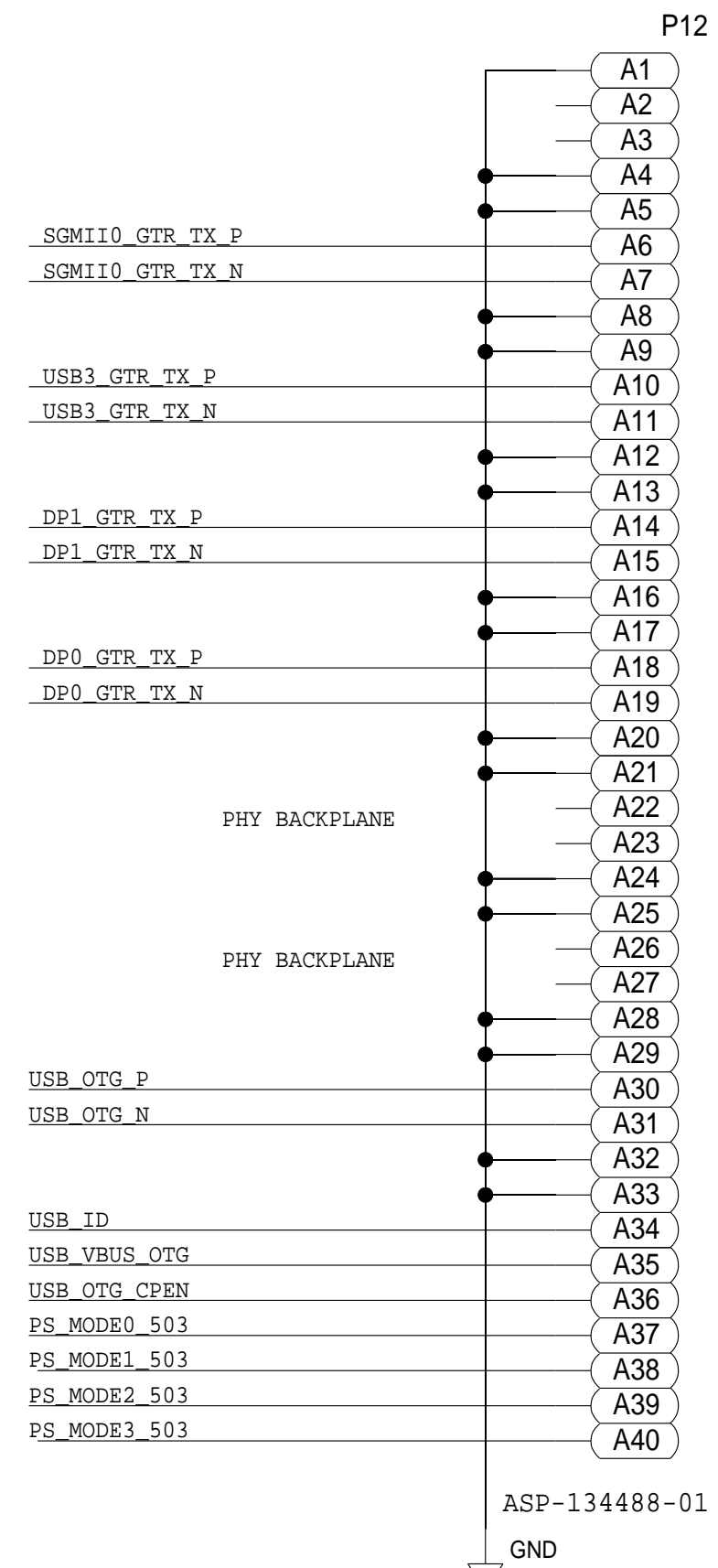
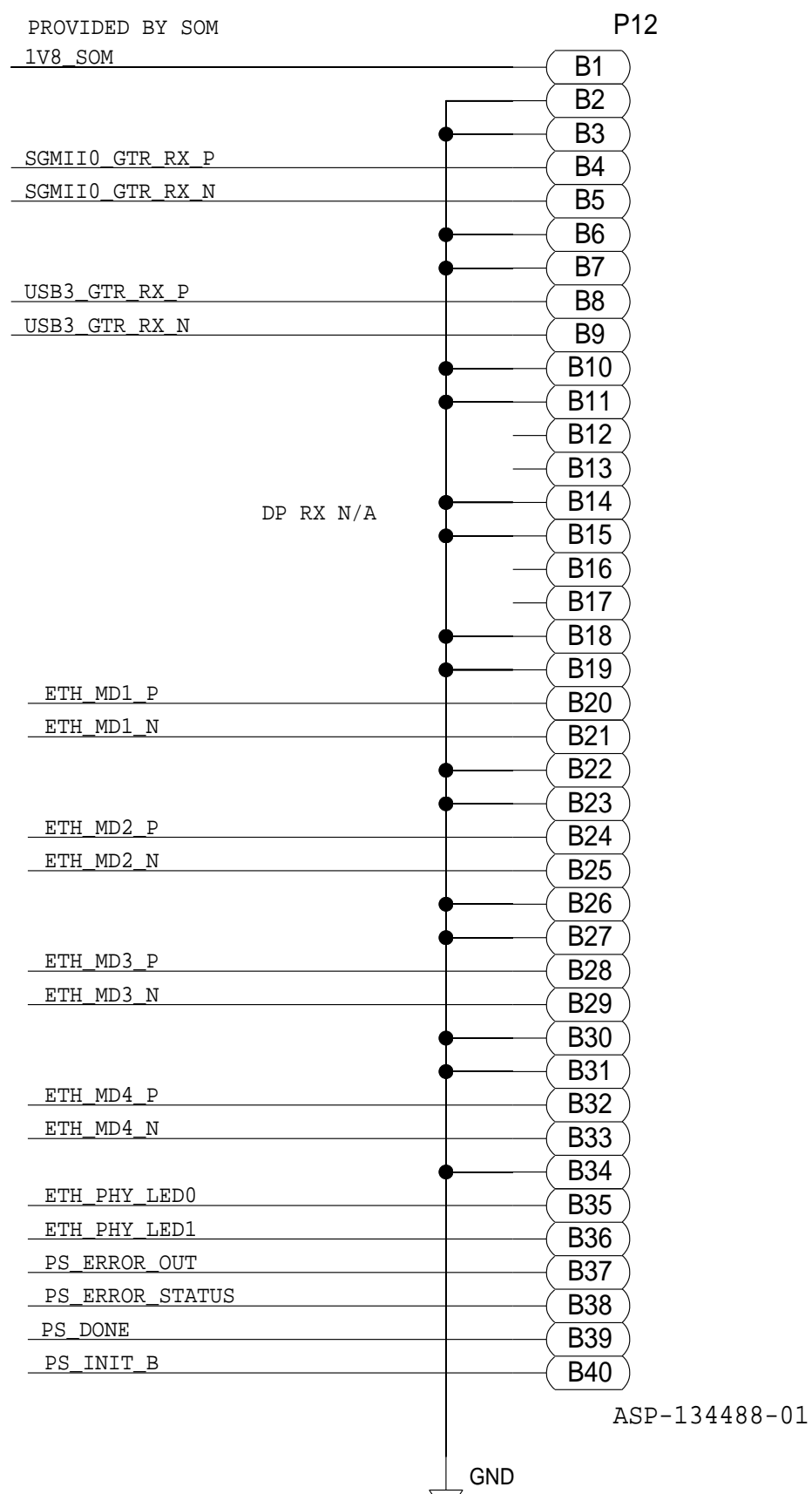
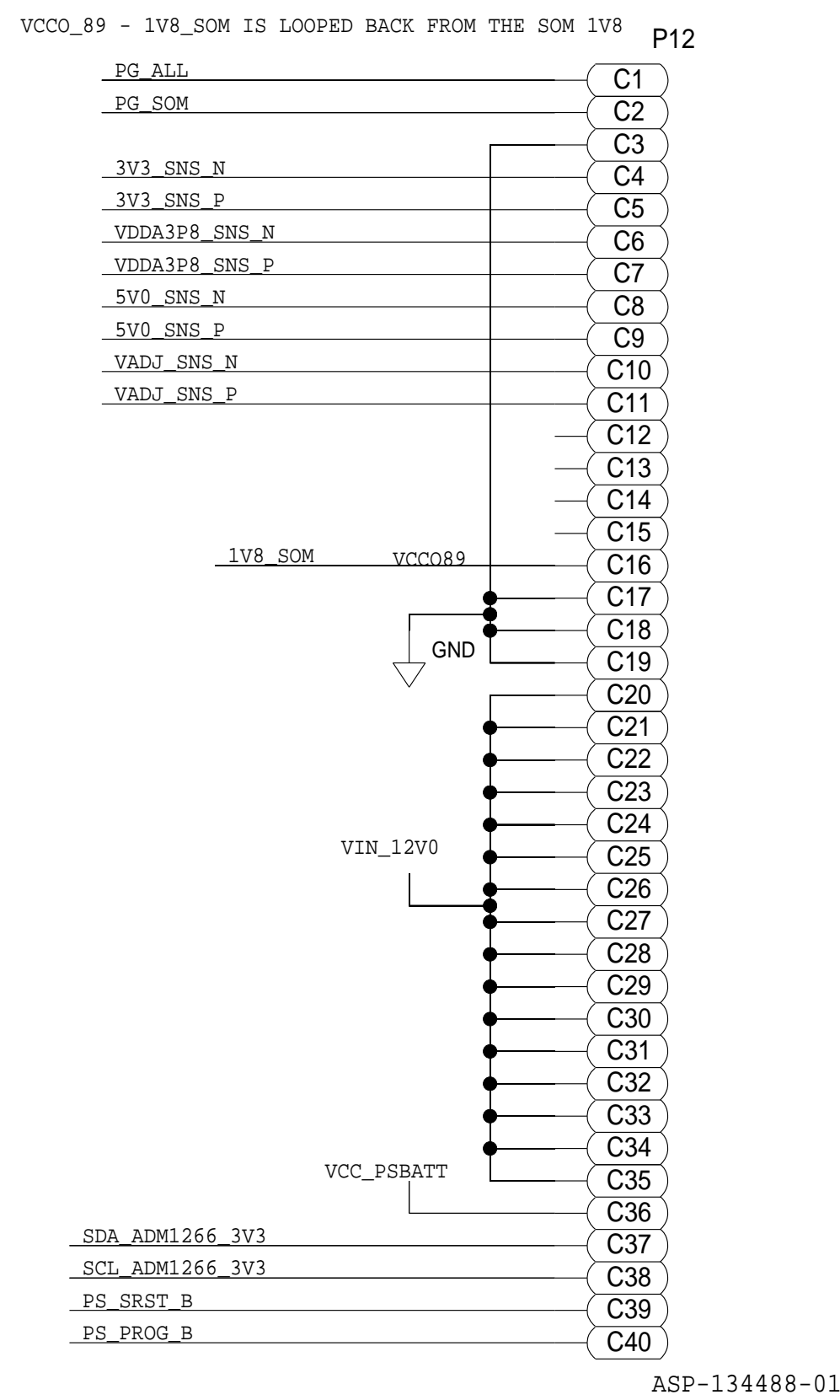
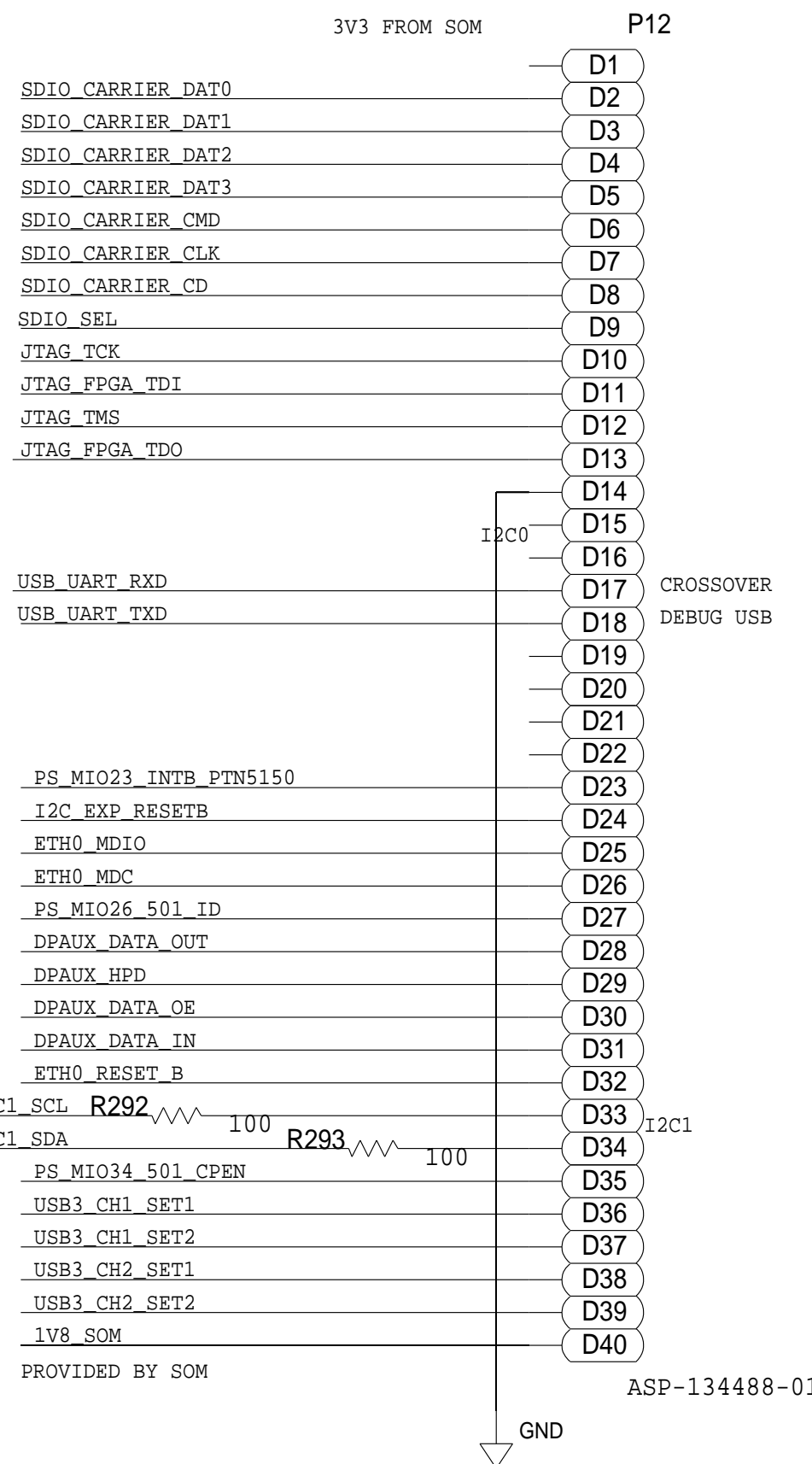
[illegible]

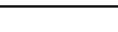
SOM CONNECTOR 1

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



PS

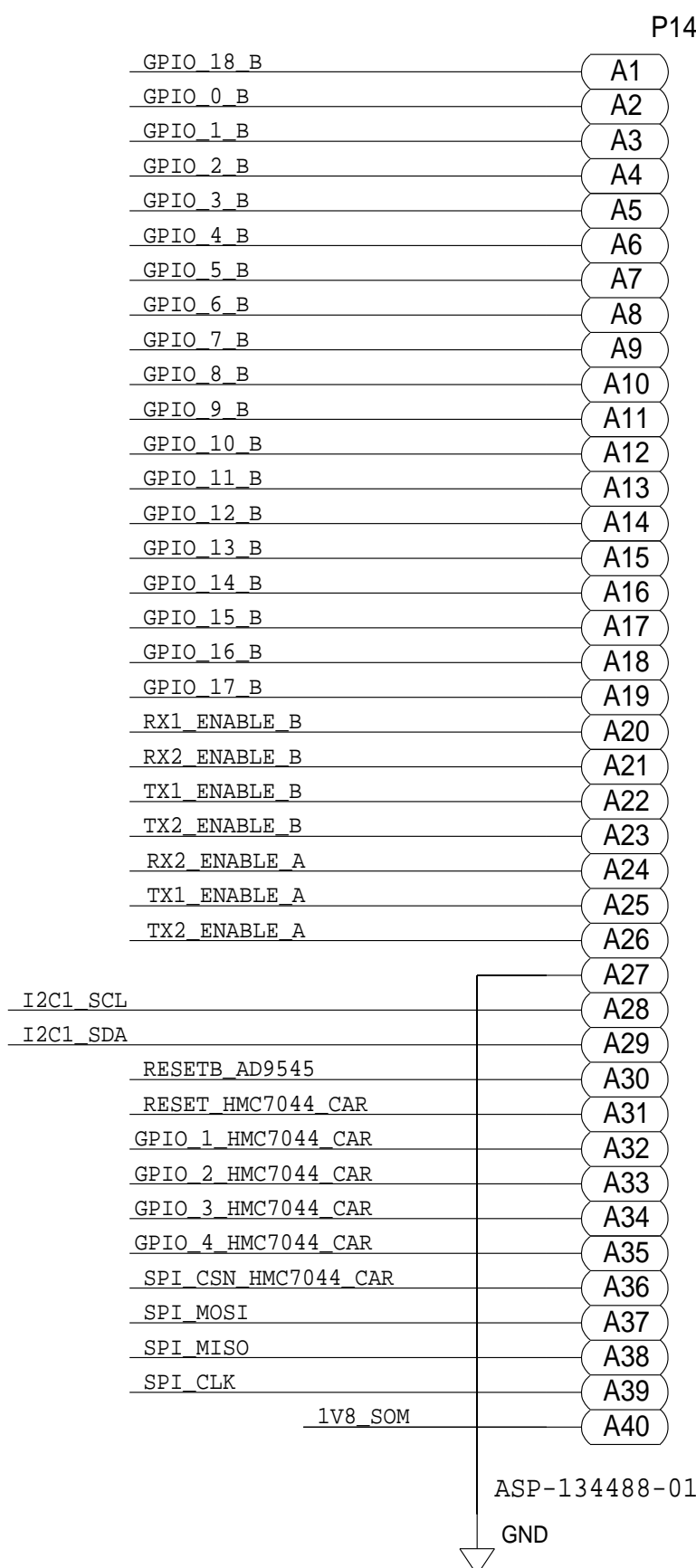
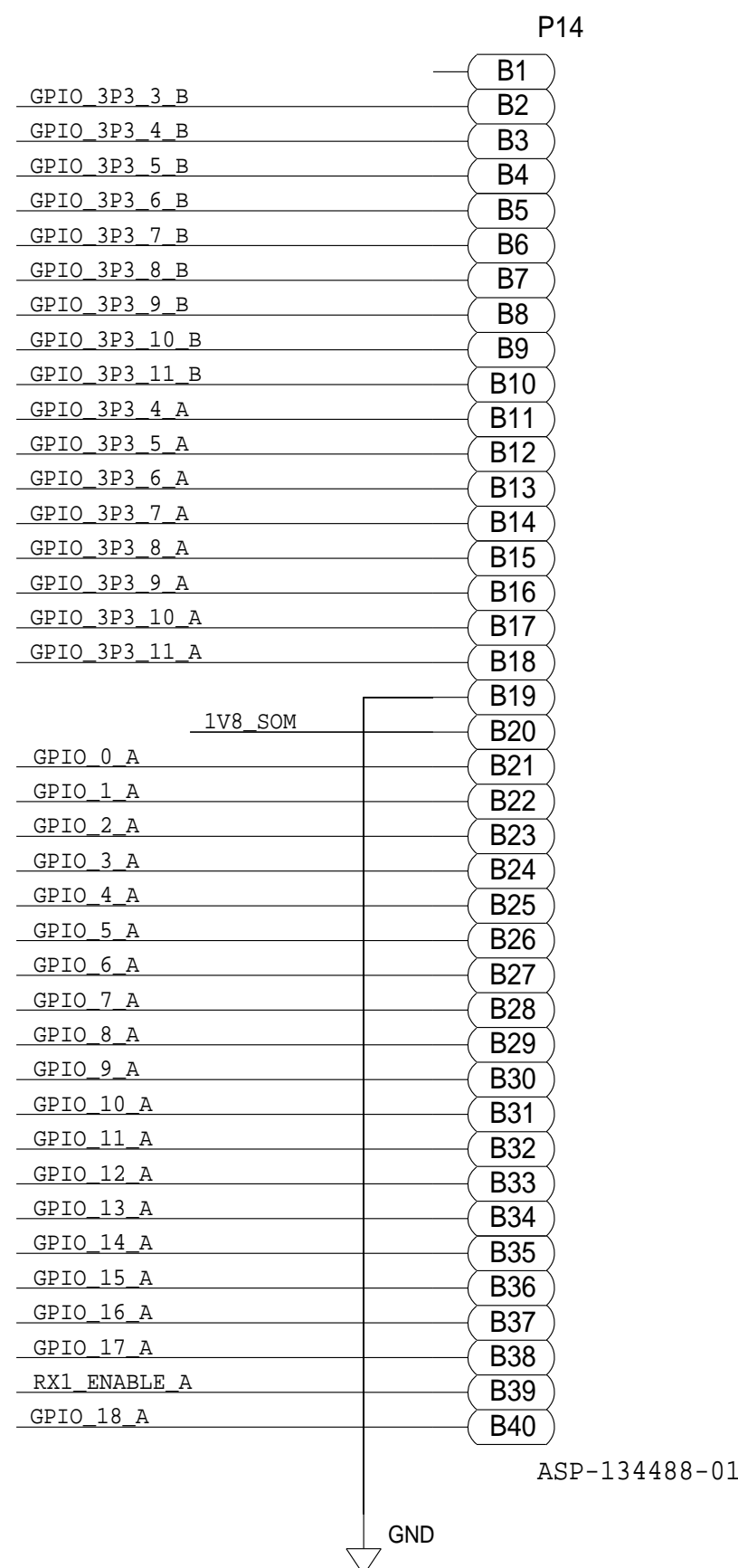
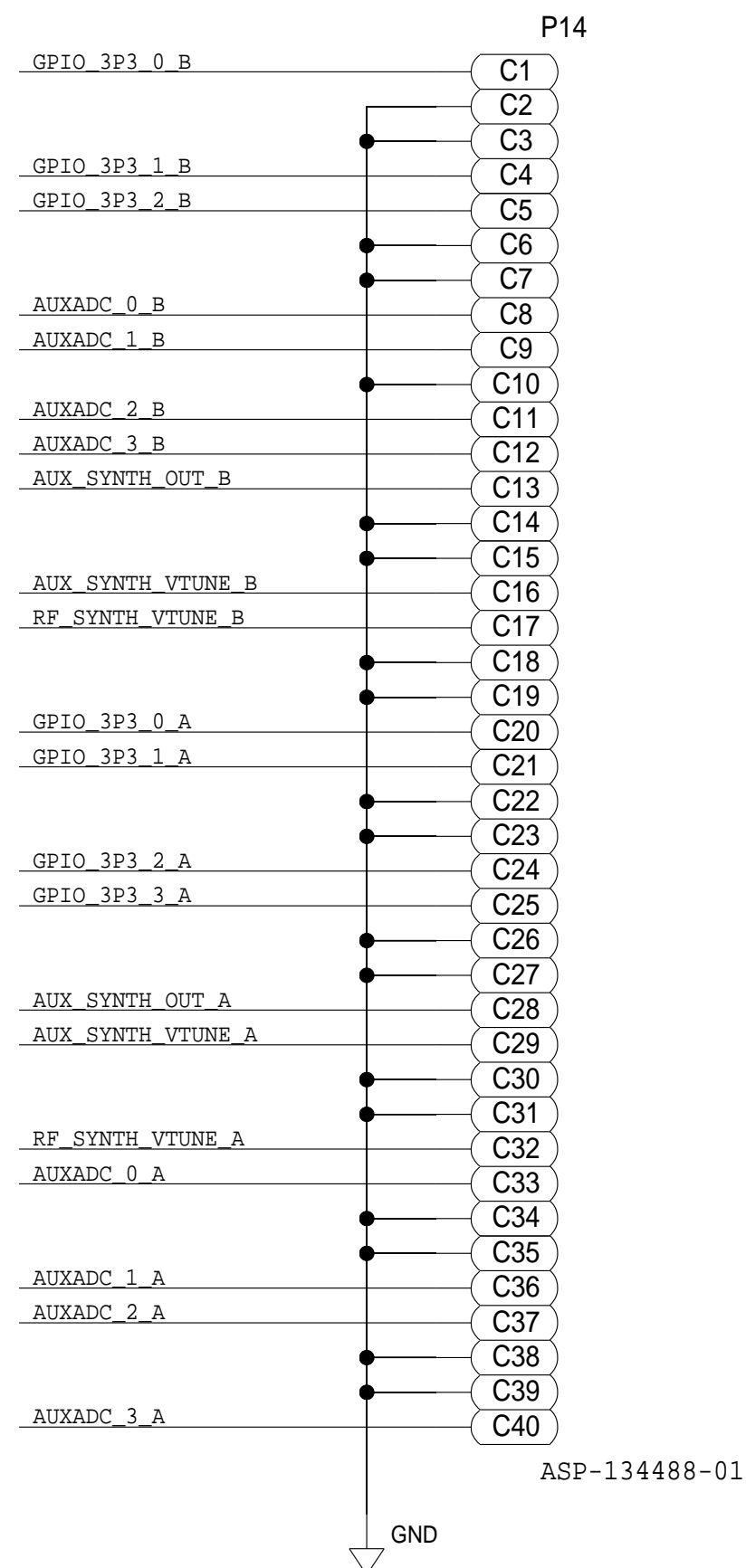
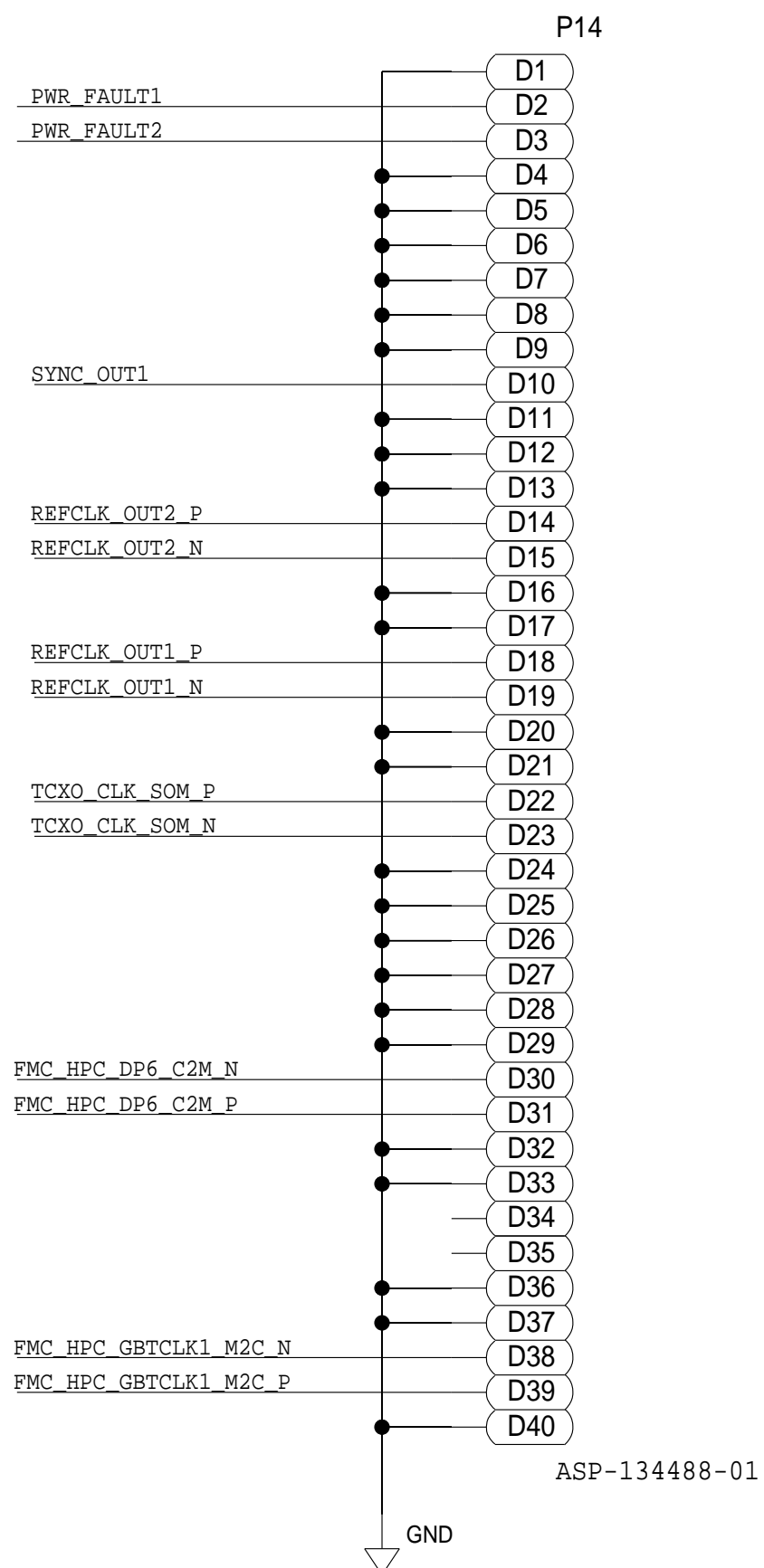
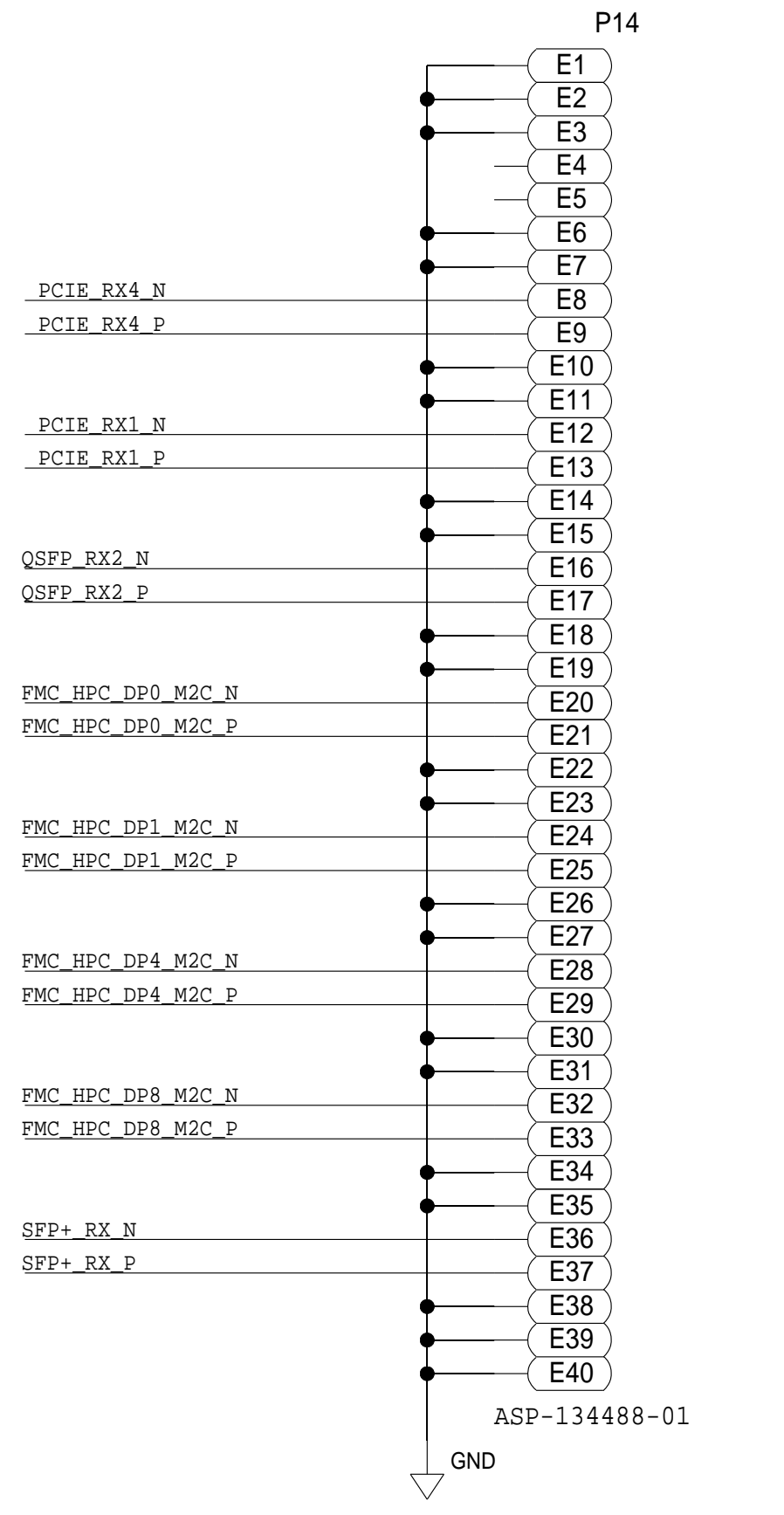
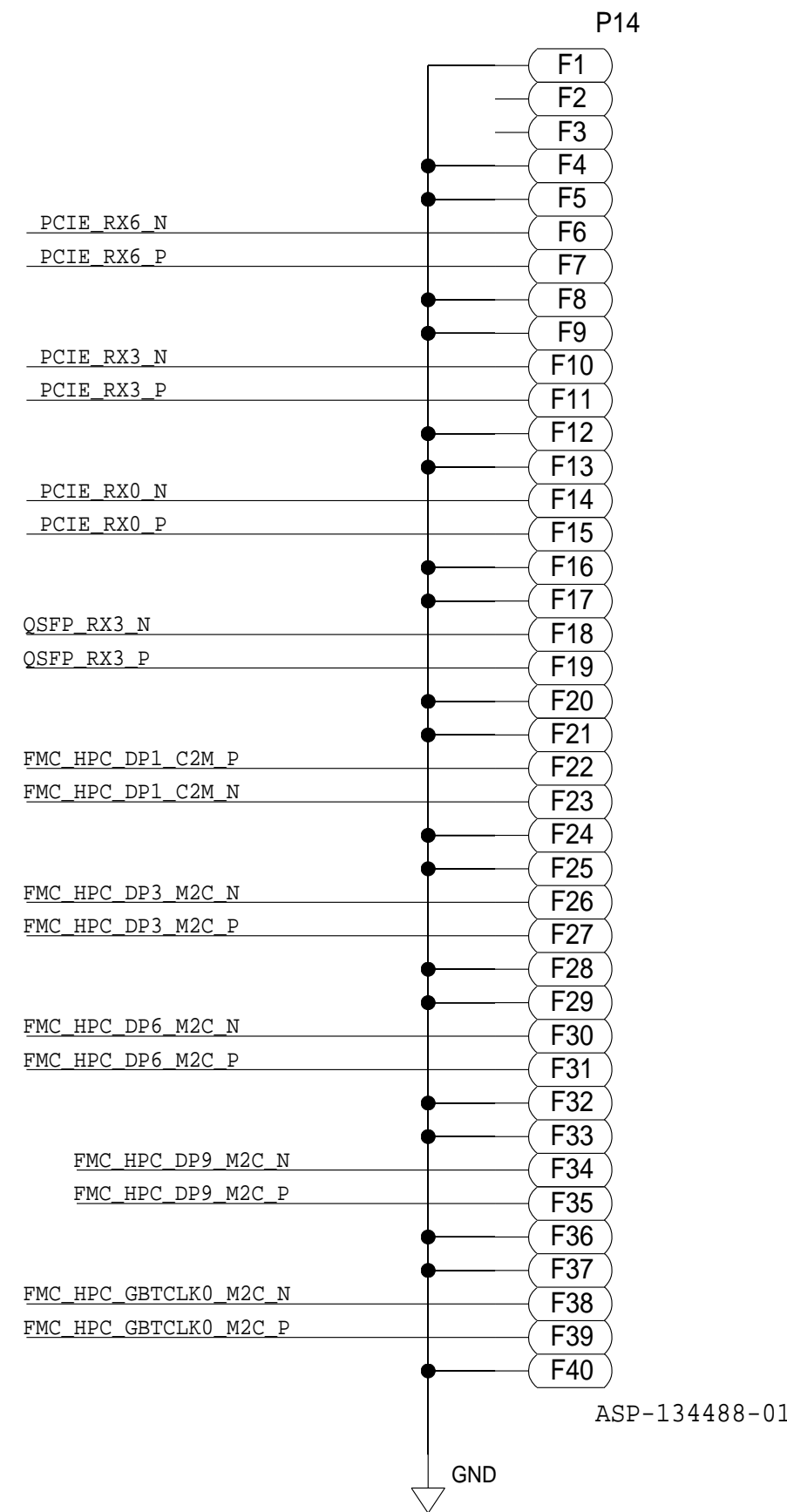
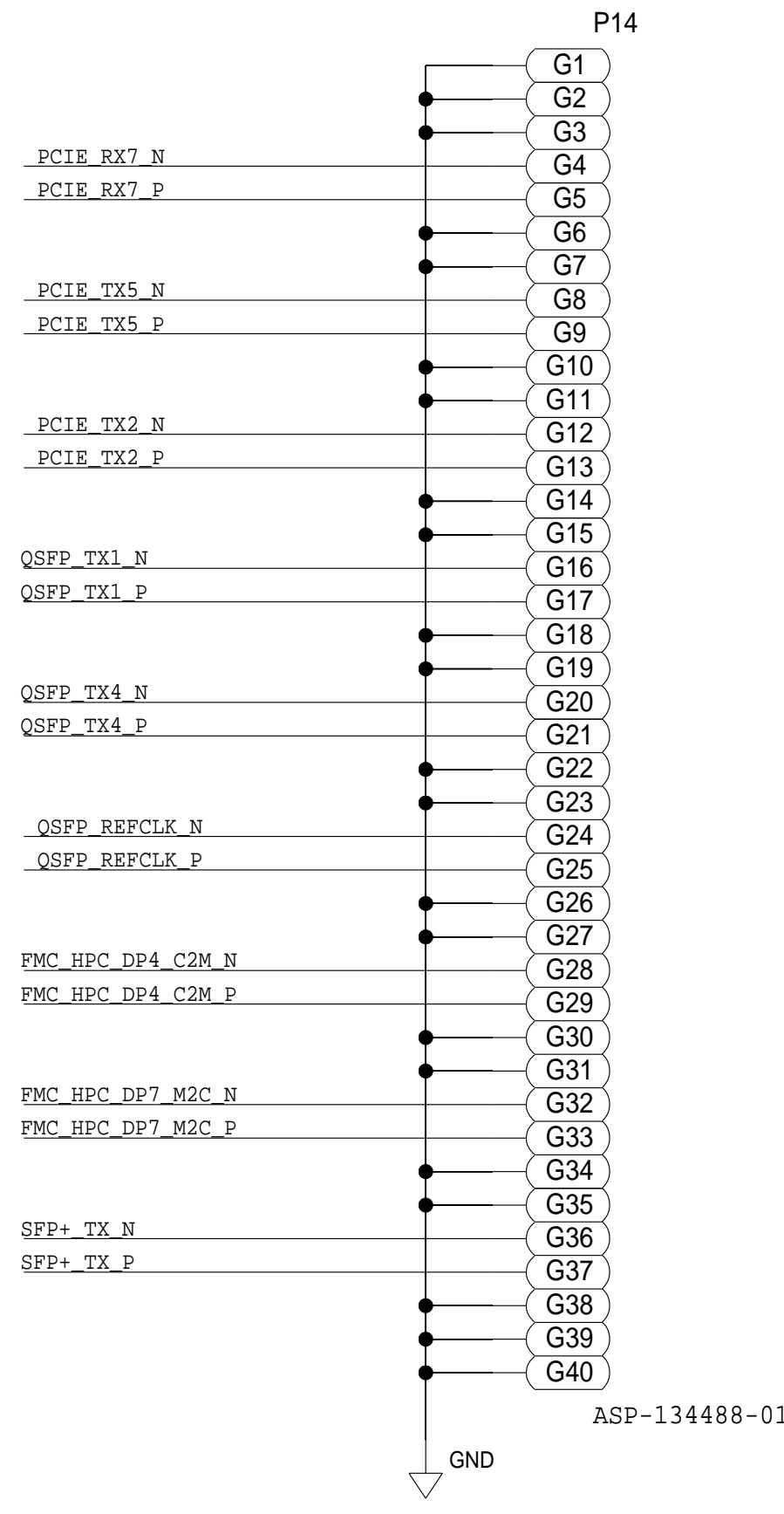
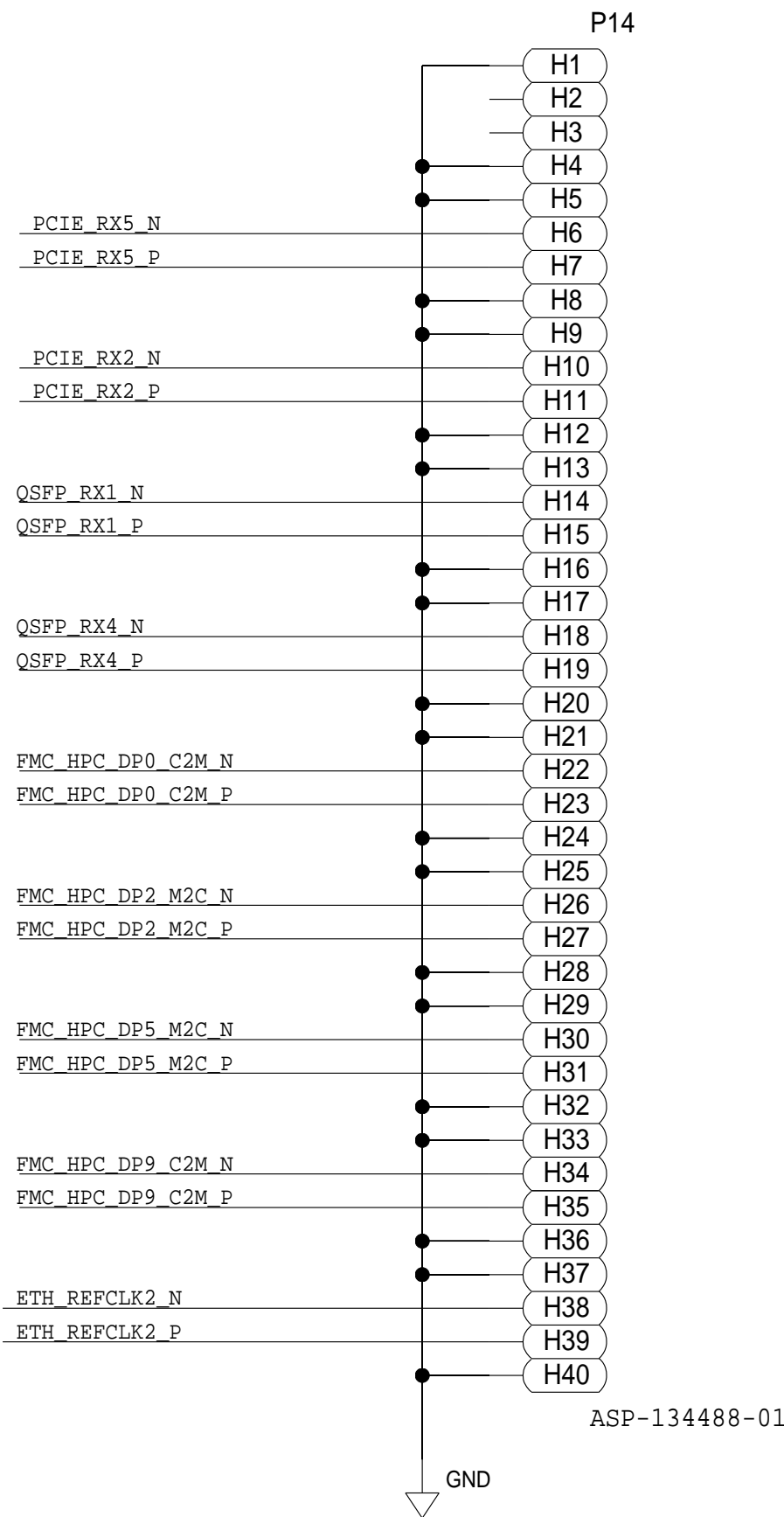
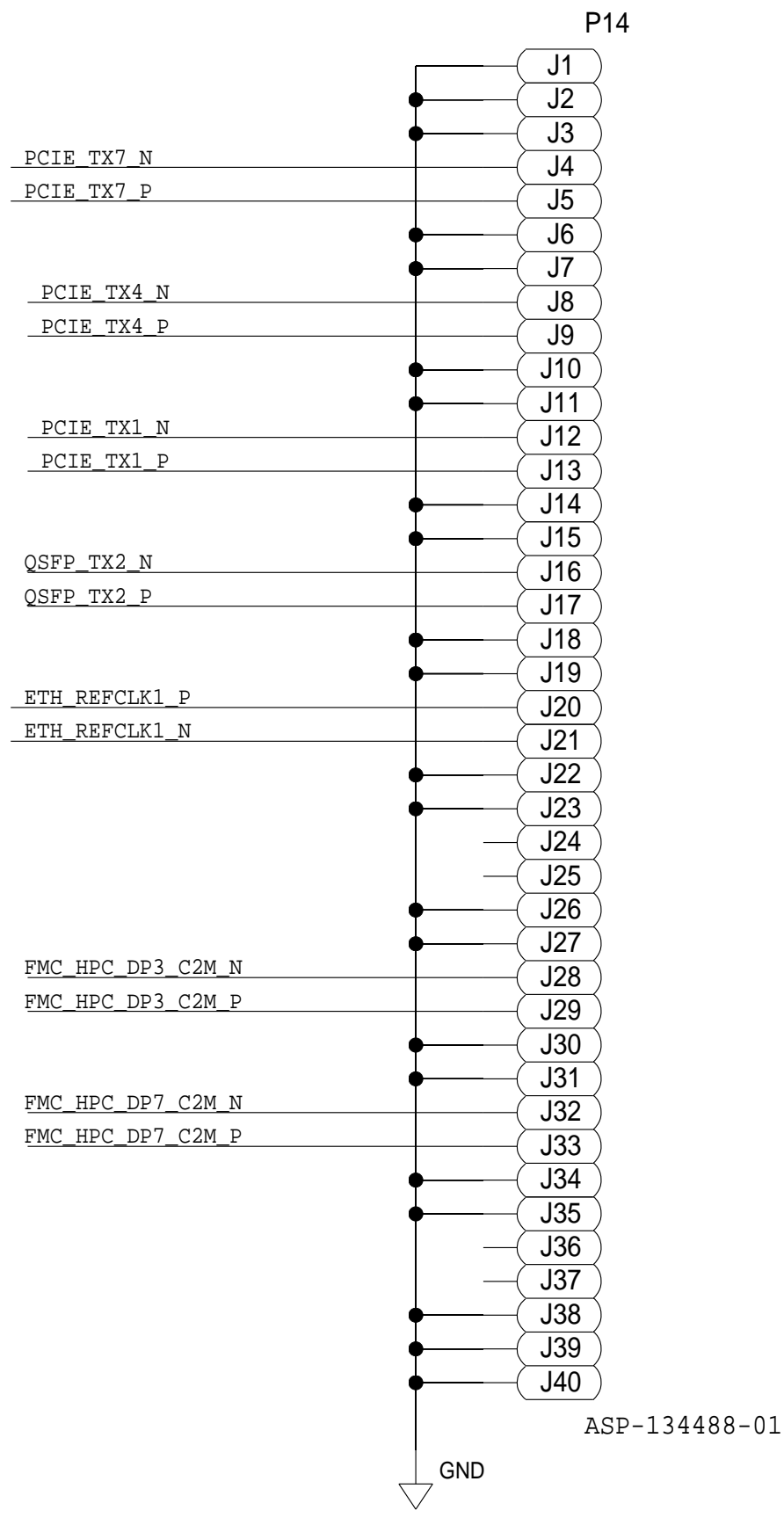
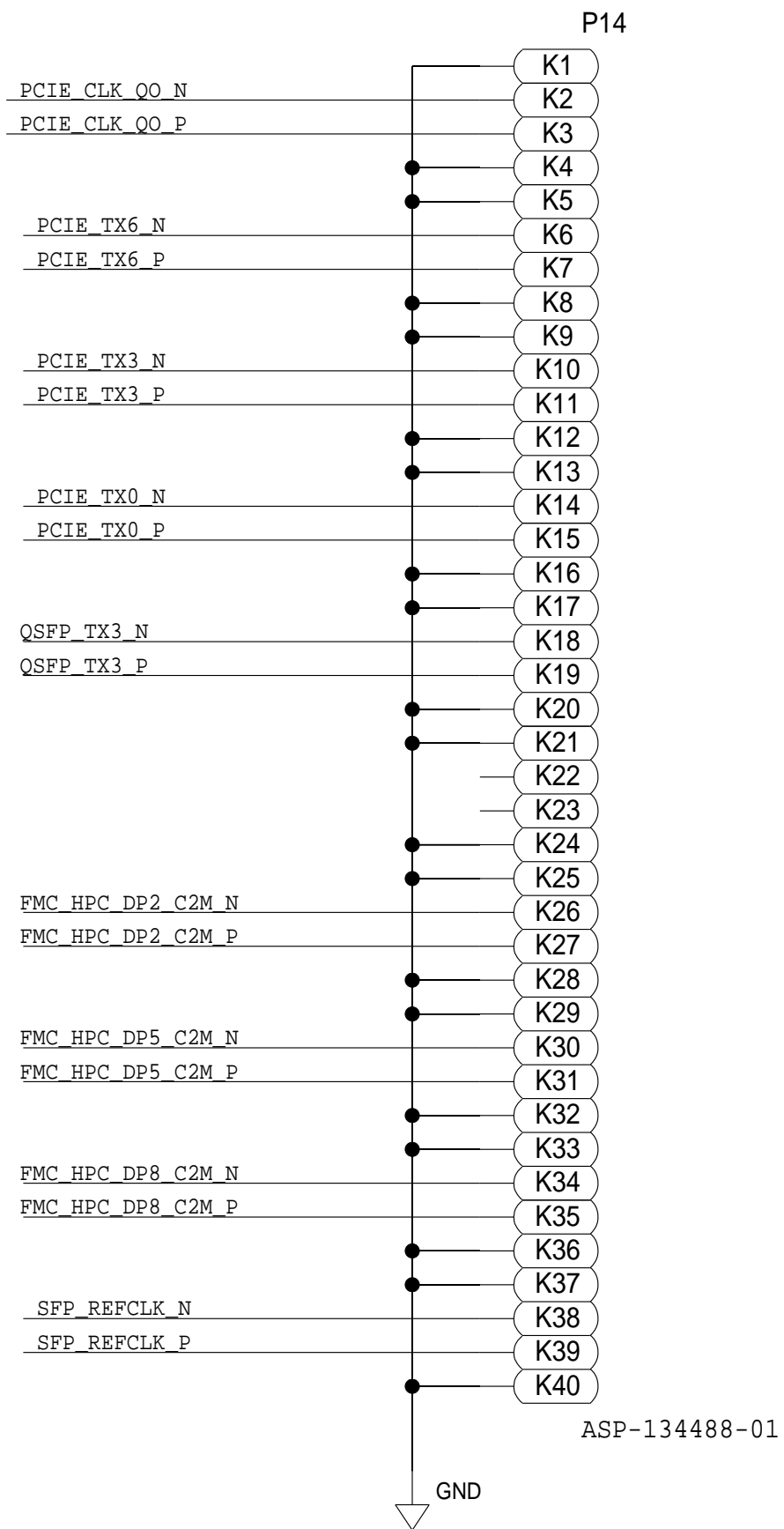



 ANALOG DEVICES	SCHEMATIC				
	HW TYPE : Customer Evaluation Z Product(s) : ADRV9009 : NA				
<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN CONNECTION WITH ANY OTHER PRODUCT, OR FOR ANY OTHER PURPOSE, WITHOUT THE PERMISSION OF ANALOG DEVICES.</small> <small>THE EQUIPMENT DESIGNATION MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY OWNED ANALOG DEVICES</small>	DESIGN VIEW <DESIGN_VIEW>		DRAWING NO. 02_048950		REV C
	PTD ENGINEER C.GOGA		SIZE D	SCALE 1:1	SHEET 2 OF 19

SOM CONNECTOR 2

FMC MALE CONNECTOR

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



 ANALOG DEVICES	<h1 style="text-align: center;">SCHEMATIC</h1>				
	<p>HW TYPE : Customer Evaluation Z</p> <p>Product(s) : ADRV9009</p> <p>: NA</p>				
<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, NOR USED IN ANY MANNER WITHOUT THE WRITTEN PERMISSION OF ANALOG DEVICES. THE EQUIPMENT DESIGNATION MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.</small>	DESIGN VIEW <DESIGN_VIEW>		DRAWING NO. 02_048950		REV C
PTD ENGINEER C. GOGA	SIZE D	SCALE 1:1	SHEET 3 OF 19		

87654321

REV

DESCRIPTION

DATE

APPROVED

FMC HA/HB CONNECTIONS

GPIO_3P3_0_A

JP43

GPIO_3P3_0_A_FMC

GPIO_3P3_1_A

JP44

GPIO_3P3_1_A_FMC

GPIO_3P3_2_A

JP45

GPIO_3P3_2_A_FMC

GPIO_3P3_3_A

JP46

GPIO_3P3_3_A_FMC

GPIO_3P3_4_A

JP47

GPIO_3P3_4_A_FMC

GPIO_3P3_5_A

JP48

GPIO_3P3_5_A_FMC

GPIO_3P3_6_A

JP49

GPIO_3P3_6_A_FMC

GPIO_3P3_7_A

JP50

GPIO_3P3_7_A_FMC

GPIO_3P3_8_A

JP51

GPIO_3P3_8_A_FMC

GPIO_3P3_9_A

JP52

GPIO_3P3_9_A_FMC

GPIO_3P3_10_A

JP53

GPIO_3P3_10_A_FMC

GPIO_3P3_11_A

JP54

GPIO_3P3_11_A_FMC

AUXADC_0_A

JP78

AUXADC_0_A_FMC

AUXADC_1_A

JP79

AUXADC_1_A_FMC

AUXADC_2_A

JP80

AUXADC_2_A_FMC

AUXADC_3_A

JP81

AUXADC_3_A_FMC

AUX_SYNTH_OUT_A

JP82

AUX_SYNTH_OUT_A_FMC

AUX_SYNTH_VTUNE_A

JP83

AUX_SYNTH_VTUNE_A_FMC

RF_SYNTH_VTUNE_A

JP84

RF_SYNTH_VTUNE_A_FMC

GPIO_3P3_0_B

JP31

GPIO_3P3_0_B_FMC

GPIO_3P3_1_B

JP32

GPIO_3P3_1_B_FMC

GPIO_3P3_2_B

JP33

GPIO_3P3_2_B_FMC

GPIO_3P3_3_B

JP34

GPIO_3P3_3_B_FMC

GPIO_3P3_4_B

JP35

GPIO_3P3_4_B_FMC

GPIO_3P3_5_B

JP36

GPIO_3P3_5_B_FMC

GPIO_3P3_6_B

JP37

GPIO_3P3_6_B_FMC

GPIO_3P3_7_B

JP38

GPIO_3P3_7_B_FMC

GPIO_3P3_8_B

JP39

GPIO_3P3_8_B_FMC

GPIO_3P3_9_B

JP40

GPIO_3P3_9_B_FMC

GPIO_3P3_10_B

JP41

GPIO_3P3_10_B_FMC

GPIO_3P3_11_B

JP42

GPIO_3P3_11_B_FMC

GPIO_0_B

JP55

GPIO_0_B_FMC

GPIO_1_B

JP56

GPIO_1_B_FMC

GPIO_2_B

JP57

GPIO_2_B_FMC

GPIO_3_B

JP58

GPIO_3_B_FMC

GPIO_4_B

JP59

GPIO_4_B_FMC

GPIO_5_B

JP60

GPIO_5_B_FMC

GPIO_6_B

JP61

GPIO_6_B_FMC

GPIO_7_B

JP62

GPIO_7_B_FMC

GPIO_8_B

JP63

GPIO_8_B_FMC

GPIO_9_B

JP64

GPIO_9_B_FMC

GPIO_10_B

JP65

GPIO_10_B_FMC

GPIO_11_B

JP66

GPIO_11_B_FMC

GPIO_12_B

JP67

GPIO_12_B_FMC

GPIO_13_B

JP68

GPIO_13_B_FMC

GPIO_14_B

JP69

GPIO_14_B_FMC

GPIO_15_B

JP70

GPIO_15_B_FMC

GPIO_16_B

JP71

GPIO_16_B_FMC

GPIO_17_B

JP72

GPIO_17_B_FMC

RX1_ENABLE_B

JP73

RX1_ENABLE_B_FMC

RX2_ENABLE_B

JP74

RX2_ENABLE_B_FMC

TX1_ENABLE_B

JP75

TX1_ENABLE_B_FMC

TX2_ENABLE_B

JP76

TX2_ENABLE_B_FMC

GPIO_18_B

JP77

GPIO_18_B_FMC

GPIO_0_A

JP8

GPIO_0_A_FMC

GPIO_1_A

JP9

GPIO_1_A_FMC

GPIO_2_A

JP10

GPIO_2_A_FMC

GPIO_3_A

JP11

GPIO_3_A_FMC

GPIO_4_A

JP12

GPIO_4_A_FMC

GPIO_5_A

JP13

GPIO_5_A_FMC

GPIO_6_A

JP14

GPIO_6_A_FMC

GPIO_7_A

JP15

GPIO_7_A_FMC

GPIO_8_A

JP16

GPIO_8_A_FMC

GPIO_9_A

JP17

GPIO_9_A_FMC

GPIO_10_A

JP18

GPIO_10_A_FMC

GPIO_11_A

JP19

GPIO_11_A_FMC

GPIO_12_A

JP20

GPIO_12_A_FMC

GPIO_13_A

JP21

GPIO_13_A_FMC

GPIO_14_A

JP22

GPIO_14_A_FMC

GPIO_15_A

JP23

GPIO_15_A_FMC

GPIO_16_A

JP24

GPIO_16_A_FMC

GPIO_17_A

JP25

GPIO_17_A_FMC

RX1_ENABLE_A

JP26

RX1_ENABLE_A_FMC

RX2_ENABLE_A

JP27

RX2_ENABLE_A_FMC

TX1_ENABLE_A

JP28

TX1_ENABLE_A_FMC

TX2_ENABLE_A

JP29

TX2_ENABLE_A_FMC

GPIO_18_A

JP30

GPIO_18_A_FMC

87654321

SIZE

SCALE

SHEET

OF

ANALOG DEVICES

DESIGN VIEW

PTD ENGINEER

C. GOGA

SCHEMATIC

HW TYPE : Customer Evaluation Z

Product(s) : ADRV9009

: NA

DRAWING NO.

02_048950

REV

C

SIZE

D

SCALE

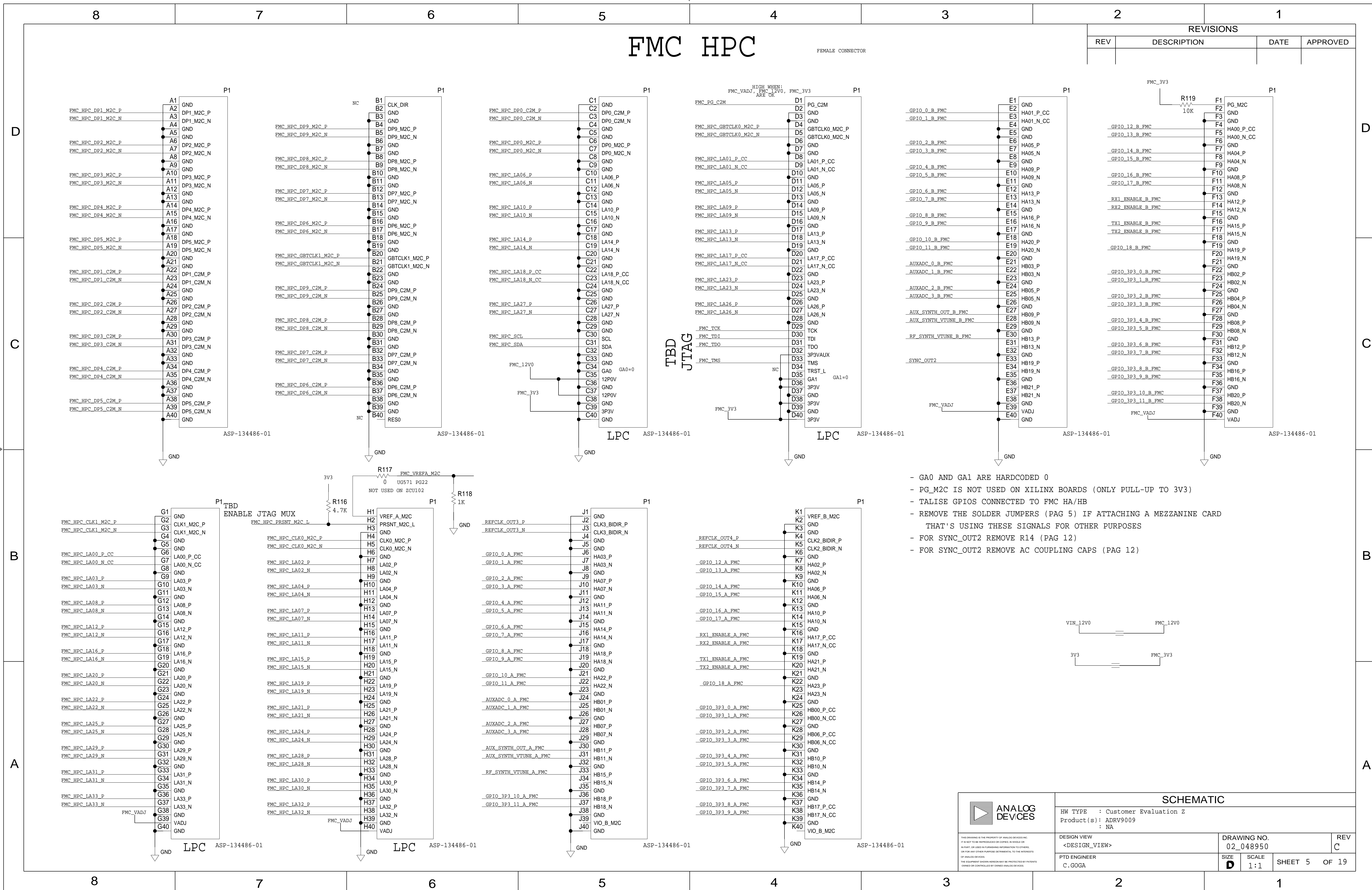
1:1

SHEET

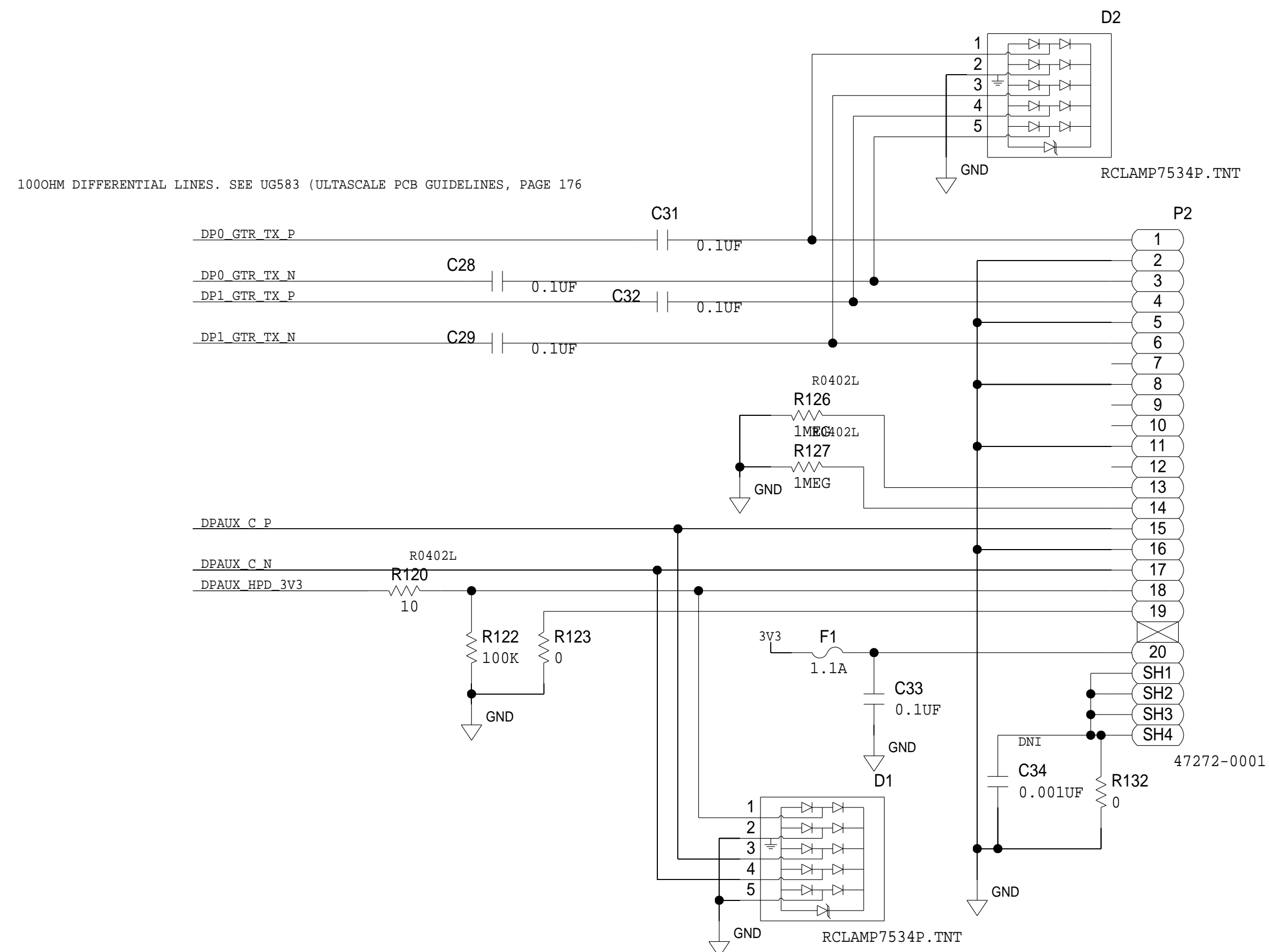
4

OF

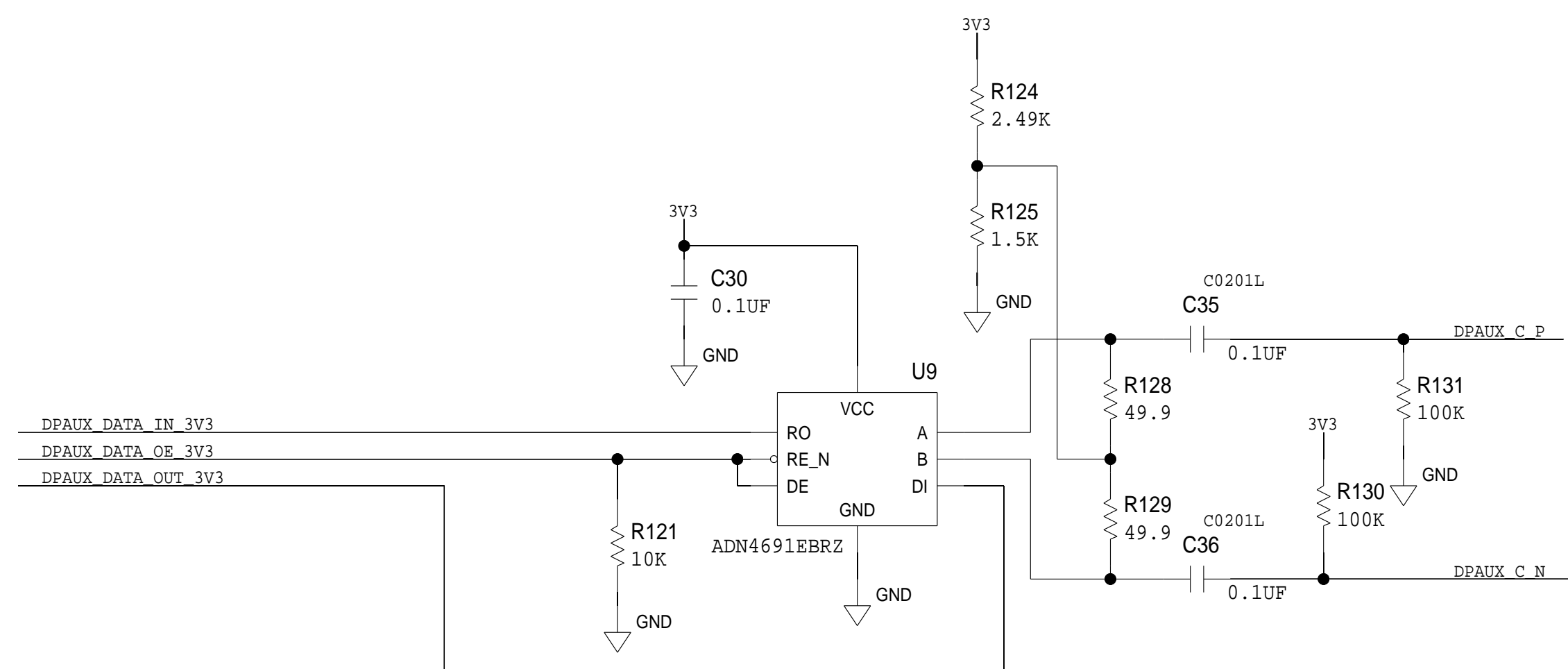
19



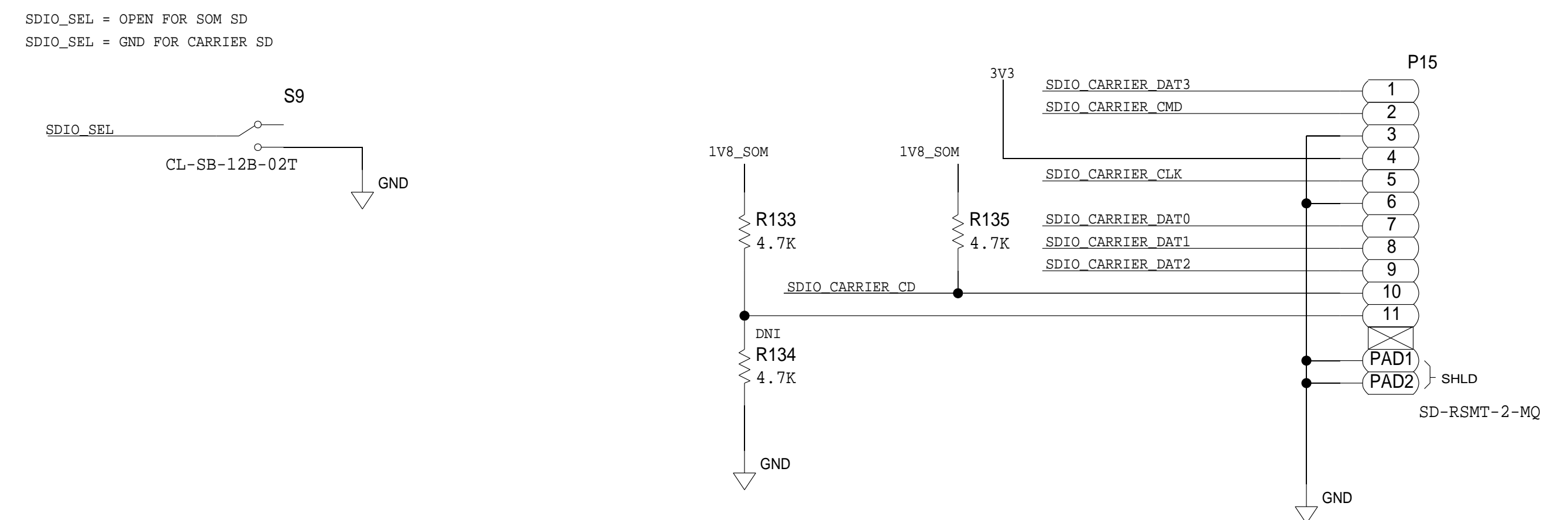
DISPLAYPORT



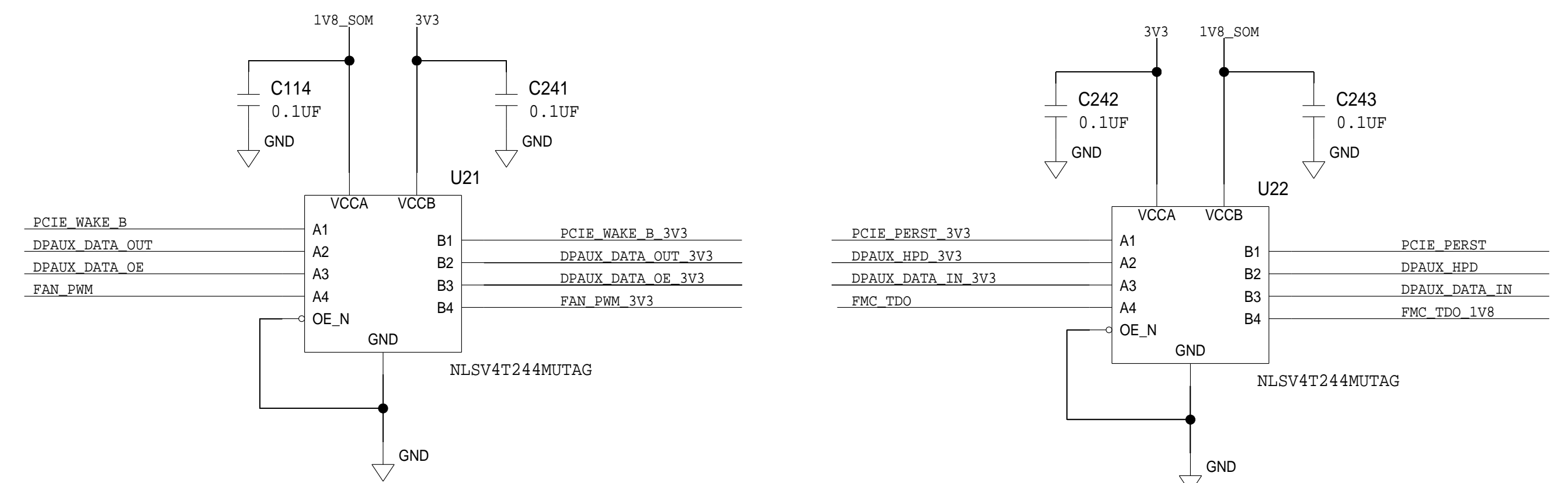
DPAUX GENERATOR



SD CARD

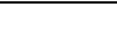


VOLTAGE TRANSLATORS



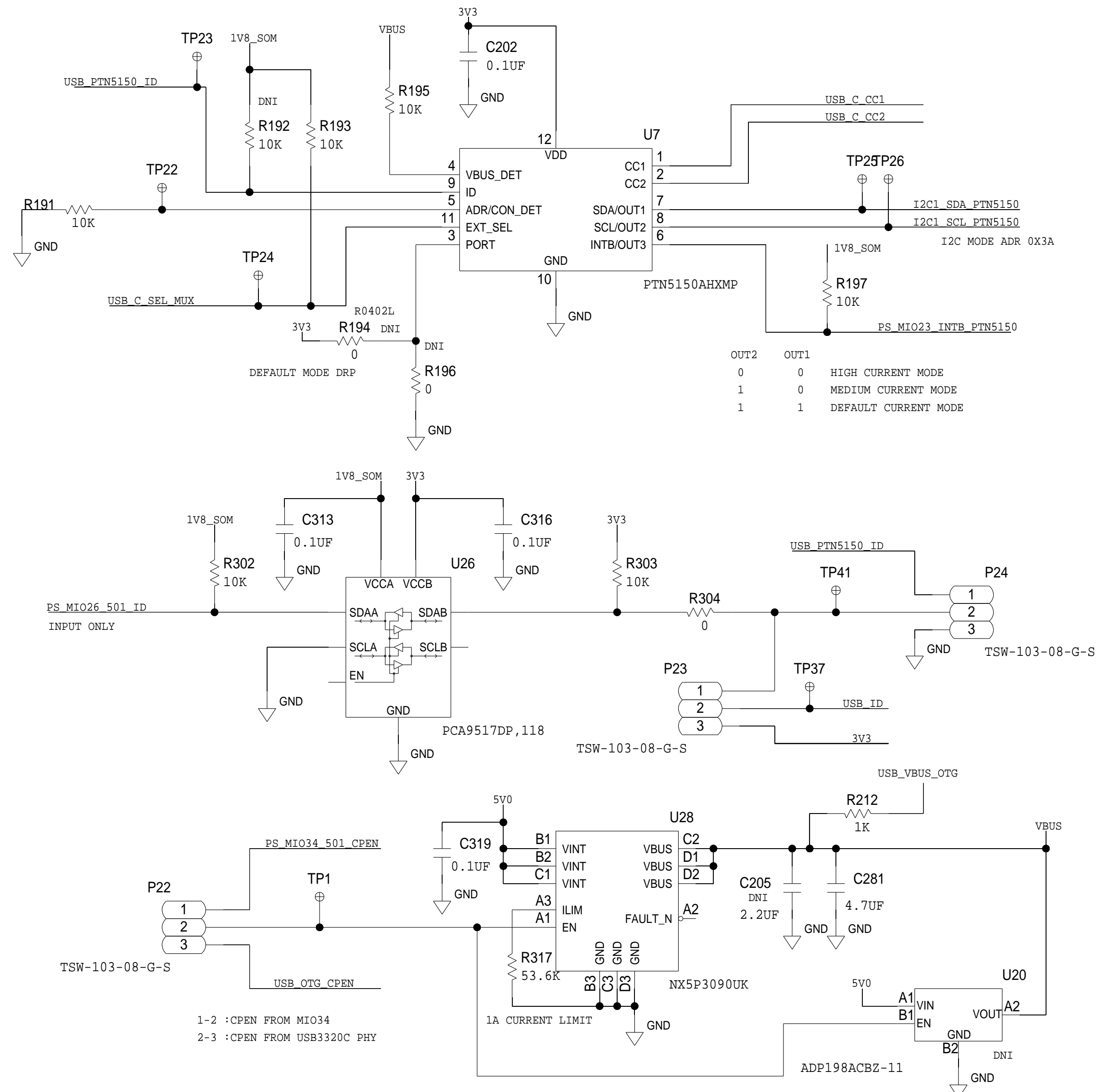
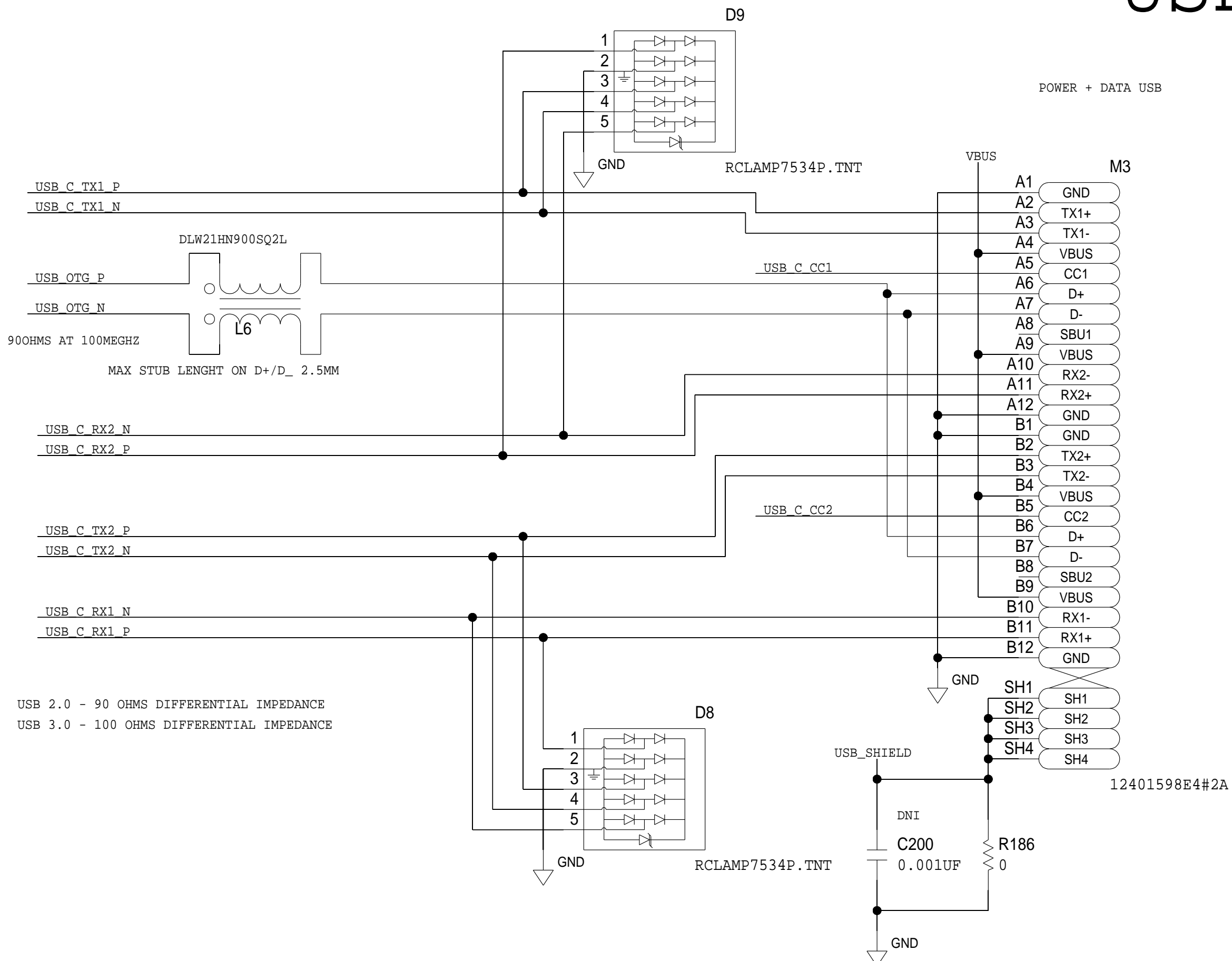
SOM -> CARRIER

CARRIER -> SOM

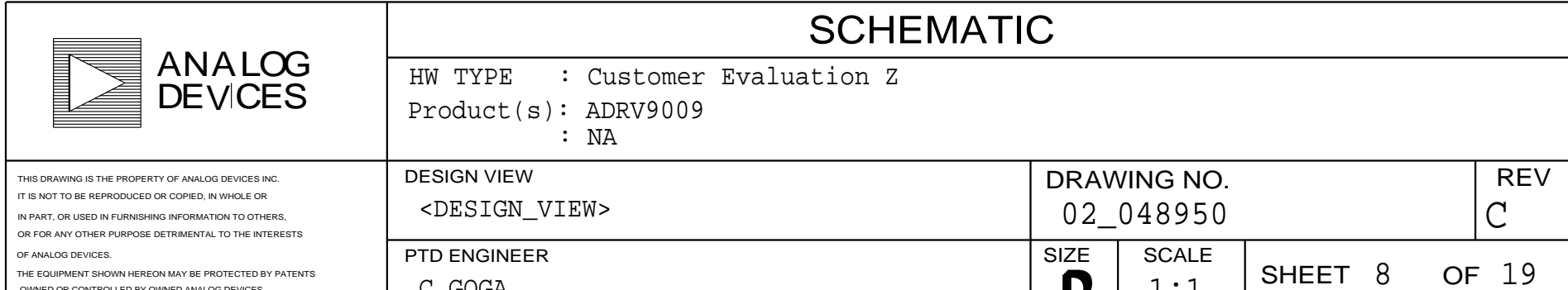
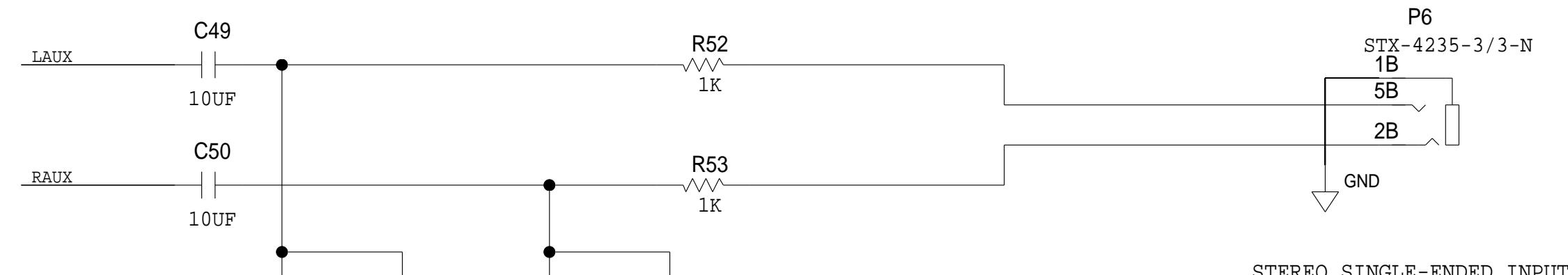
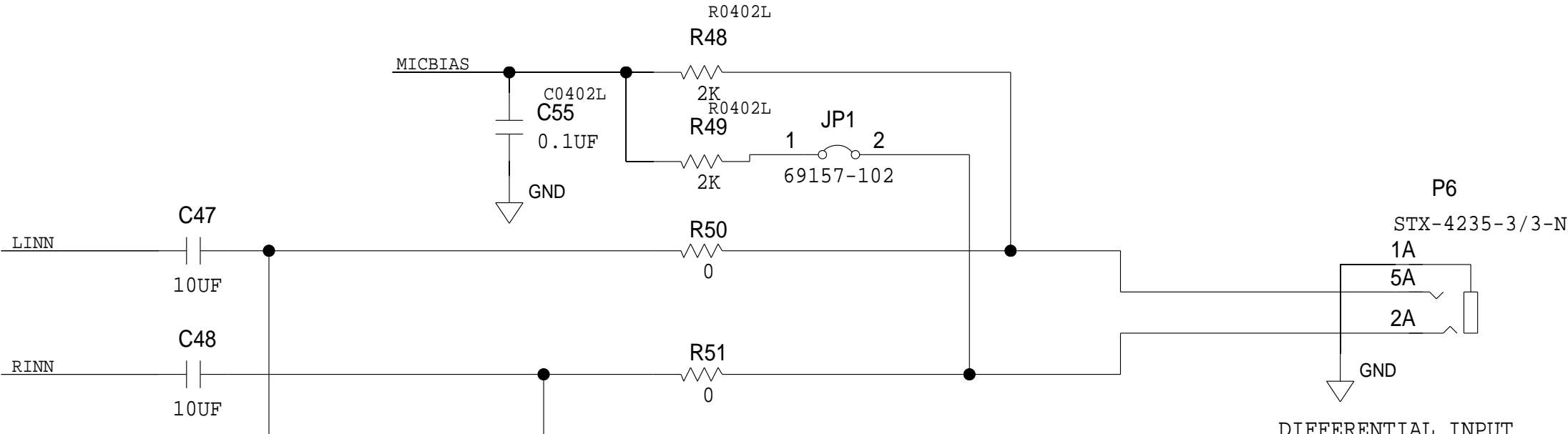
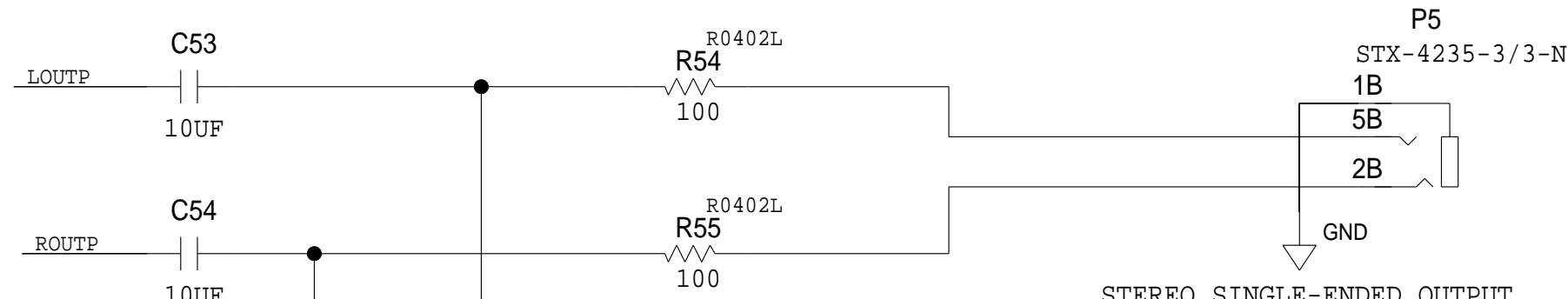
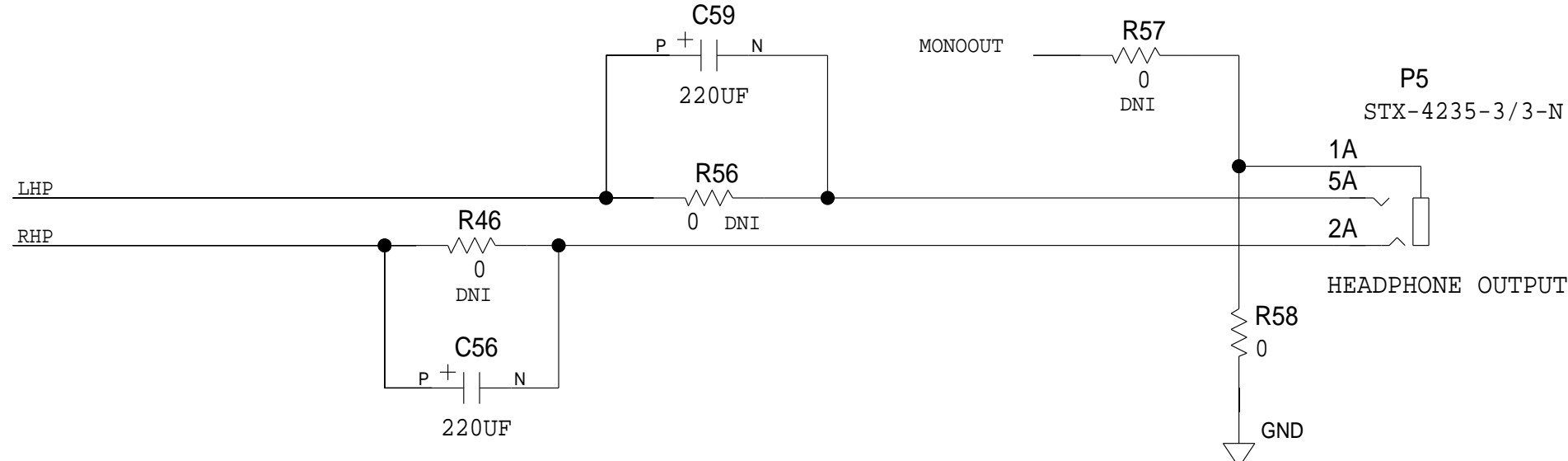
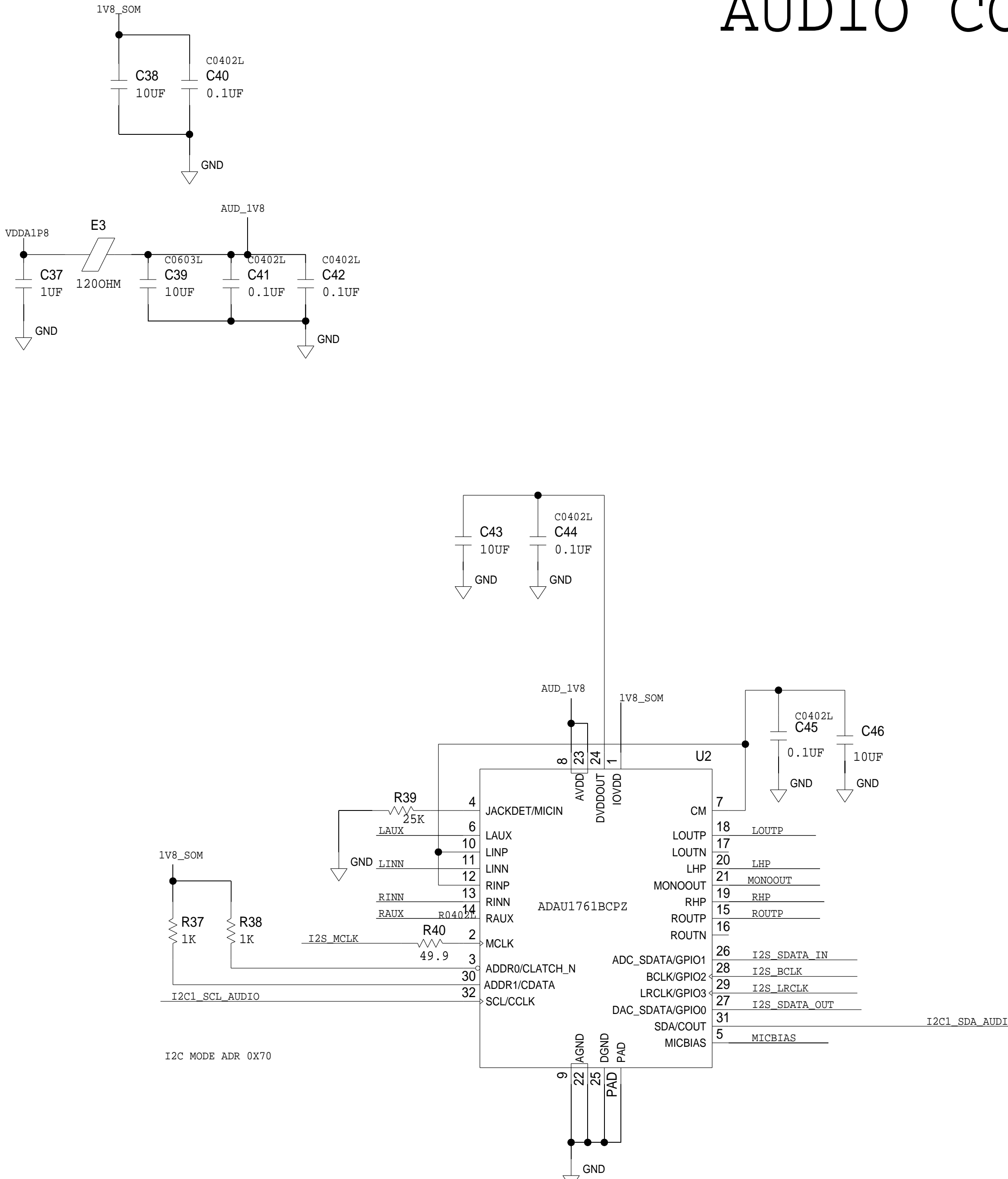
 ANALOG DEVICES		<h1 style="text-align: center;">SCHEMATIC</h1>			
HW TYPE : Customer Evaluation Z Product(s) : ADV9009 : NA					
DESIGN VIEW <DESIGN_VIEW>		DRAWING NO. 02_048950		REV C	
PTD ENGINEER C. GOGA		SIZE D	SCALE 1:1	SHEET 6 OF 19	

USB 3.1

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

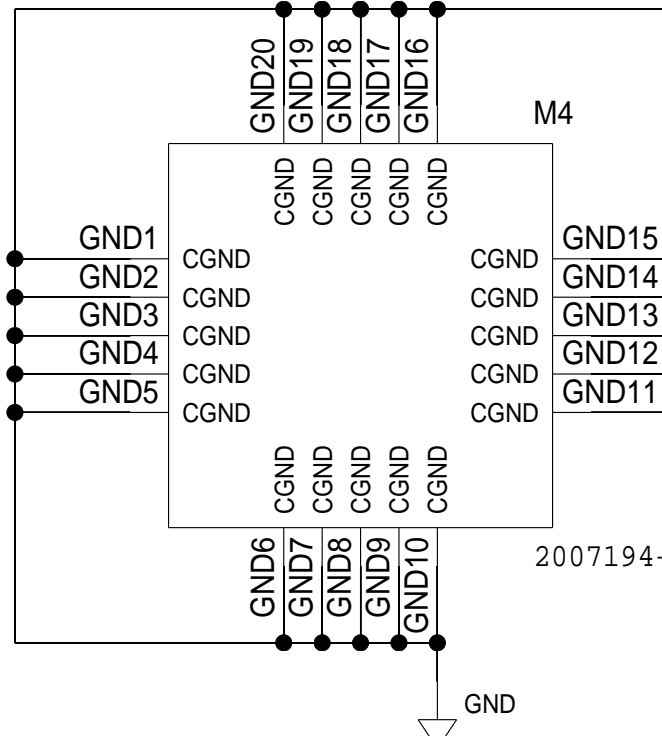
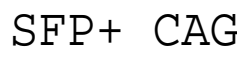
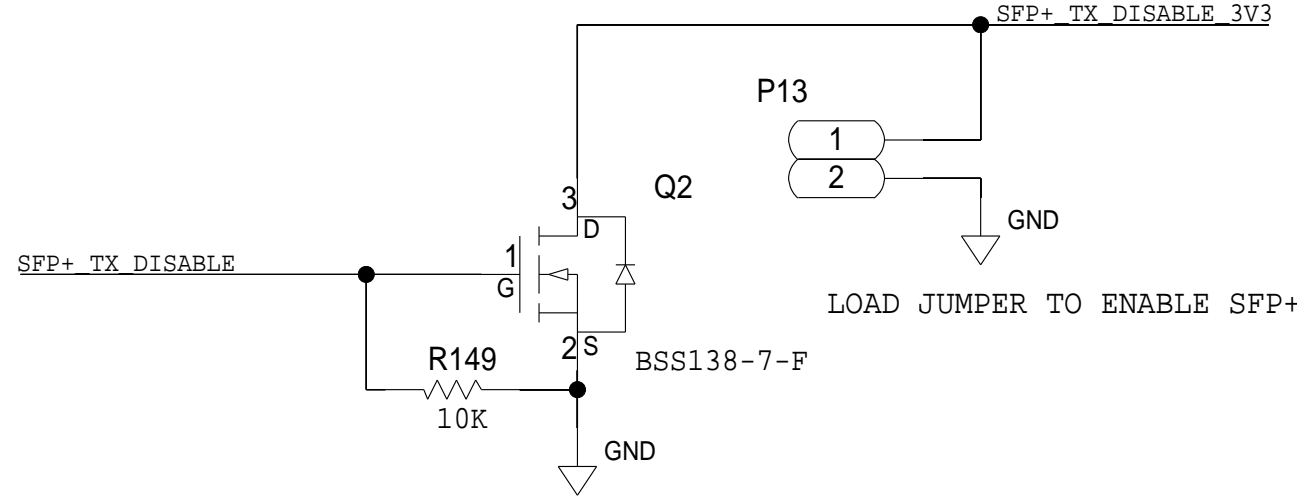
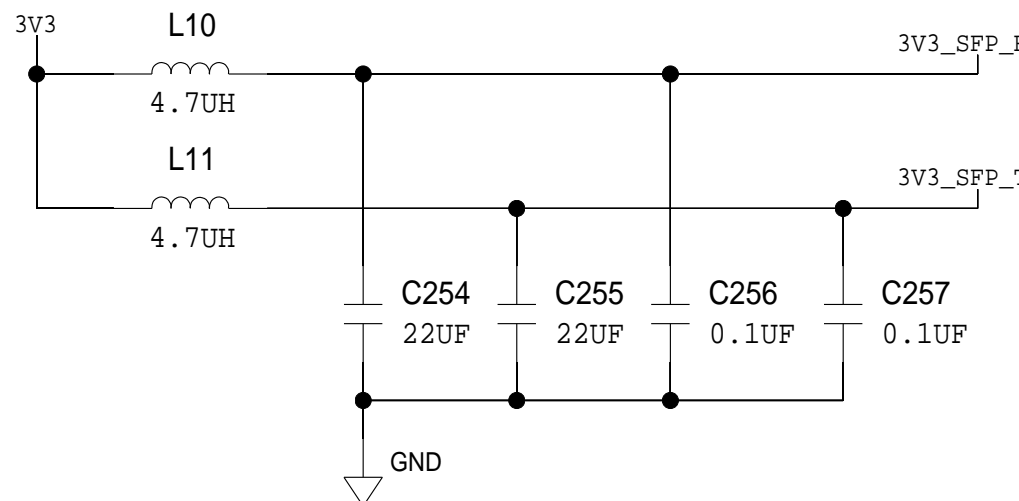
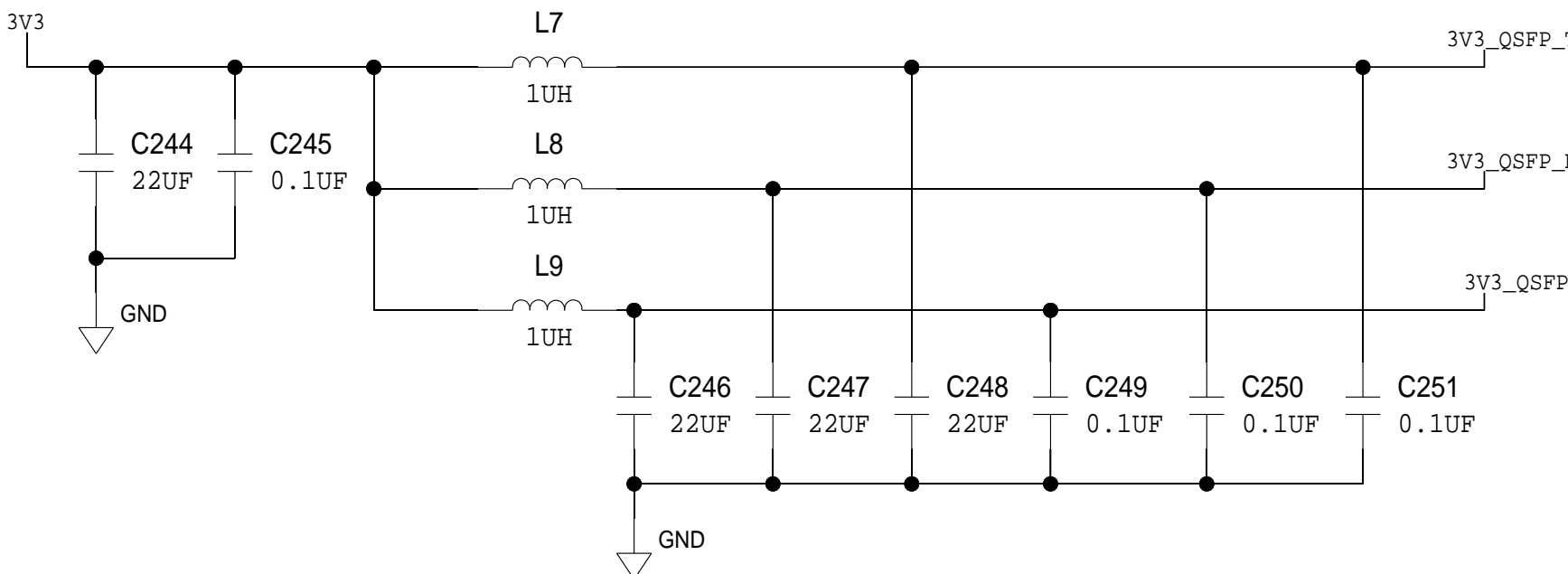
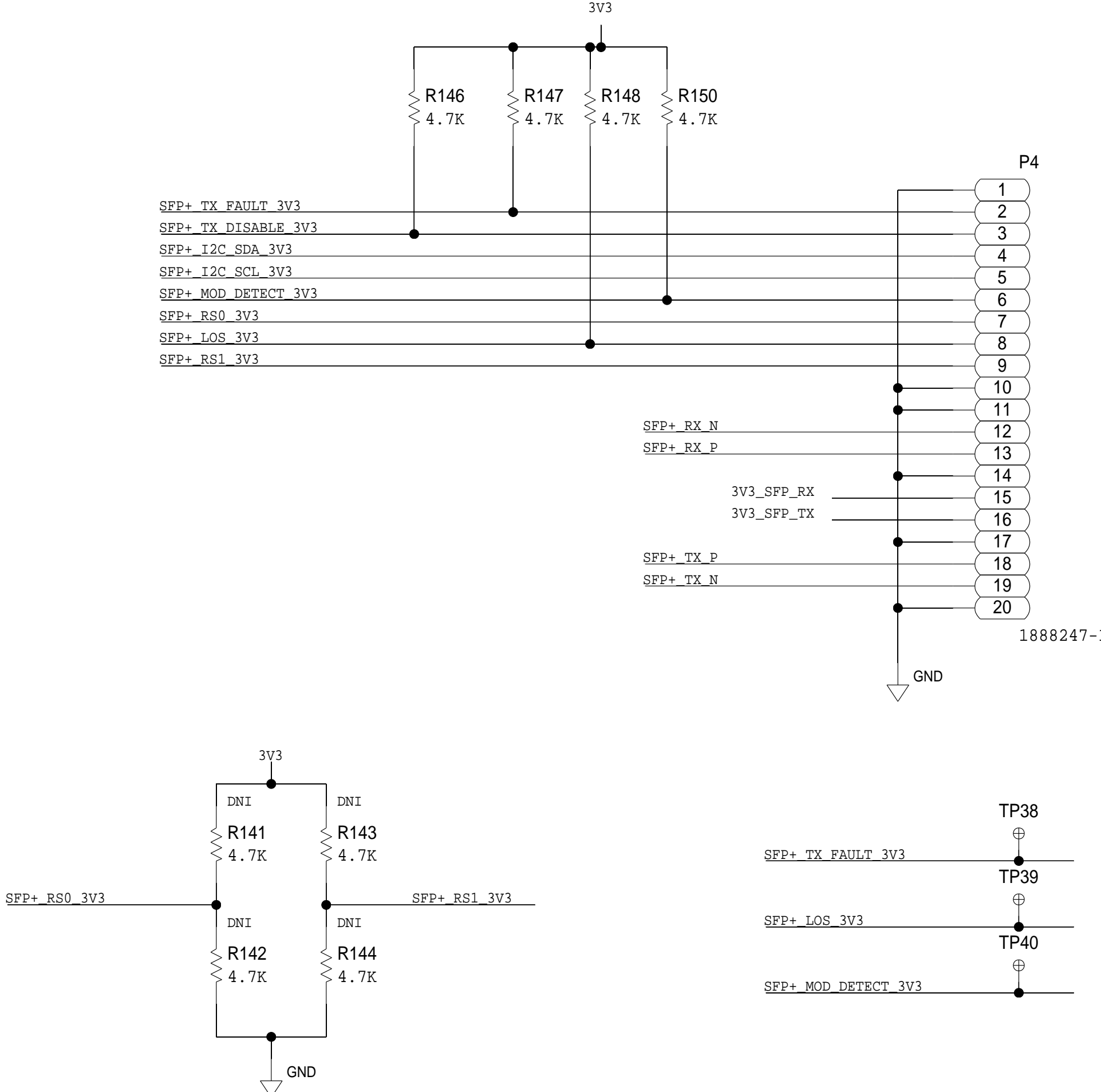
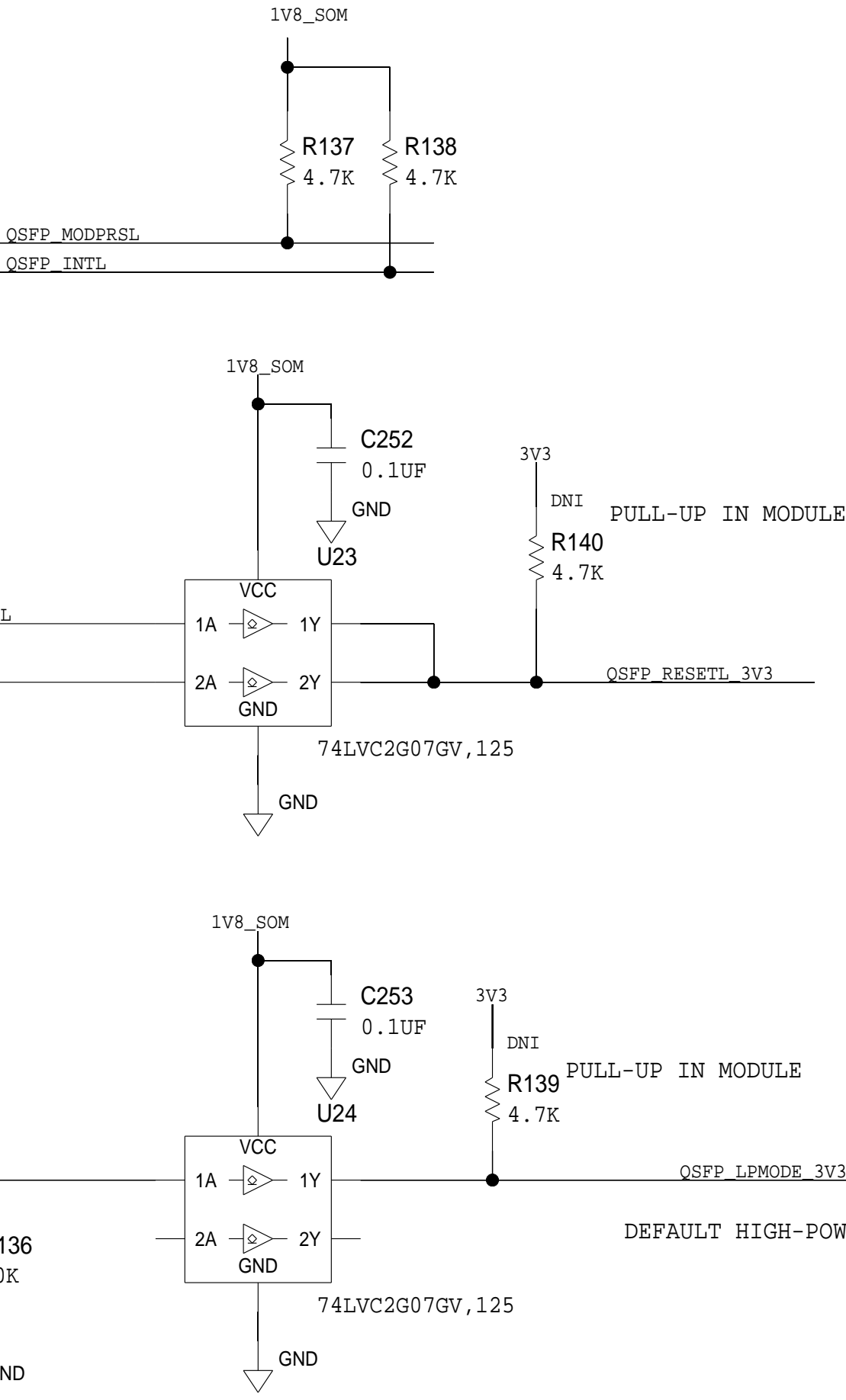
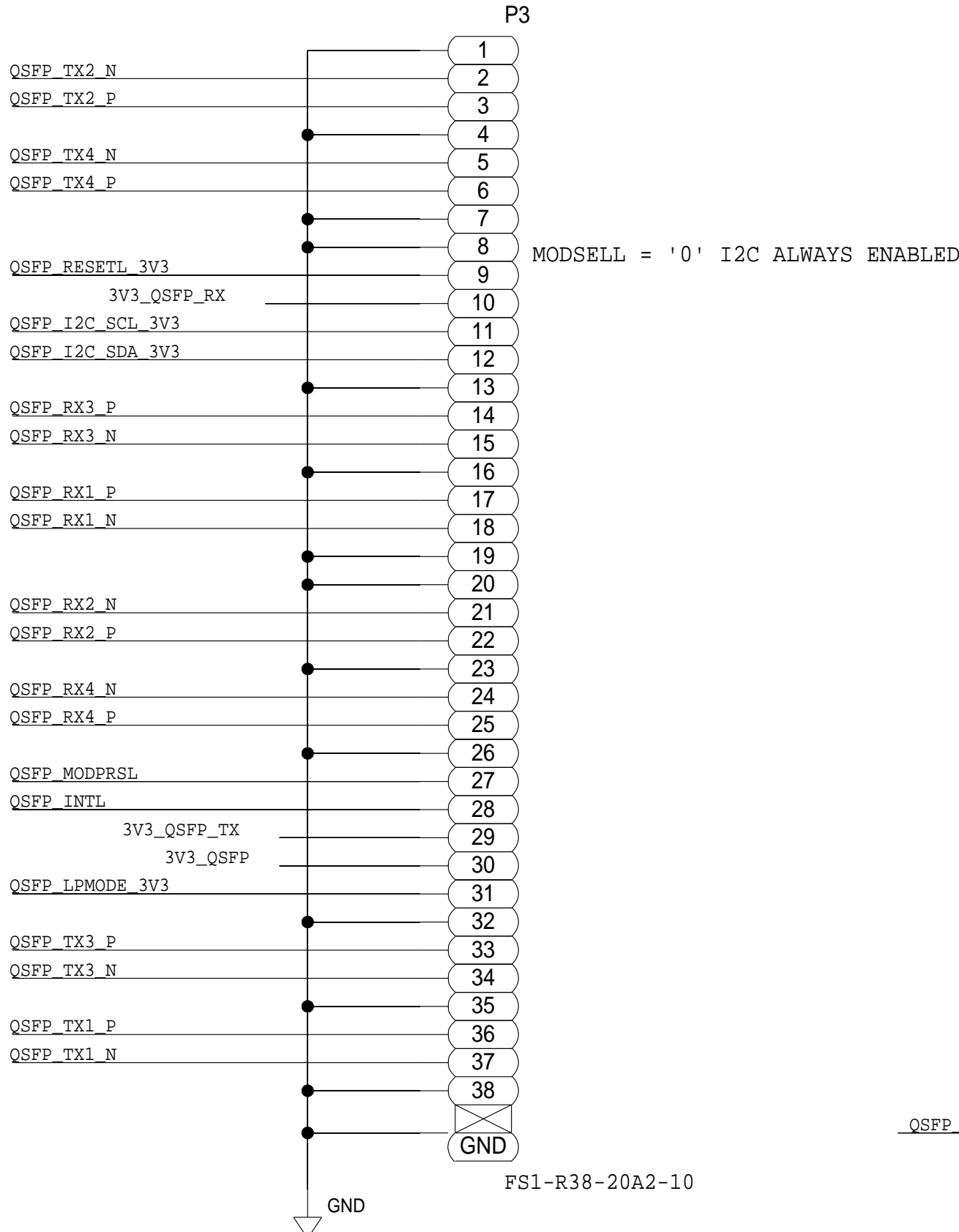



AUDIO CODEC



SFP+, QSFP+

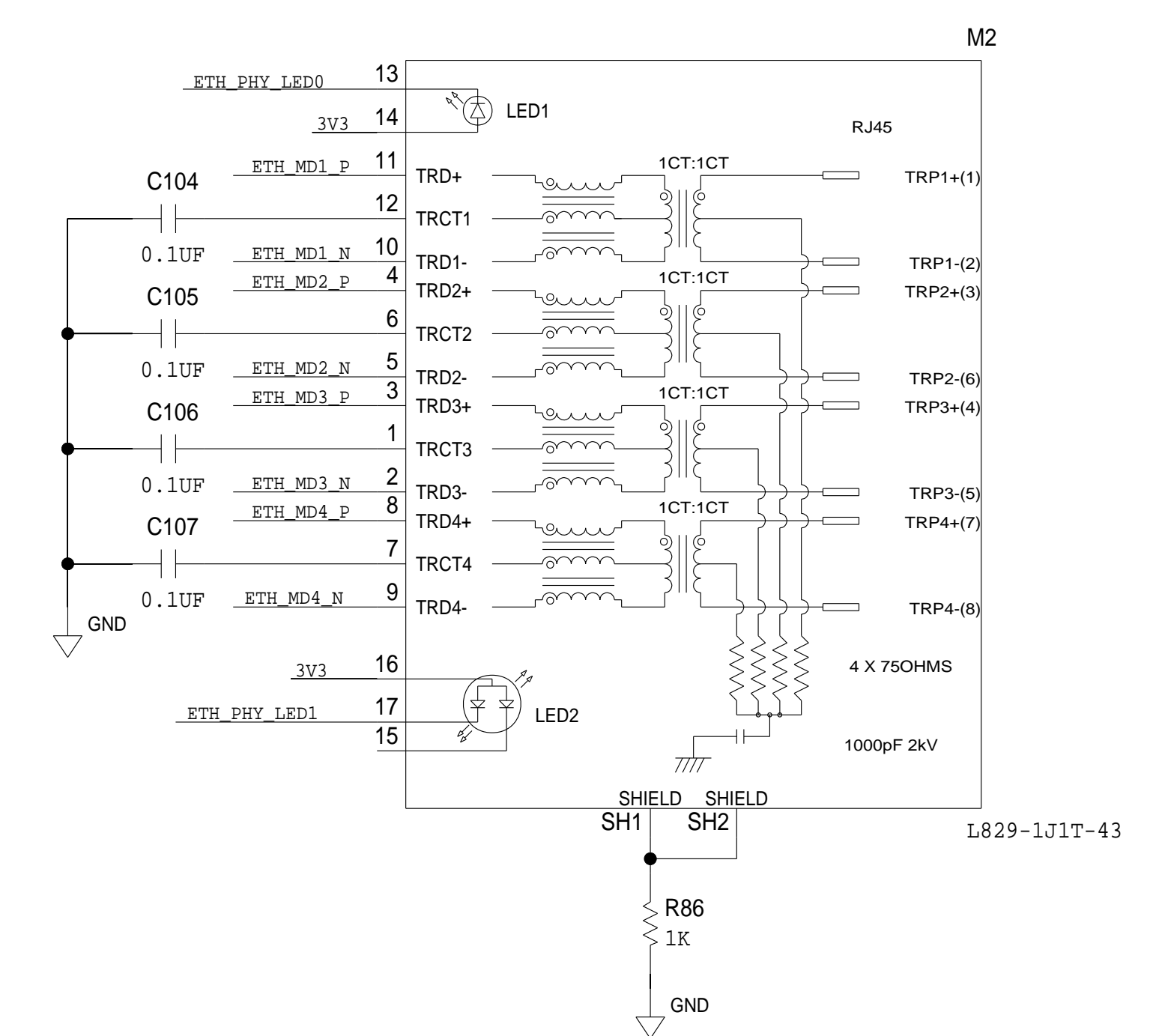
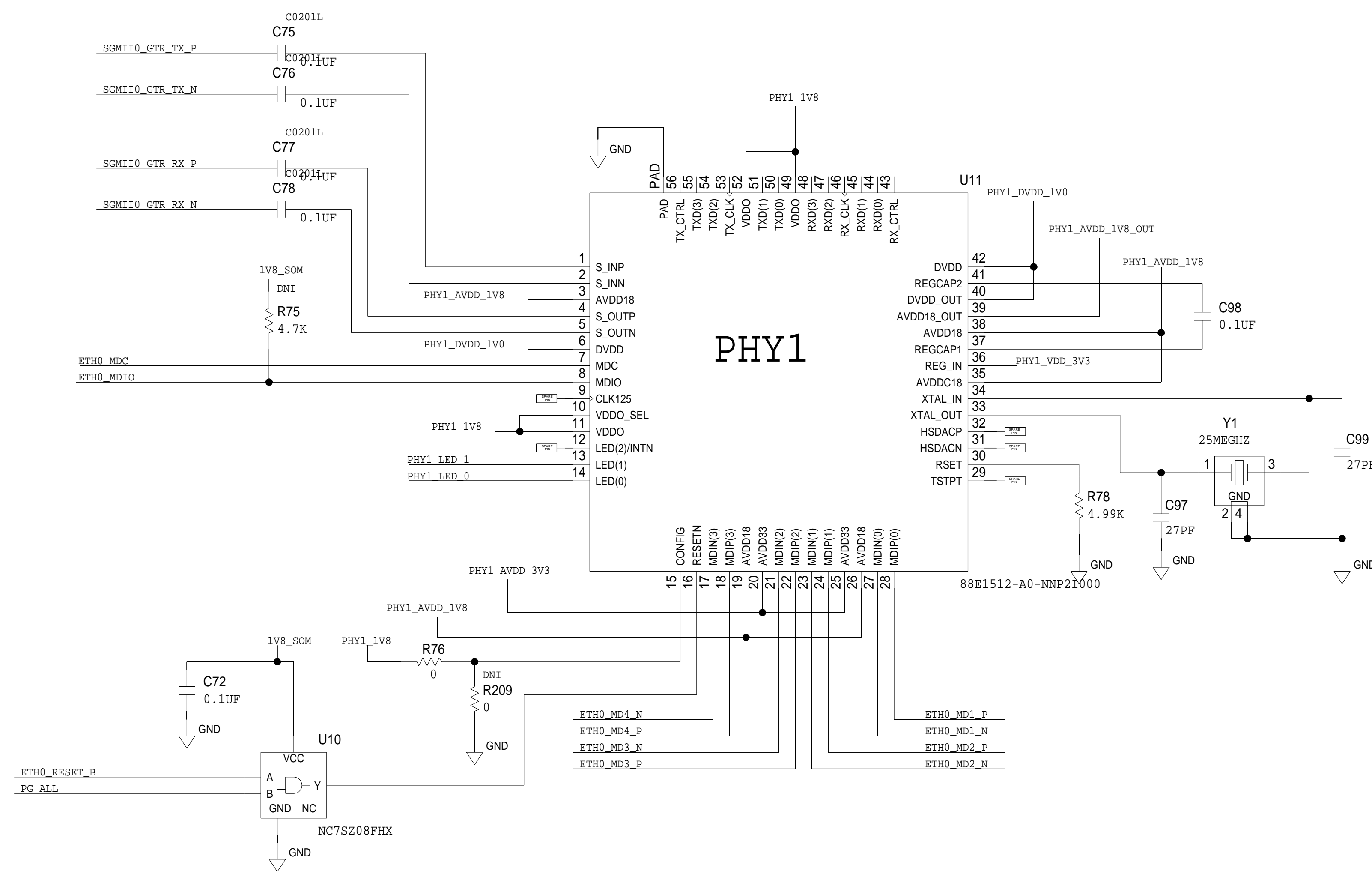
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



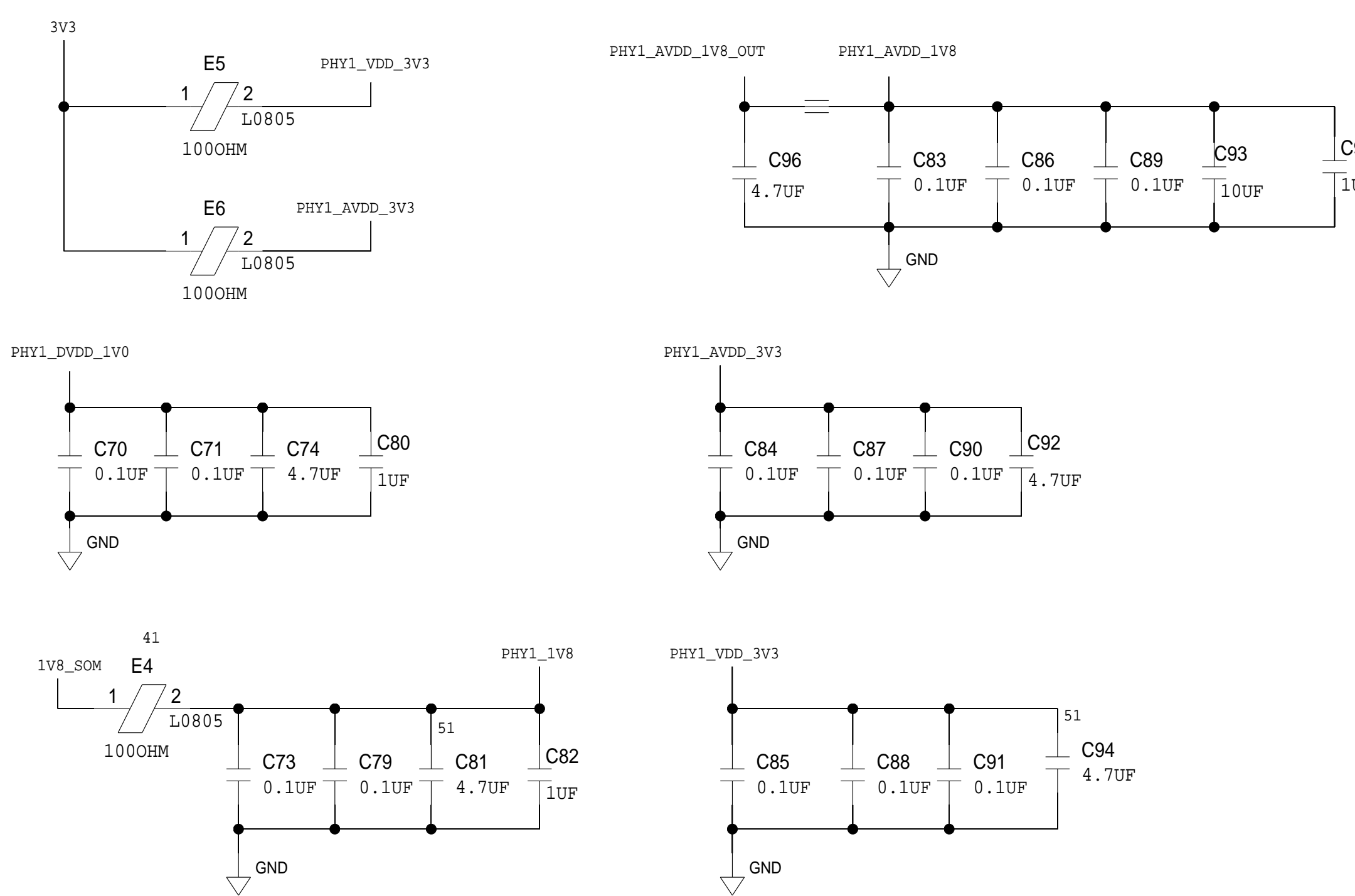
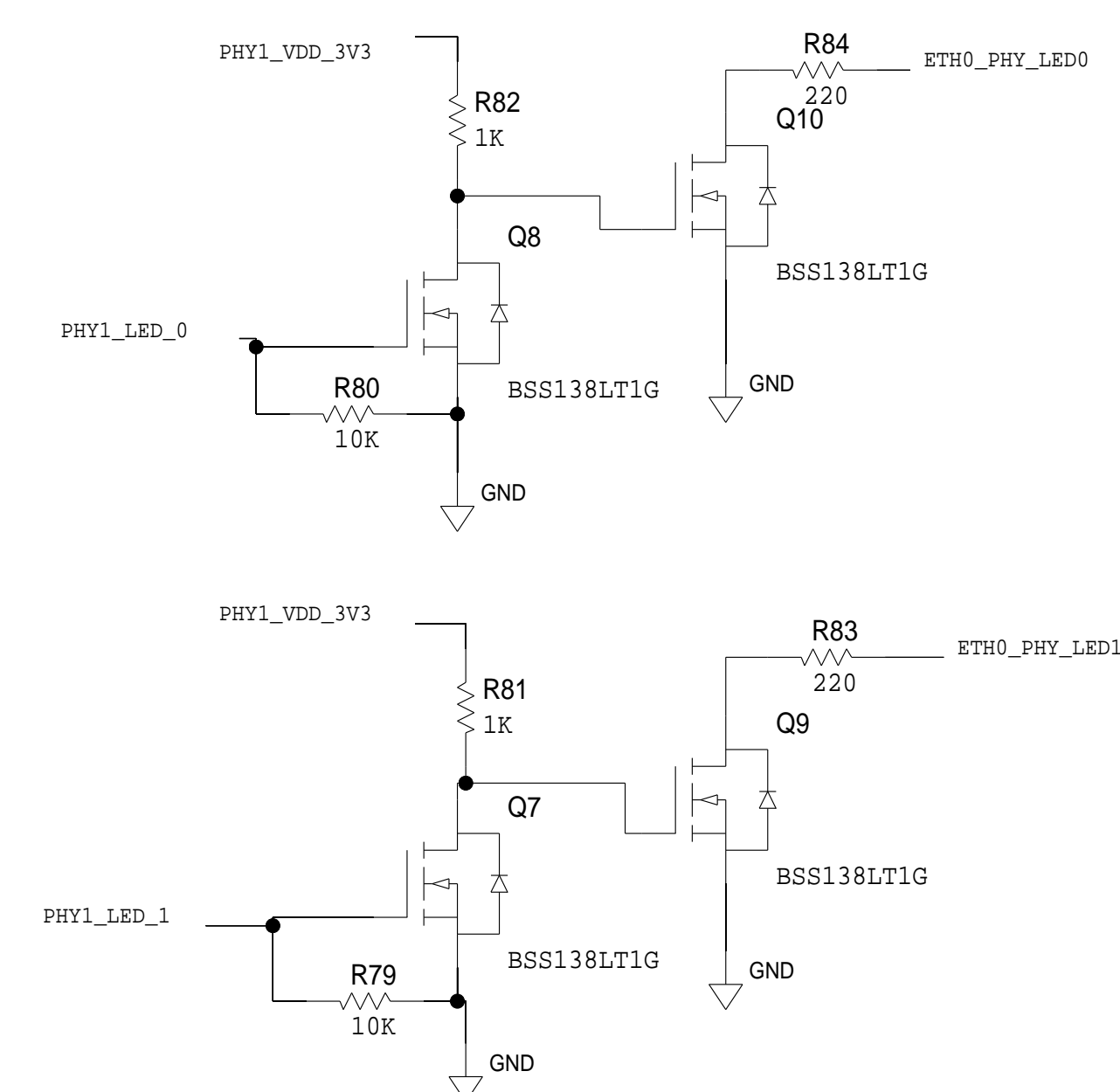
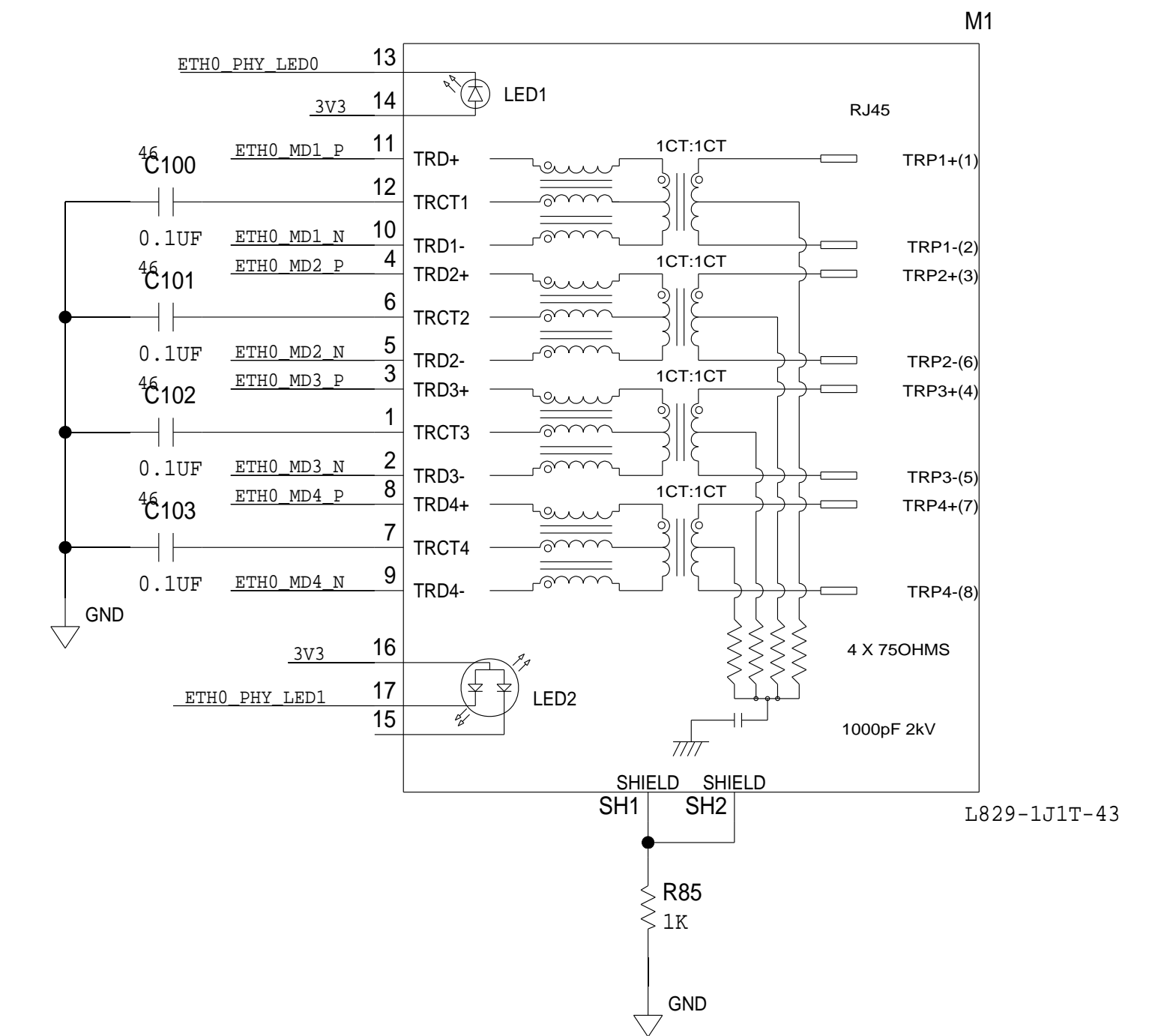
 ANALOG DEVICES	SCHEMATIC				
	HW TYPE : Customer Evaluation Z Product(s) : ADRV9009 : NA				
<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSES PERTAINING TO THE INTERESTS OF ANALOG DEVICES.</small> <small>THIS EQUIPMENT SCHEMATIC DRAWING MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ORAINED ANALOG DEVICES.</small>	DESIGN VIEW <DESIGN_VIEW>		DRAWING NO. 02_048950		REV C
	PTD ENGINEER C. GOGA		SIZE D	SCALE 1:1	SHEET 9 OF 19


ETHERNET

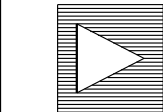
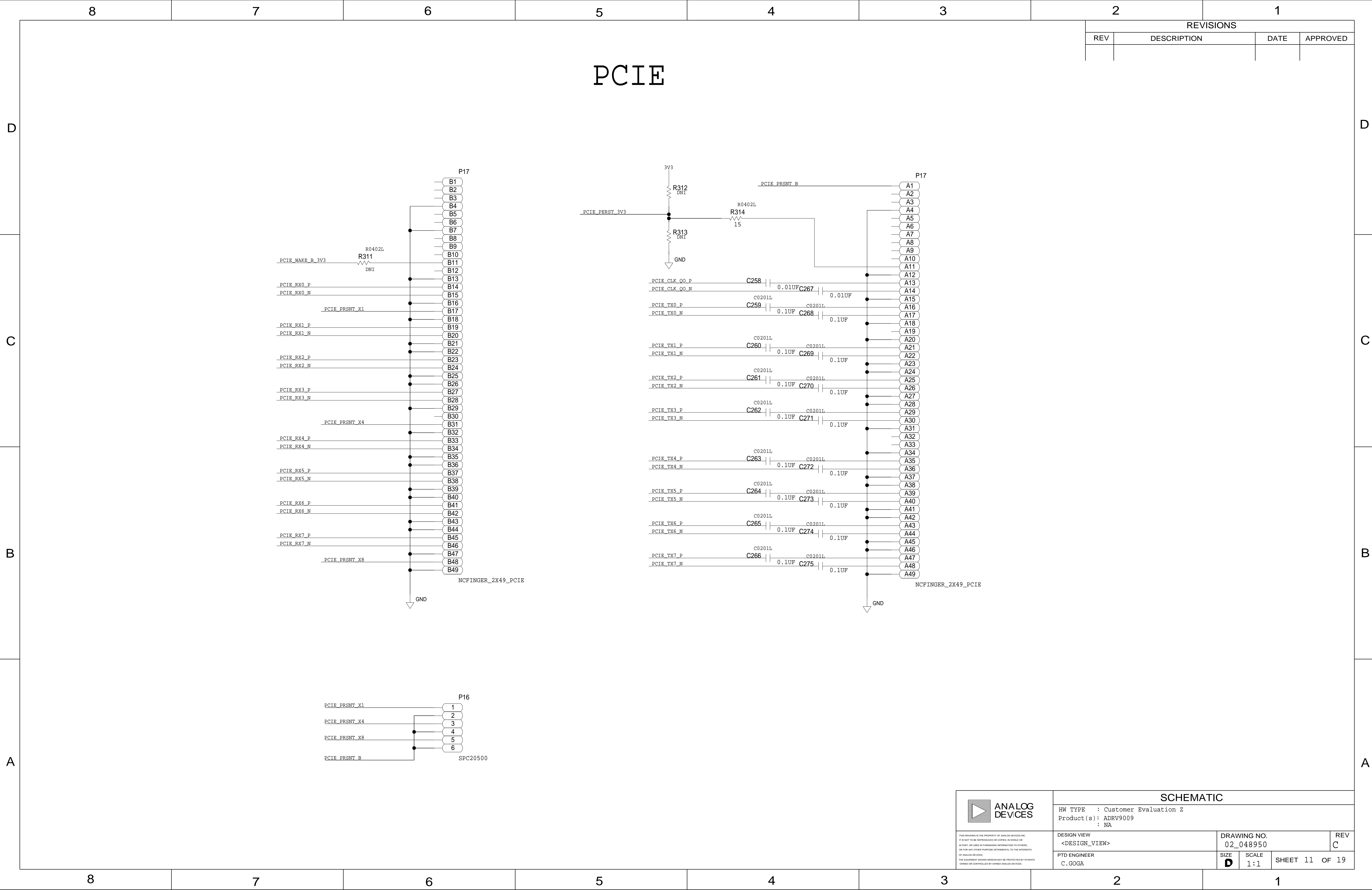
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



PASSIVE CONN



 ANALOG DEVICES	<h1 style="text-align: center;">SCHEMATIC</h1>			
	<p>HW TYPE : Customer Evaluation Z</p> <p>Product(s) : ADRV9009</p> <p>: NA</p>			
<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSES DETERMINED, TO THE INTERESTS OF ANALOG DEVICES. THIS EQUIPMENT DESIGN INFORMATION MAY BE PROVIDED BY PATENTS OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.</small>	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_048950	REV C	
	PTD ENGINEER C. GOGGA	SIZE D	SCALE 1:1	SHEET 10 OF 19

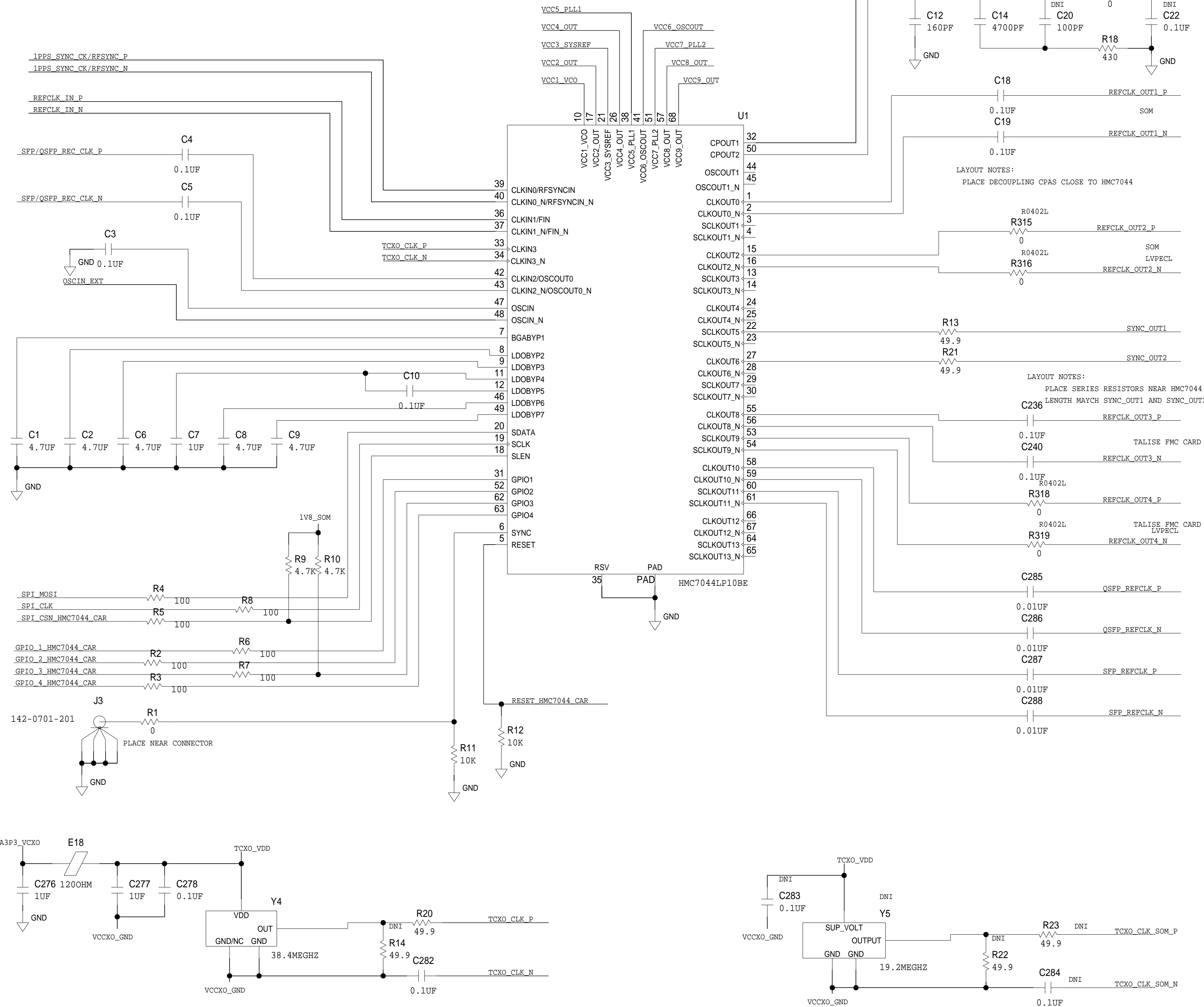


ANALOG
DEVICES

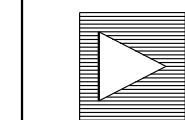
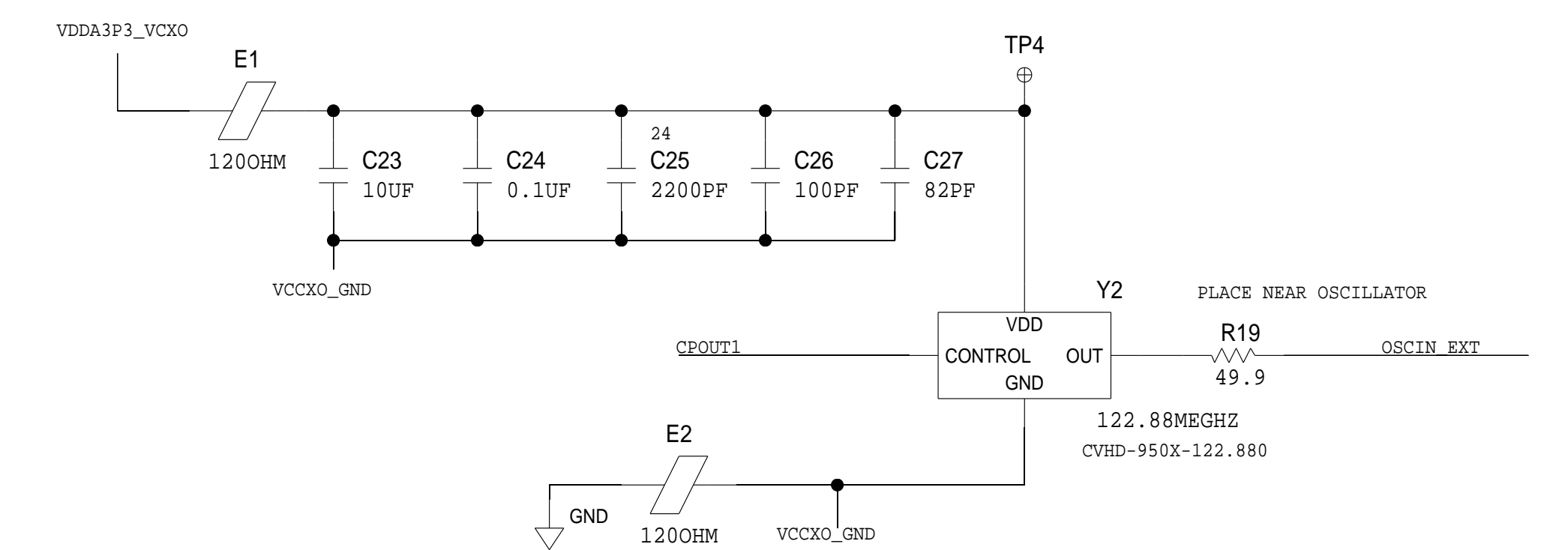
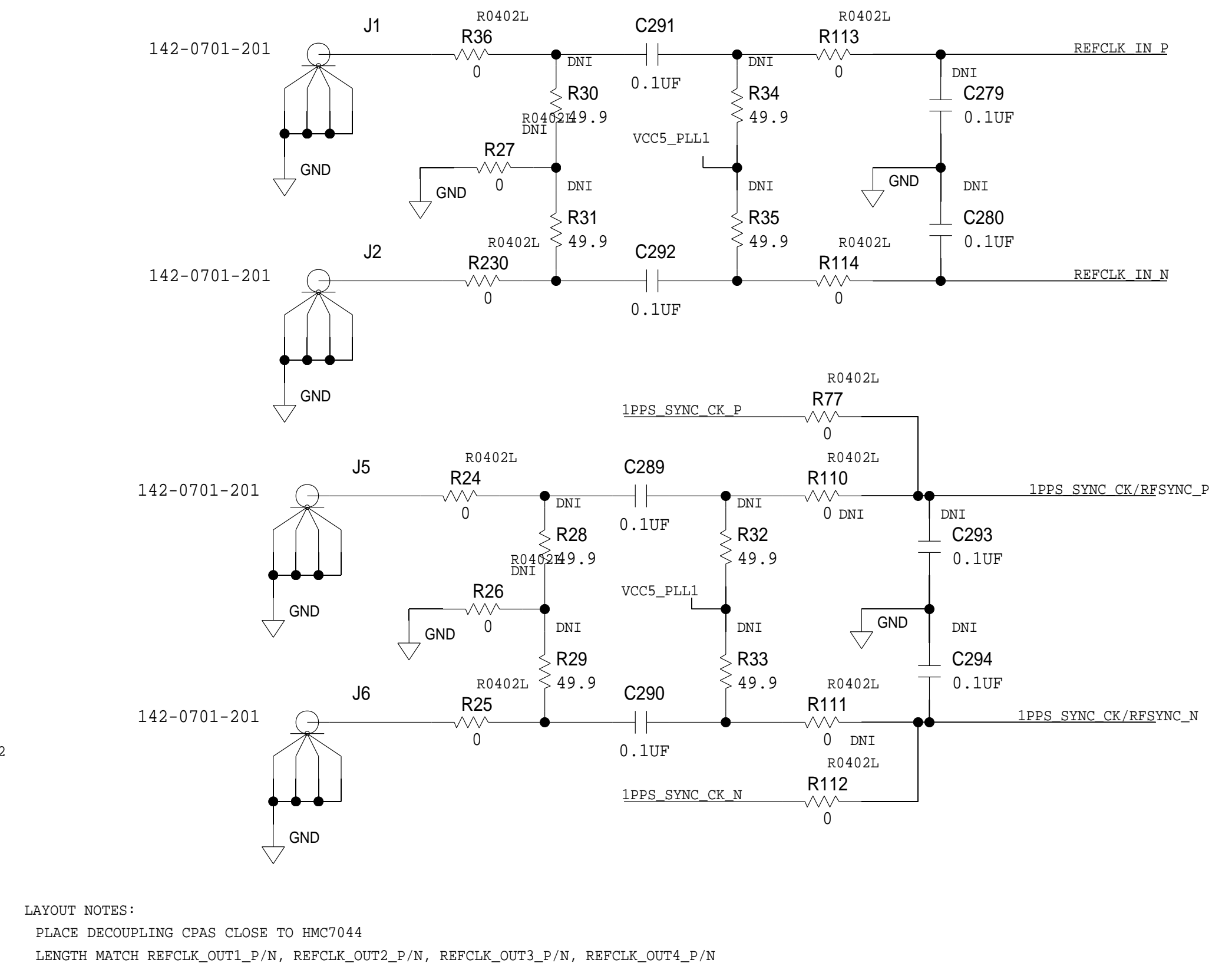
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR
IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS,
OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS
OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY ANALOG DEVICES.

SCHEMATIC			
HW TYPE : Customer Evaluation Z Product(s): ADRV9009 : NA			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_048950	REV C	
PTD ENGINEER C.GOGA	SIZE D	SCALE 1:1	SHEET 11 OF 19

HMC7044



REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

ANALOG
DEVICES

SCHEMATIC

```
HW TYPE      : Customer Evaluation Z
Product(s)   : ADRV9009
              : NA
```

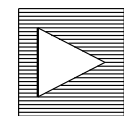
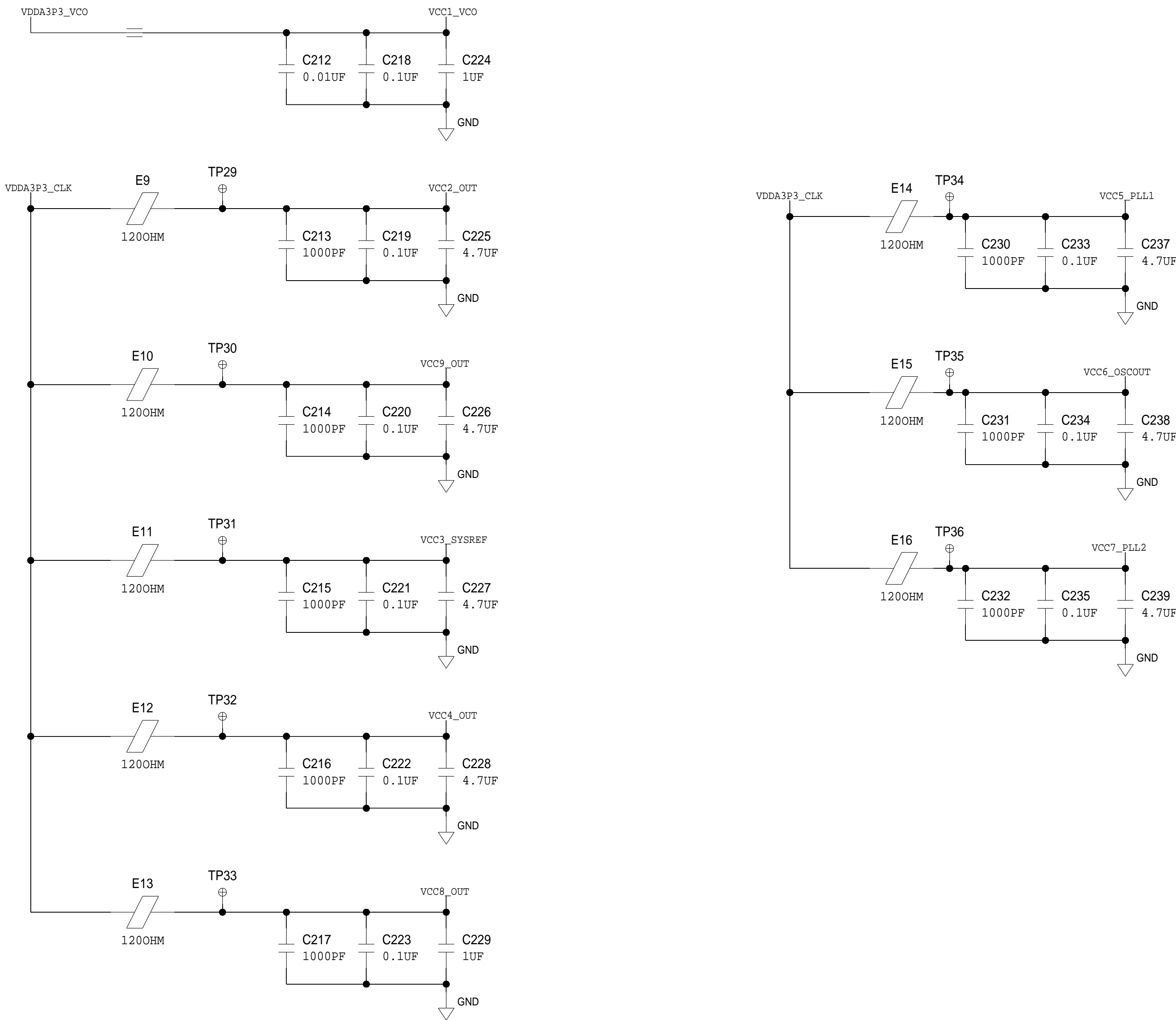
DESIGN VIEW	<DESIGN_VIEW>
PTD ENGINEER	C. GOGA

DRAWING NO.	02_048950
-------------	-----------

SIZE D	SCALE 1:1	SHEET 12 OF 19
------------------	--------------	----------------

THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR
IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS,
OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS
OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENT
OWNED OR CONTROLLED BY OWNED ANALOG DEVICES.

DECOUPLING HMC7044

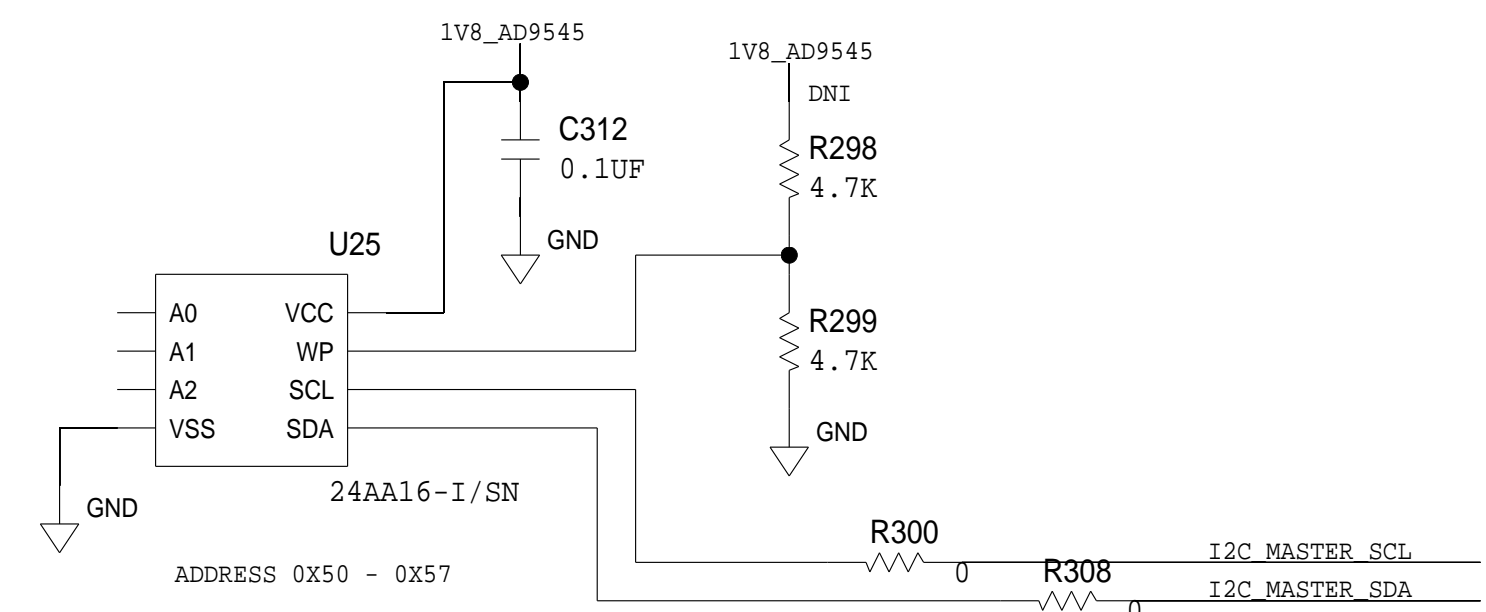
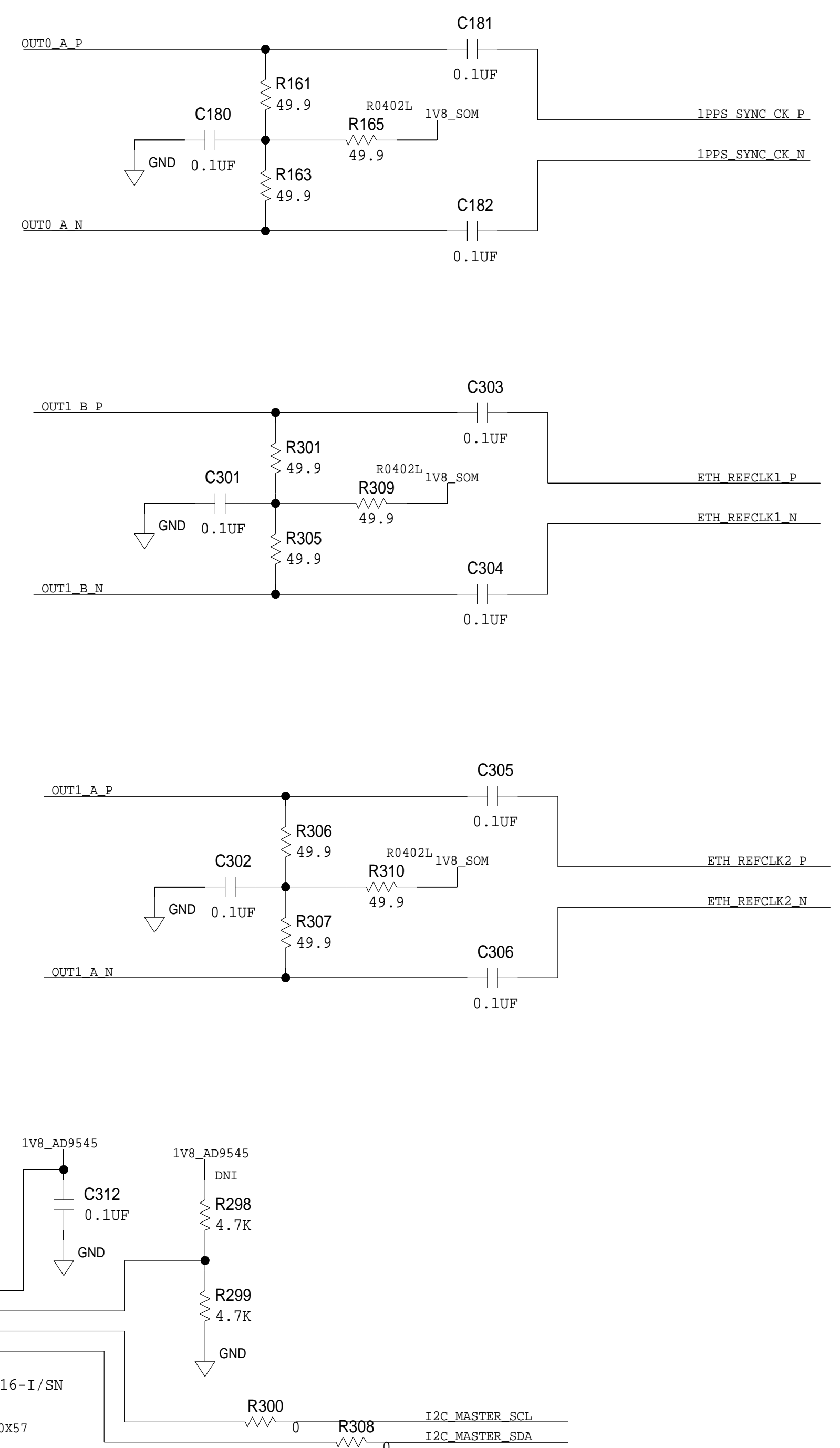


ANALOG
DEVICES

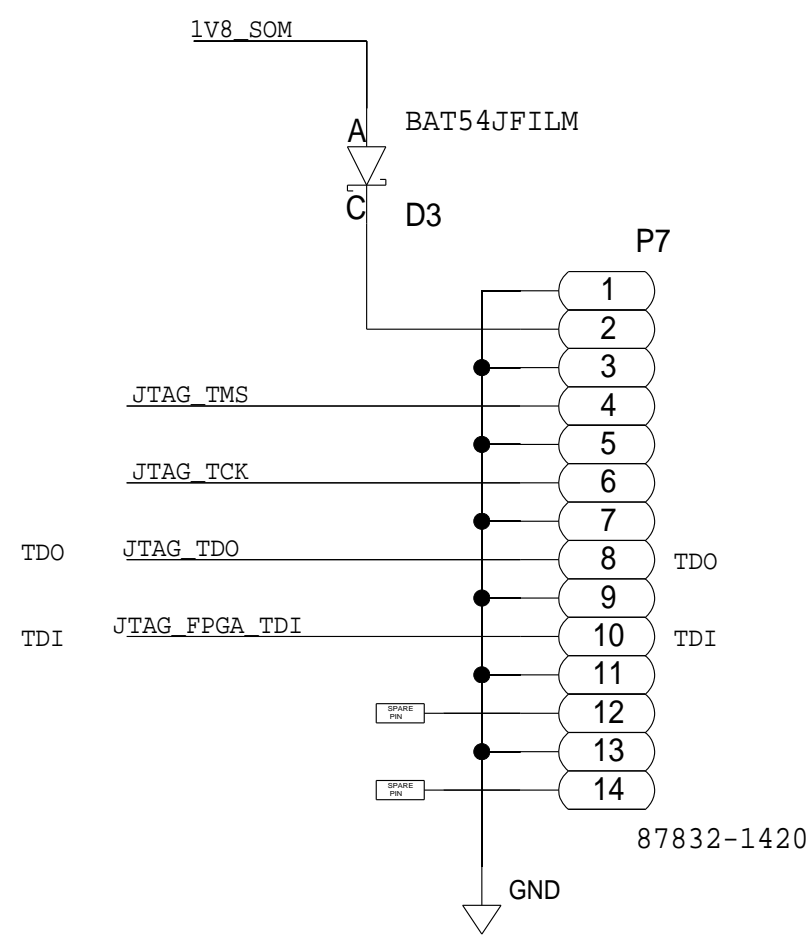
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC.
IT IS NOT TO BE REPRODUCED OR COPIED IN WHOLE OR
IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS,
OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS
OF ANALOG DEVICES.
THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS
OWNED OR CONTROLLED BY ANALOG DEVICES.

SCHEMATIC				
HW TYPE : Customer Evaluation Z Product(s): ADRV9009 : NA				
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_048950		REV C	
PTD ENGINEER C. GOGA	SIZE D	SCALE 1:1	SHEET 13 OF 19	

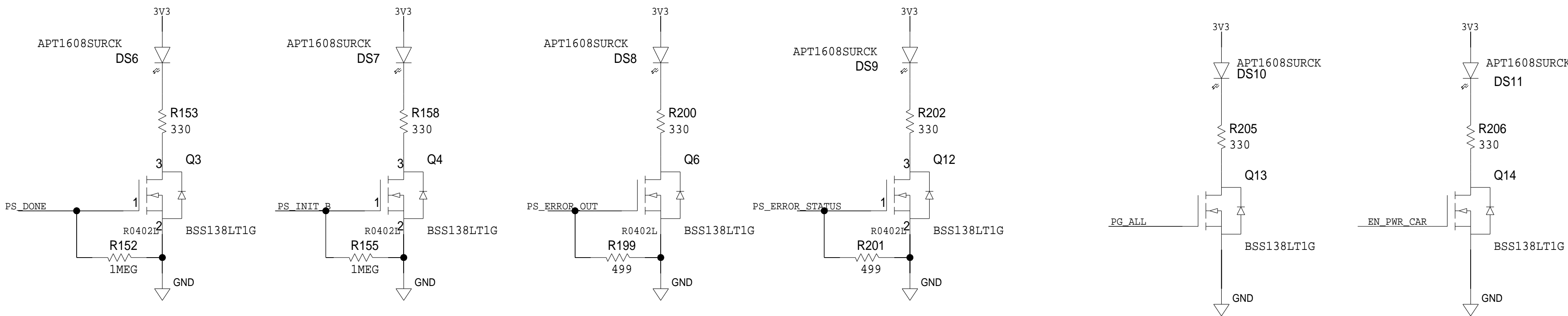
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



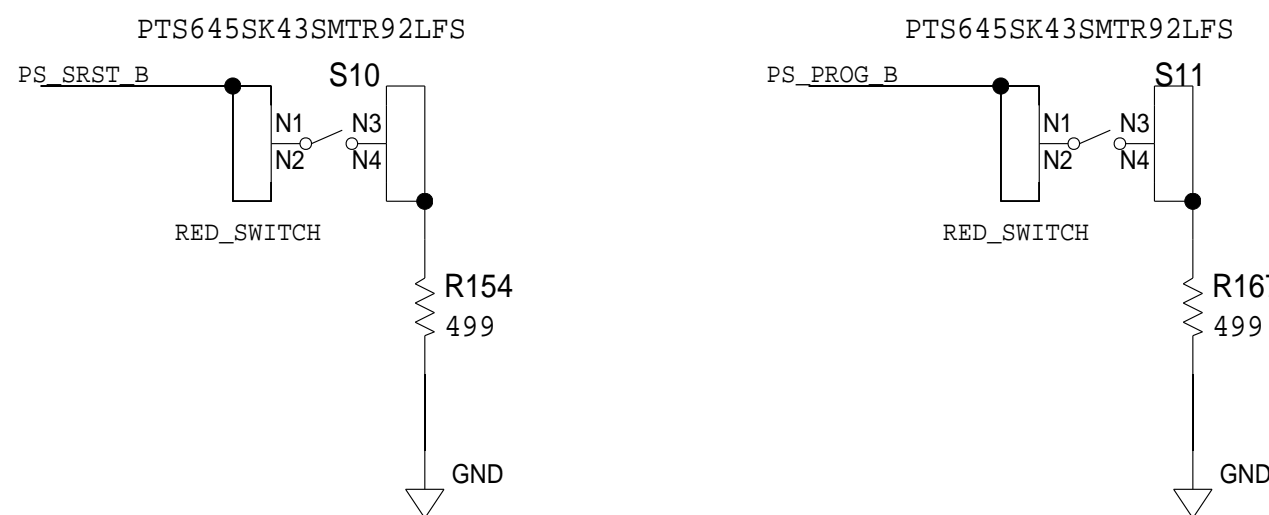
JTAG,ULTRASCALE CONFIGURATION LEDS,PUSH BUTTONS



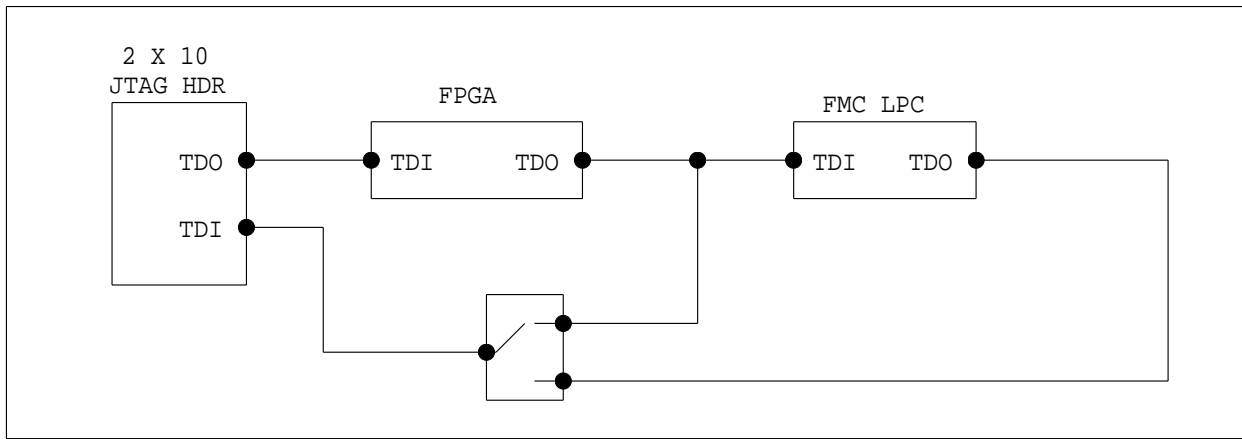
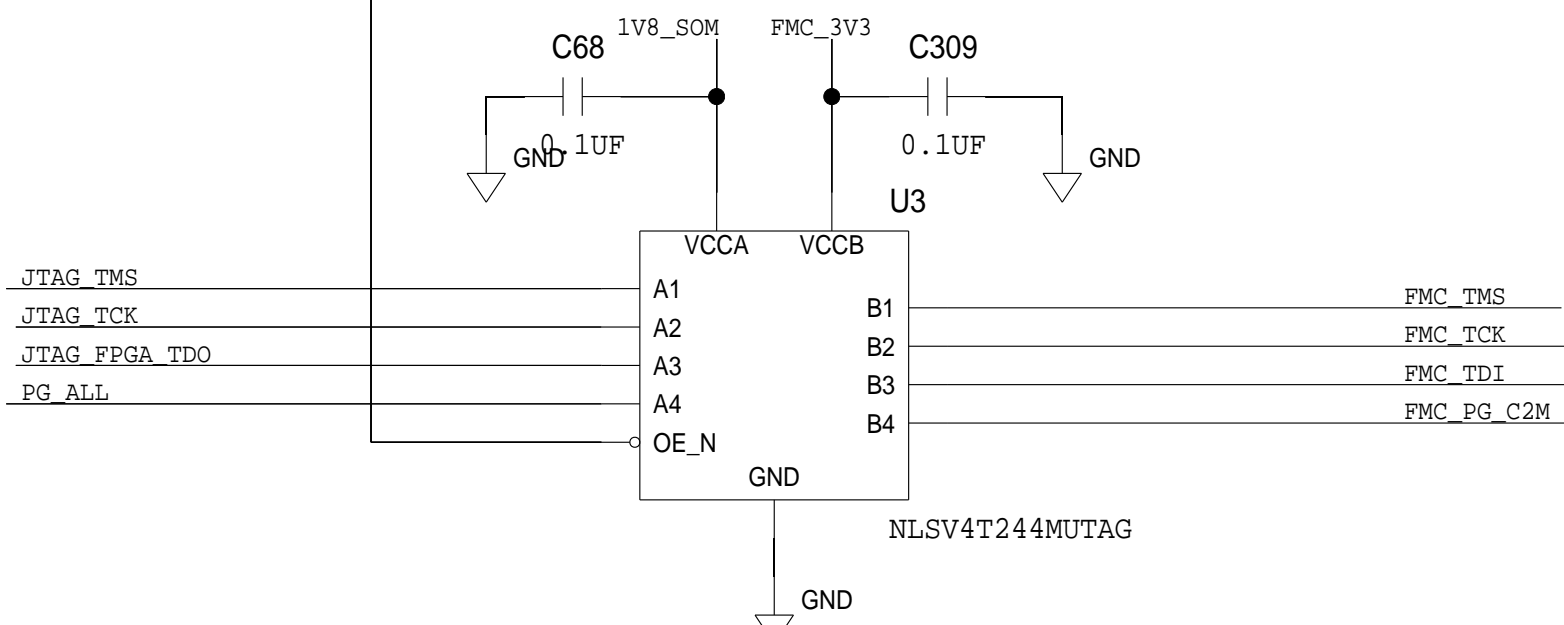
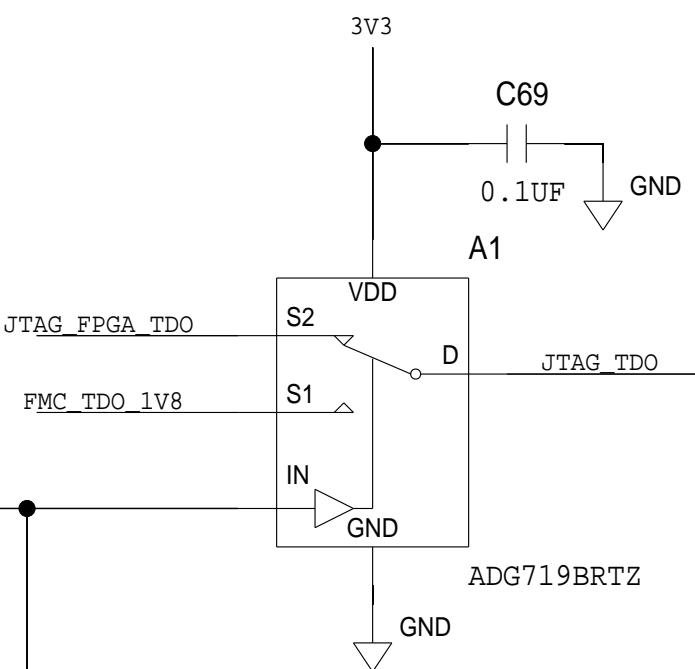
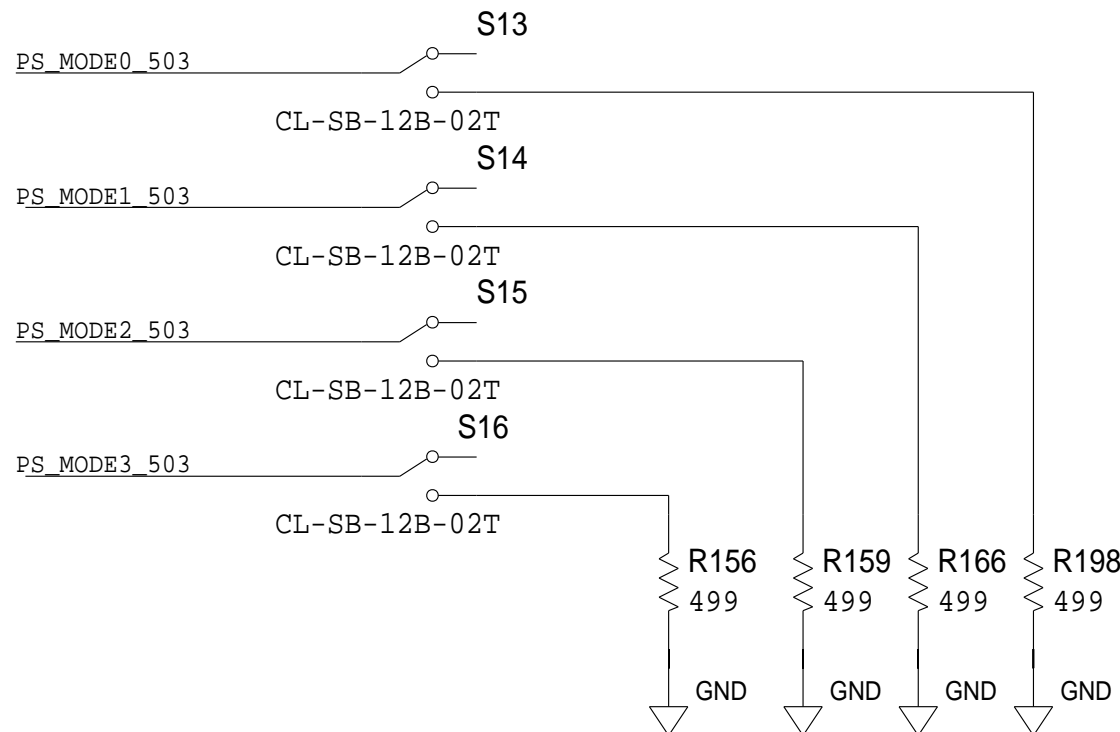
ULTRASCALE CONFIGURATION LEDS



ULTRASCALE RESET BUTTONS

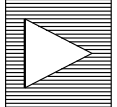


ULTRASCALE MODE SWITCHES



BEFORE LOADING R289, R291, UNLOAD R127, R129 ON THE SOM

REVISIONS				
REV	DESCRIPTION	DATE	APPROVED	

 ANALOG DEVICES		SCHEMATIC		
HW TYPE : Customer Evaluation Z		Product(s): ADRV9009		
DESIGN VIEW		DRAWING NO.		REV
<DESIGN_VIEW>		02_048950		C
PTD ENGINEER		SIZE	SCALE	SHEET 15 OF 19
C. GOGA		D	1:1	

UART, PMOD, USER LEDS, PUSH BUTTONS, SLIDE SWITCHES

