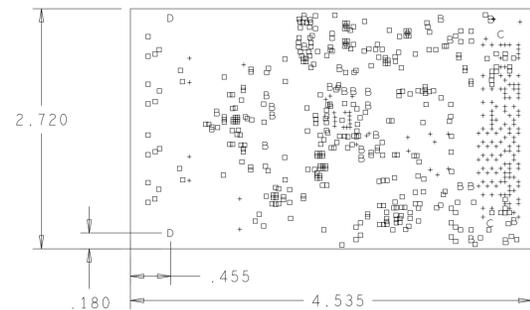


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	14OCT16	P.H.



SPECIFICATIONS:

MATERIALS; ALL LAMINATES AND BONDING MATERIALS SHOULD BE SELECTED FROM IPC-4101 OR IPC-4103, MINIMUM Tg>170degC, Td>300degC, U.L. RATING OF 94 V-0

MATERIAL FAMILY; ISOLA 370HR

CLADDING; EXTERNAL LAYERS .5 OZ. COPPER, OVERPLATE TO 1.5 OZ. INTERNAL SIGNAL LAYERS .5 OZ. COPPER. INTERNAL PLANE LAYERS 1 OZ. COPPER.

NOTE: IF THE LAYER STACKUP CONFLICTS WITH THE ABOVE CLADDING SPECIFICATIONS THEN THE LAYER STACKUP SHALL TAKE PRECEDENCE.

SOLDER MASK; SHALL BE LIQUID PHOTOIMAGEABLE (LPI) APPLIED ON BOTH SIDES OVER BARE COPPER OR GOLD AND SHALL MEET IPC-SM-840 (LATEST REV.) CLASS 3. COLOR BLUE.

SILK SCREEN; SHALL BE PERMANENT NON-CONDUCTIVE EPOXY INK, COLOR: WHITE SYNTHETIC INKJET PRINTING ALLOWED FOR DENSE BOARDS. COLOR: WHITE

SURFACE FINISH; ENIG (Electroless Nickel/Immersion Gold) 100 MICRO INCHES NICKEL/ 3-5 MICRO INCHES GOLD

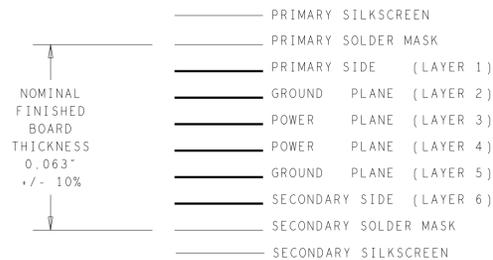
REQUIREMENTS:

- REFER TO IPC-6010 SERIES (LATEST REV.), CLASS 2 FOR FABRICATION UNLESS OTHERWISE SPECIFIED.
- ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00115, (LATEST REVISION.)
- MODIFICATIONS TO THE ARTWORK ARE NOT ALLOWED WITHOUT WRITTEN AUTHORIZATION.
- HOLE PATTERN TOLERANCES FOR UNDIMENSIONED HOLES SHALL BE A DIAMETER OF 0.005 INCHES FROM THEIR TRUE POSITION.
- PLATED HOLE WALL THICKNESS SHALL NOT BE LESS THAN 0.001 INCH MINIMUM AVERAGE, WITH NO READING LESS THAN .0008 BY CROSS SECTION.
- HOLE DIAMETERS APPLY AFTER PLATING.
- FINISHED CONDUCTOR WIDTHS SHALL NOT BE REDUCED FROM THE NOMINAL INDICATED ON THE MASTER PATTERN, BY MORE THAN THE CONDUCTOR THICKNESS.
- MINIMUM DESIGN LINE WIDTH IS .003 INCH.
- MINIMUM DESIGN SPACING IS .005 INCH.
- NON-FUNCTIONAL PAD REMOVAL FROM INNER SIGNAL LAYERS MAY BE PERFORMED AFTER CUSTOMER APPROVAL.
- IF PAD SIZES PROVIDED ARE NOT LARGE ENOUGH TO MAINTAIN ANNULAR RING REQUIREMENT, MFGR. MAY REQUEST APPROVAL TO TEAR DROP PADS TO MAINTAIN ANNULAR RING. (AT PAD TO TRACE INTERSECTION ONLY AND ELECTRICAL INTEGRITY MUST BE MAINTAINED.)
- THIEVING MAY BE ADDED TO COMPENSATE FOR LOW COPPER DENSITY AREAS ON THIS DESIGN ONLY AFTER REVIEW AND APPROVAL FROM THE CUSTOMER:
 - THIEVING TO CARD EDGE, FIDUCIALS, NON-PLATED THROUGH HOLES, ALL OTHER FEATURES TO BE 0.200 INCH MINIMUM.
 - THERE SHALL BE NO THIEVING IN ANY AREAS FREE OF SOLDER MASK OR INTERNAL COPPER PLANES.
- MFGR. TO LEGIBLY ETCH OR STAMP/SCREEN WITH PERMANENT NON-CONDUCTIVE INK ON SECONDARY SIDE IN A CLEAR AREA UNLESS OTHERWISE INDICATED:
 - U.L. CODE-FLAMMABILITY RATING
 - DATE CODE (STAMP).
 - LOT NUMBER
 - MFGR LOGO
 - SUCCESSFUL ELECTRICAL TEST.
- REPAIRS PER IPC-7711/21 (LATEST REV.) ARE ALLOWED.
- THRU VIAS FILLED WITH NON-CONDUCTIVE EPOXY AND PLATED OVER. COPLANAR ON BOTH SIDES WITHIN .001 INCH PRIOR TO FINAL PLATING.
- BOARDS TO BE PANELIZED AND SHIPPED INTACT

INTENTIONAL SHORTS; IF SUPPLIED DATA INCLUDES A FILE "READ_ME.2", THEN INTENTIONAL NET SHORTS EXIST. CUSTOMER REVIEW AND APPROVAL IS REQUIRED IF SUPPLIED DATA REPORTS ANY CONDITION THAT DOES NOT MATCH "READ_ME.2" FILE PROVIDED.

TEST REQUIREMENTS; 100% NETLIST ELECTRICAL VERIFICATION USING CUSTOMER SUPPLIED IPC-D-356 NETLIST FOR OPENS AND SHORTS WHEN "GERBER DATA" IS PROVIDED. THIS VERIFICATION ALSO REQUIRED FOR "ODB++" DATA PER EMBEDDED NETLIST.

6 LAYER STACKUP



CHARACTERISTIC IMPEDANCE = 50 OHMS +/- 10%
ARTWORK LINE WIDTH FOR IMPEDANCE CONTROLLED LINES = 0.008"

HOLE TOLERANCE

UNLESS SPECIFIED
PLATED: +/- .003
NON PLATED: +/- .003

FINISHED HOLES IN MILS				
ALL UNITS ARE IN MILS				
FIGURE	SIZE	PLATED	QTY	TOLERANCE/NOTES
□	10.0	PLATED	369	SEE NOTE 15
+	12.0	PLATED	159	SEE NOTE 15
B	45.0	PLATED	24	
c	50.0	NON-PLATED	2	
d	105.0	NON-PLATED	2	

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		APPROVAL	DATE	HSC DIVISION 804 WOBURN STREET WILMINGTON, MA 01887	
TOLERANCES		TEMPLATE ENGINEER		TITLE FABRICATION	
DECIMALS	FRACTIONS	HARDWARE SERVICES		AD9250 EVALUATION BRD	
.XX	0/16	HARDWARE SYSTEMS		DESIGNER P. ALONGI	
.XXX	0/64	TEST ENGINEER		PRO-ENGINEER A. DESIMONE	
.XXXX	0/256	COMPONENT ENGINEER		CHECKER	
		TEST PROCESS		SCALE 1/1	
		HARDWARE RELEASE		DRAWING NUMBER 09-044691	
FINISH			01/15	SIZE D	REV A
DO NOT SCALE DWG				FSCM NO 24355	SHEET 1 OF 1