

REV	DESCRIPTION	DATE	APPROVED
1			

REV	DESCRIPTION	DATE	APPROVED
2			

REV	DESCRIPTION	DATE	APPROVED
3			

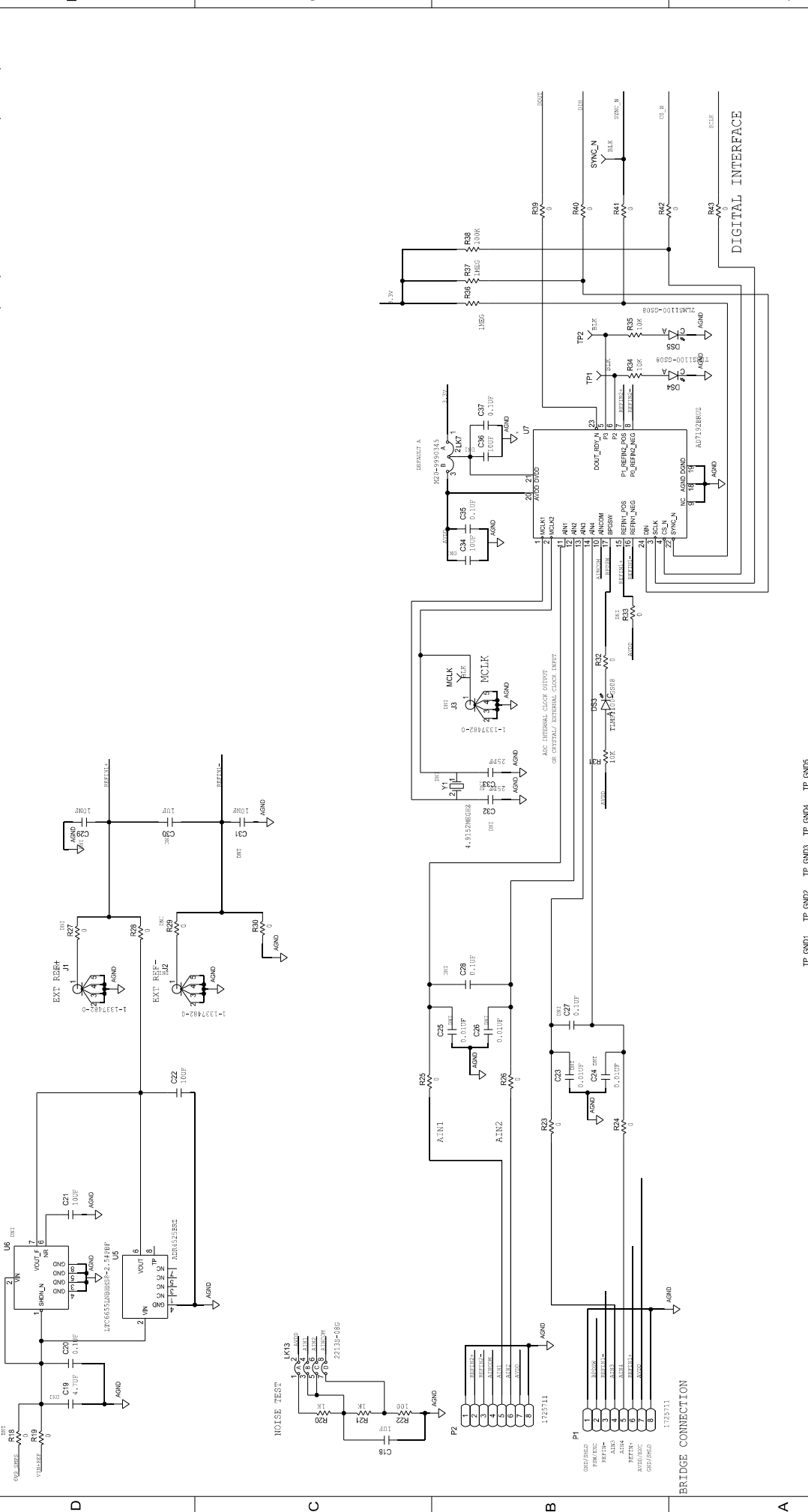
REV	DESCRIPTION	DATE	APPROVED
4			

REV	DESCRIPTION	DATE	APPROVED
5			

REV	DESCRIPTION	DATE	APPROVED
6			

REV	DESCRIPTION	DATE	APPROVED
7			

REV	DESCRIPTION	DATE	APPROVED
8			



ANALOG DEVICES

REV: 02-09-04-02
 PRODUCT: AD7192
 SCALE: 1:1
 SHEET: 3 OF 4

SCHEMATIC

REV: 02-09-04-02
 PRODUCT: AD7192
 SCALE: 1:1
 SHEET: 3 OF 4

DESIGN VIEW: <C85 (REV_7)85>
 PFD ENGINEER: V. JEDRUSIAK

TP_GND1 TP_GND2 TP_GND3 TP_GND4 TP_GND5

BRIDGE CONNECTION

DIGITAL INTERFACE

