MOTOR2 DRIVE STAGE

BOOTSTRAPPED ISOLATED GATE DRIVERS

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PRODUCT(S): AD7403: MOTOR CONTROL BD - LOW VOLTAGE DRIVE EBZ

SCHEMATIC

PTD ENGINEER
DESIGN VIEW
REV DATE

REV DESCRIPTION DATE APPROVED

REVISIONS

SCALE
1:1

SHEET
OF ANALOG DEVICES

PRODUCT
PRODUCT

REV

D"
OVER-CURRENT & INRUSH PROTECTION

0.11V @ 22A

REVERSE POLARITY PROTECTION

OVC_OUT2 = IN_CURRENT2 < REF_OVC; OVC_OUT2: 0 - INPUT CURRENT BELOW OVERCURRENT TRIP POINT
1 - INPUT CURRENT ABOVE OVERCURRENT TRIP POINT
OVC_CT2 = !OVC_OUT2

OVC_CT1 = !OVC_OUT1

OVC_CT = OVC_CT1 & OVC_CT2

1 - GOOD, SYSTEM POWER ON
0 - FAULT, SYSTEM POWER OFF

INR_CT2 = IN_CURRENT2 < REF_INR; INR_CT2: 1 - INPUT CURRENT BELOW INRUSH TRIP POINT
0 - INPUT CURRENT ABOVE INRUSH TRIP POINT

INR_CT1 = IN_CURRENT1 < REF_INR; INR_CT1: 1 - INPUT CURRENT BELOW INRUSH TRIP POINT
0 - INPUT CURRENT ABOVE INRUSH TRIP POINT

PWR_CT1 = INR_CT1 & OVC_CT; PWR_CT1: 0 - FAULT, SYSTEM POWER OFF
1 - GOOD, SYSTEM POWER ON

PWR_CT2 = INR_CT2 & OVC_CT; PWR_CT2: 0 - FAULT, SYSTEM POWER OFF
1 - GOOD, SYSTEM POWER ON