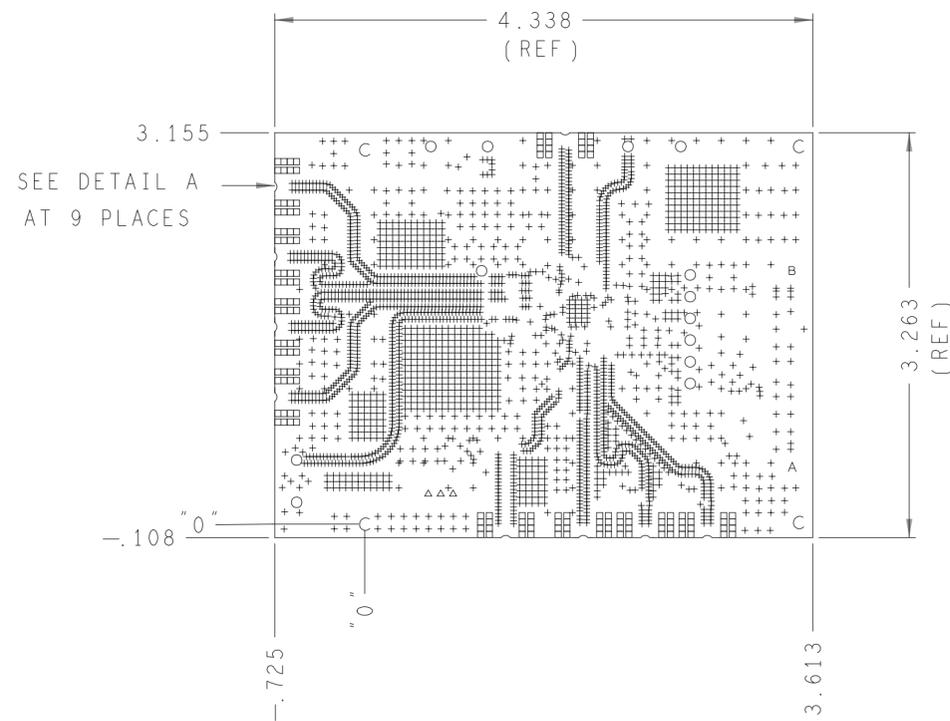
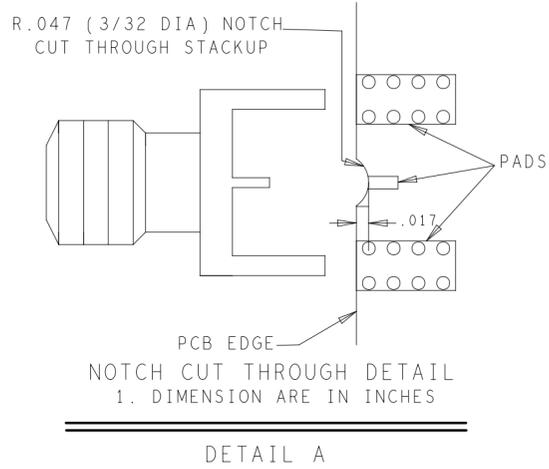


REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	08SEP21	S. CABATAN
A1	AS PER ECR-106427	10JAN22	S. CABATAN



HOLE TOLERANCE
 UNLESS SPECIFIED
 PLATED: +/-3MILS
 NON PLATED: +/-2MILS

DRILL CHART: TOP to BOTTOM				
FINISHED HOLES IN MILS				
FIGURE	SIZE	PLATED	QTY	TOLERANCE/NOTES
+	10.0	PLATED	2478	DIA MAX
□	16.0	PLATED	144	DIA MAX
△	45.0	PLATED	3	
○	63.0	PLATED	13	
A	28.0	NON-PLATED	1	
B	43.0	NON-PLATED	1	
C	125.0	NON-PLATED	4	

PRIMARY SIDE

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES DECIMALS FRACTIONS ANGLES .XX -.010 --1/32 --2 .XXX -.005 .XXXX -.0050	APPROVAL TEMPLATE ENGINEER N/A	DATE N/A	 WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887			
	HARDWARE SERVICES M. VALE	08SEP21				
MATERIAL	HARDWARE SYSTEMS N/A	N/A	TITLE FABRICATION ADRF6850-EVALZ CUSTOMER EVAL BOARD Z			
	TEST ENGINEER N/A	N/A				
	COMPONENT ENGINEER M. YAN	08SEP21				
	TEST PROCESS N/A	N/A				
FINISH	HARDWARE RELEASE K. JABATAN	08SEP21	SIZE C	FSCM NO 24355	DRAWING NUMBER 09-068323	REV A1
	DESIGNER M. VALE	08SEP21	SCALE 1/1		SHEET 1 OF 2	
	PTD ENGINEER S. CABATAN	08SEP21				
DO NOT SCALE DWG	CHECKER N/A	N/A				

CUSTOMER EVAL BOARD Z

4 | 3 | 2 | 1

SPECIFICATIONS:

ROHS COMPLIANCE NOTE: HOMOGENOUS MATERIALS IN THIS BOARD SHALL BE COMPLIANT THE EU RoHS DIRECTIVE 2002/95/EC

MATERIALS: ALL LAMINATES AND BONDING MATERIALS SHOULD BE SELECTED FROM IPC-4101 OR IPC-4103, MINIMUM Tg>170degC, Td>300degC, U.L. RATING OF 94 V-0

MATERIAL FAMILY: FR4 OR ISOLA370HR OR S1000-2 OR IT180 OR EQUIVALENT

CLADDING: EXTERNAL LAYERS .5 OZ. COPPER, OVERPLATE TO 1.5 OZ. INTERNAL PLANE LAYERS 1 OZ. COPPER.

NOTE: IF THE LAYER STACKUP CONFLICTS WITH THE ABOVE CLADDING SPECIFICATIONS THEN THE LAYER STACKUP SHALL TAKE PRECEDENCE.

SOLDER MASK: SHALL BE LIQUID PHOTOIMAGEABLE (LPI) APPLIED ON BOTH SIDES OVER BARE COPPER OR GOLD AND SHALL MEET IPC-SM-840 (LATEST REV.) CLASS 3. COLOR GREEN.

SILK SCREEN: SHALL BE PERMANENT NON-CONDUCTIVE EPOXY INK, COLOR: WHITE SYNTHETIC INKJET PRINTING ALLOWED FOR DENSE BOARDS, COLOR: WHITE

SURFACE FINISH: ENIG (ELECTROLESS NICKEL/IMMERSION GOLD) PER IPC-4552 LATEST REVISION

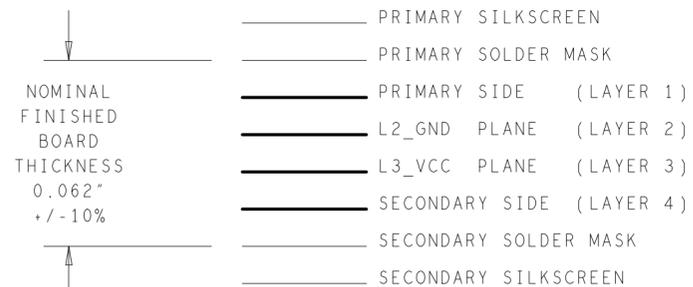
INTENTIONAL SHORTS: IF SUPPLIED DATA INCLUDES A FILE "READ_ME.2". THEN INTENTIONAL NET SHORTS EXIST. CUSTOMER REVIEW AND APPROVAL IS REQUIRED IF SUPPLIED DATA REPORTS ANY CONDITION THAT DOES NOT MATCH "READ_ME.2" FILE PROVIDED.

TEST REQUIREMENTS: 100% NETLIST ELECTRICAL VERIFICATION USING CUSTOMER SUPPLIED IPC-D-356 NETLIST FOR OPENS AND SHORTS WHEN "GERBER DATA" IS PROVIDED. THIS VERIFICATION ALSO REQUIRED FOR "ODB++" DATA PER EMBEDDED NETLIST.

REQUIREMENTS:

1. REFER TO IPC-6010 SERIES (LATEST REV.), CLASS 2 FOR FABRICATION UNLESS OTHERWISE SPECIFIED.
2. ACCEPTABILITY PER ANALOG DEVICES, INC. SPECIFICATION TST00115. (LATEST REVISION.)
3. MODIFICATIONS TO THE ARTWORK ARE NOT ALLOWED WITHOUT WRITTEN AUTHORIZATION.
4. HOLE PATTERN TOLERANCES FOR UNDIMENSIONED HOLES SHALL BE A DIAMETER OF 0.005 INCHES FROM THEIR TRUE POSITION.
5. PLATED HOLE WALL THICKNESS SHALL NOT BE LESS THAN 0.001 INCH MINIMUM AVERAGE, WITH NO READING LESS THAN .0008 BY CROSS SECTION.
6. HOLE DIAMETERS APPLY AFTER PLATING.
7. FINISHED CONDUCTOR WIDTHS SHALL NOT BE REDUCED FROM THE NOMINAL INDICATED ON THE MASTER PATTERN, BY MORE THAN THE CONDUCTOR THICKNESS.
8. MINIMUM DESIGN LINE WIDTH IS .012 INCH.
9. MINIMUM DESIGN SPACING IS .006 INCH.
10. ~~NON-FUNCTIONAL PAD REMOVAL FROM INNER SIGNAL LAYERS MAY BE PERFORMED AFTER CUSTOMER APPROVAL.~~
11. IF PAD SIZES PROVIDED ARE NOT LARGE ENOUGH TO MAINTAIN ANNULAR RING REQUIREMENT, MFGR. MAY REQUEST APPROVAL TO TEAR DROP PADS TO MAINTAIN ANNULAR RING. (AT PAD TO TRACE INTERSECTION ONLY AND ELECTRICAL INTEGRITY MUST BE MAINTAINED.)
12. THIEVING MAY BE ADDED TO COMPENSATE FOR LOW COPPER DENSITY AREAS ON THIS DESIGN ONLY AFTER REVIEW AND APPROVAL FROM THE CUSTOMER:
 - A. THIEVING TO CARD EDGE, FIDUCIALS, NON-PLATED THROUGH HOLES, ALL OTHER FEATURES TO BE 0.200 INCH MINIMUM.
 - B. THERE SHALL BE NO THIEVING IN ANY AREAS FREE OF SOLDER MASK OR INTERNAL COPPER PLANES.
13. MFGR. TO LEGIBLY ETCH OR STAMP/SCREEN WITH PERMANENT NON-CONDUCTIVE INK ON SECONDARY SIDE IN A CLEAR AREA UNLESS OTHERWISE INDICATED;
 - A. U.L. CODE-FLAMMABILITY RATING
 - B. DATE CODE (STAMP)
 - C. LOT NUMBER
 - D. MFGR LOGO
 - E. SUCCESSFUL ELECTRICAL TEST.
14. REPAIRS PER IPC-7711/21 (LATEST REV.) ARE ALLOWED.
15. BOARDS TO BE SHIPPED IN ARRAY AND KEEP INTACT. PANEL TO BE SUBJECTED TO CUSTOMER'S APPROVAL. PANELED SOLDER PASTE STENCIL SHOULD BE SENT TO THE CUSTOMER.

4 LAYER STACKUP



IMPEDANCE TABLE

NOTE: ALL IMPEDANCE TOLERANCE +/-10%
CPWG = COPLANAR WAVEGUIDE

LAYER	IMPEDANCE	REFERENCE	LINE WIDTH	SPACE
L1	50 OHMS CPW	L2	15MILS	6MILS

PRIMARY SIDE

		WWM DIVISION 804 WOBURN STREET WILMINGTON, MA 01887	
		SIZE C	FSCM NO 24355
SCALE 1/1		SHEET 2 OF 2	

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