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JUMPER TABLE		
JP#	ON	OFF
1		
2		
3		
4		
5		

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

RELAY CONTROL CHART

CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR

* SEE ASSEMBLY INSTRUCTIONS

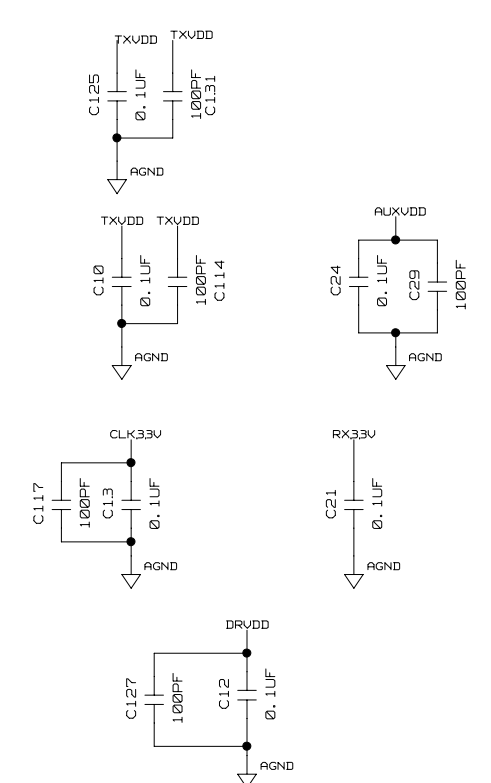
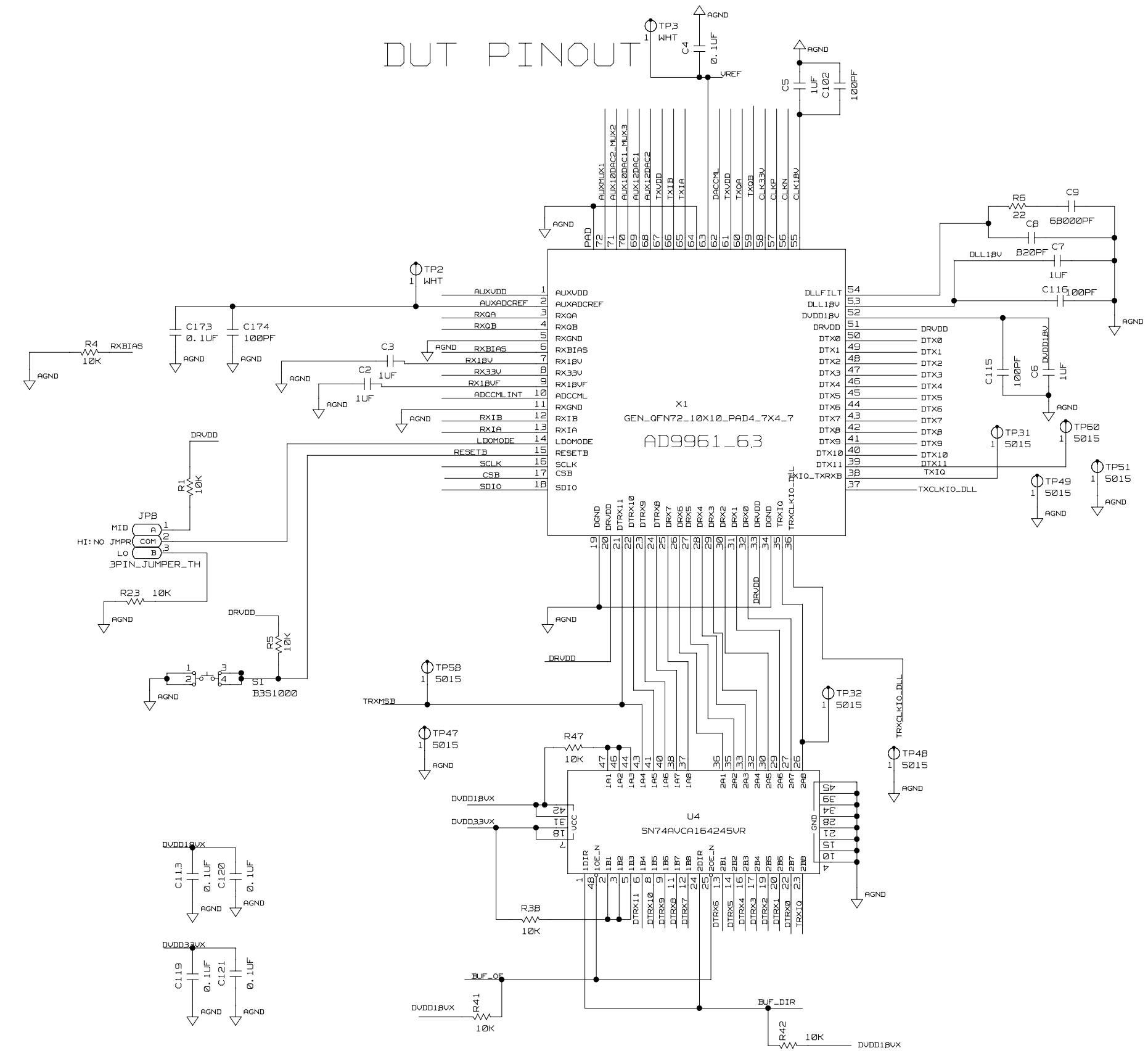
JULY 21 2010

P. O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER

TEMPLATE ENGINEER P. ALONGI	DATE 09.25.2009	<h1>SCHEMATIC</h1>		
HARDWARE SERVICES -				
HARDWARE SYSTEMS -		AD9963 SOLDER EVAL BRD AD9961/AD9963		
TEST ENGINEER -		<PRODUCT_1>		
COMPONENT ENGINEER -		<DRAWING_TITLE1> <DRAWING_TITLE2>		
TEST PROCESS -		<DRAWING_TITLE3>		
HARDWARE RELEASE -		<DRAWING_TITLE4> <DRAWING_TITLE5>		
DESIGNER -		MASTER PROJECT TEMPLATE <DRAWING_TITLE5>	TESTER TEMPLATE	DRAWING NO. 09010
PTD ENGINEER <PTD_ENGINEER>		UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		REV. B
CHECKER -		TOLERANCES		SIZE D
		DECIMALS X.XX +0.010 X.XXX +0.005	FRACTIONS +-1/32	ANGLES +-2
		SCALE <SCALE>	CODE ID NO.	SHEET 1 OF 10

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

DUT PINOUT



	SCHEMATIC		
	AD9963 SOLDER EVAL BRD AD9961/AD9963 <PRODUCT_1>		
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	PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE <SCALE>

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

ANALOG POWER SUPPLIES

DIGITAL POWER SUPPLIES

D

D

C

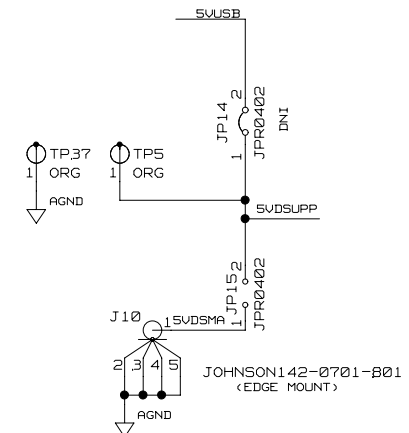
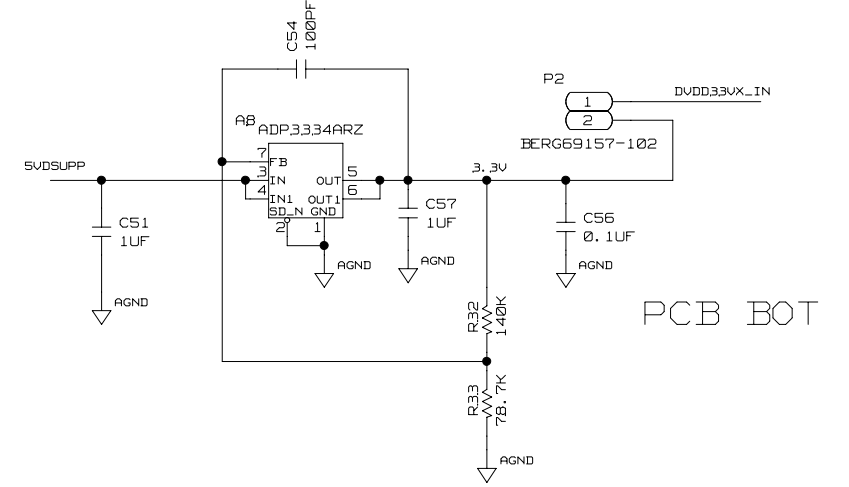
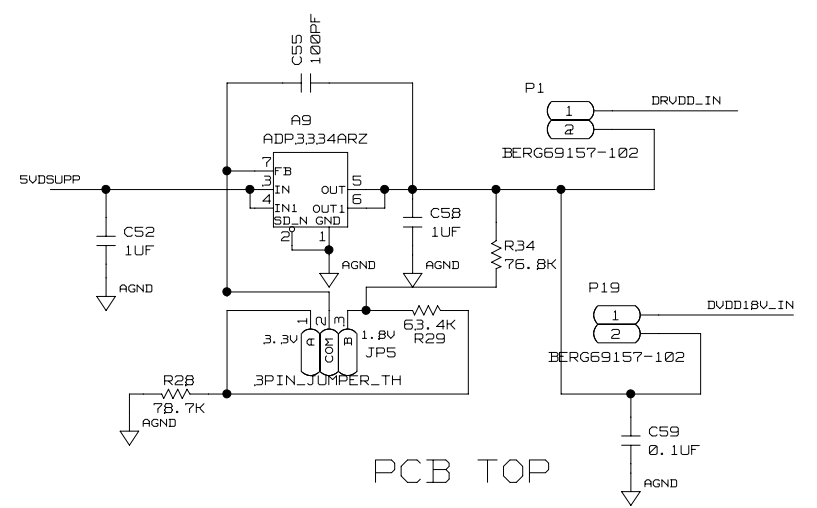
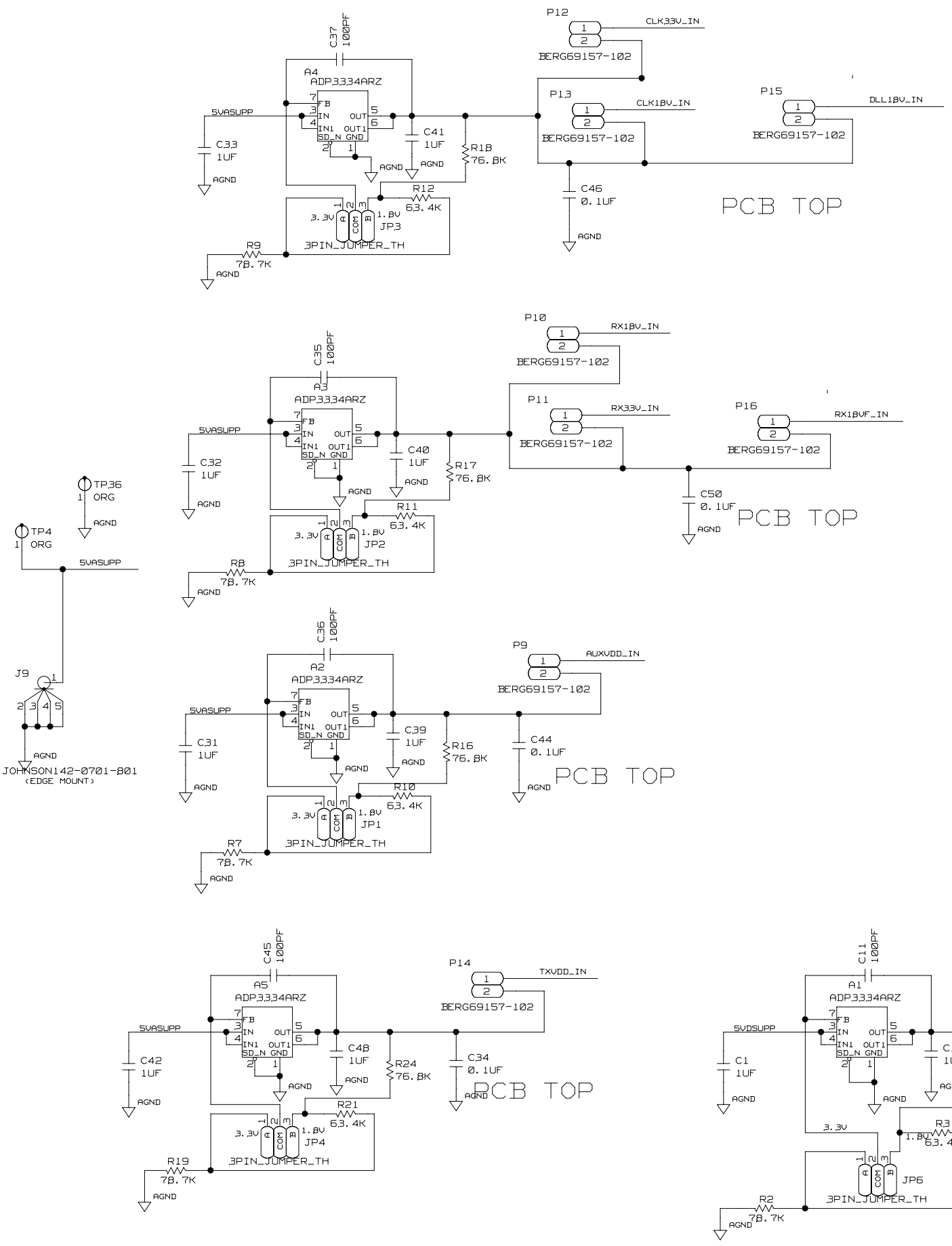
C

B

B

A

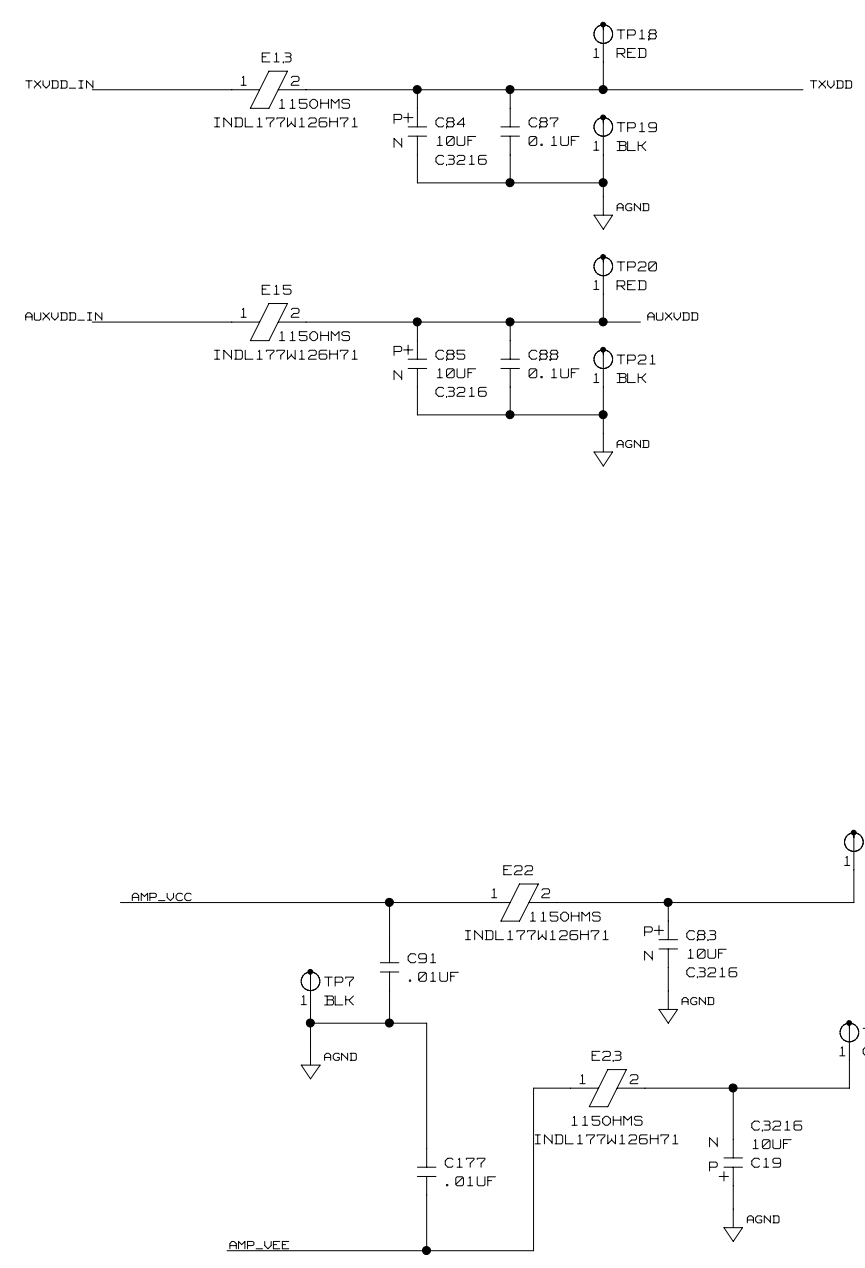
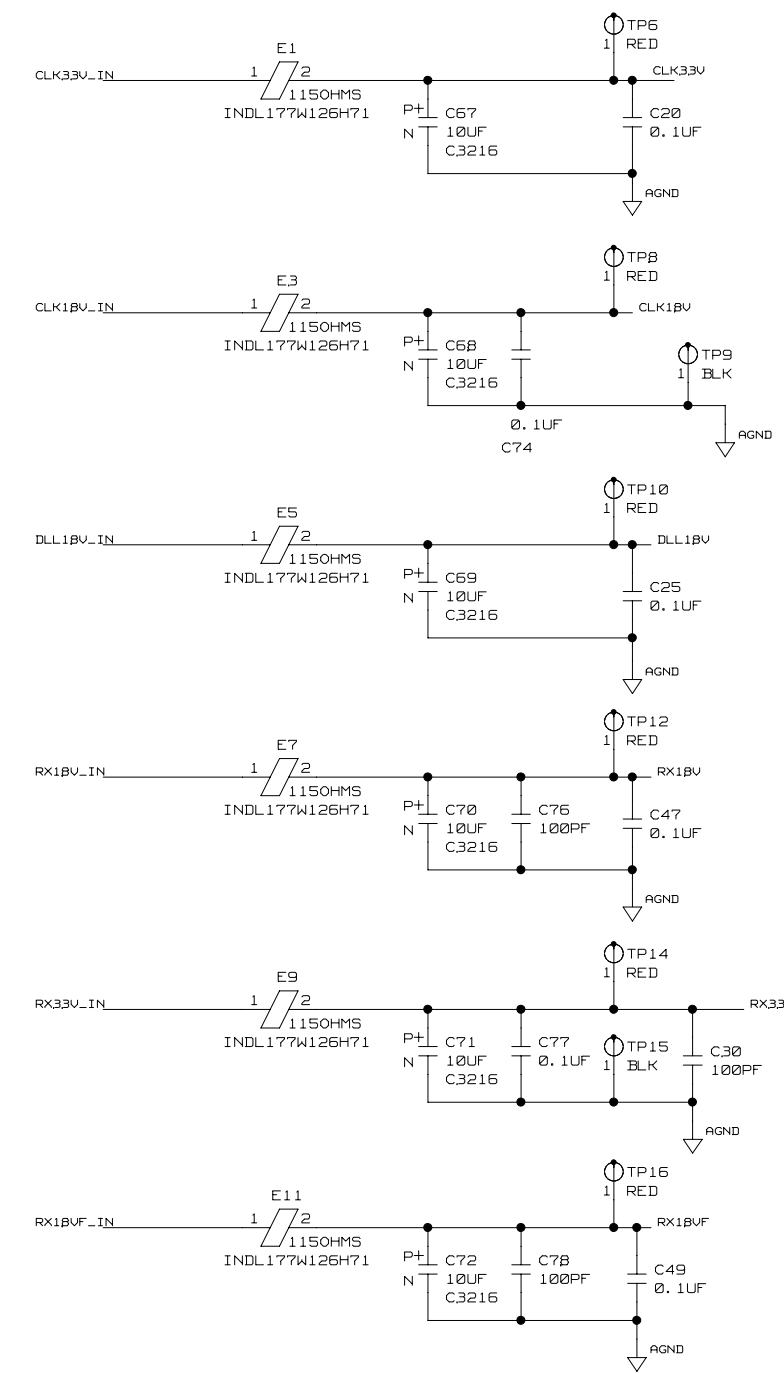
A



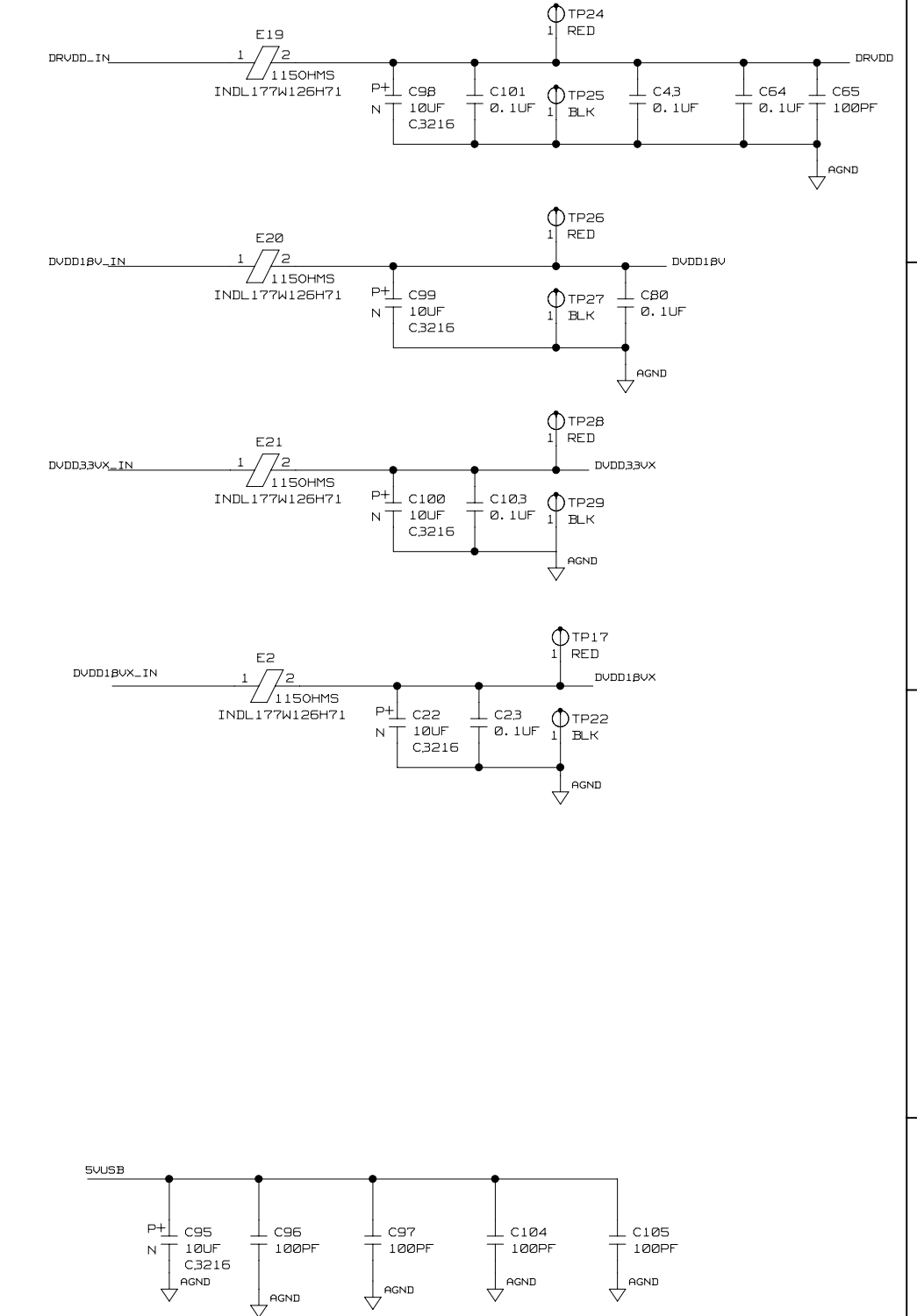
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DESIGN VIEW <DESIGN-VIEW>	DRAWING NO. 09010	REV B	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE <SCALE>	SHEET 3 OF 10

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

ANALOG SUPPLY FILTERS



DIGITAL SUPPLY FILTERS



	SCHEMATIC		
	AD9963 SOLDER EVAL BRD		
	AD9961/AD9963		
	<PRODUCT_1>		
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PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE <SCALE>	SHEET 4 OF 10

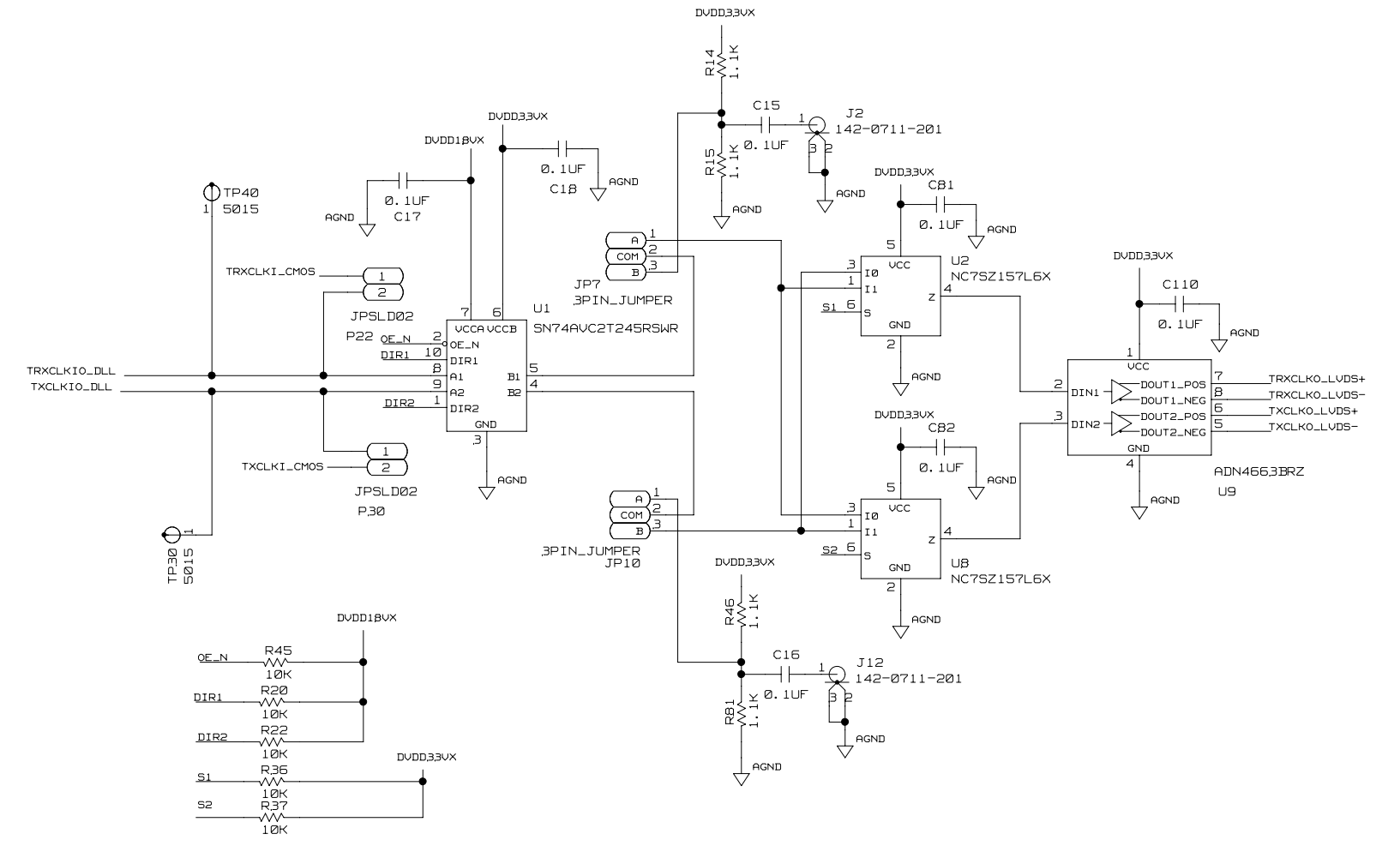
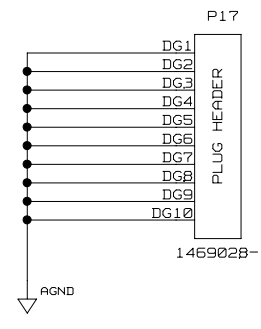
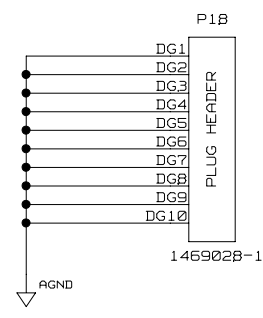
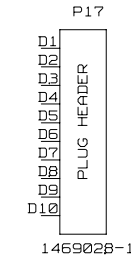
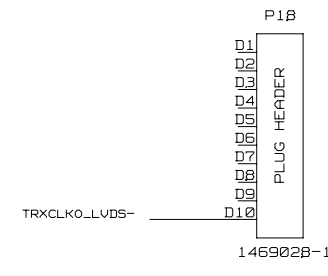
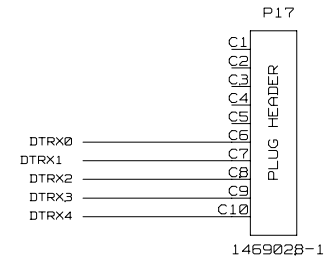
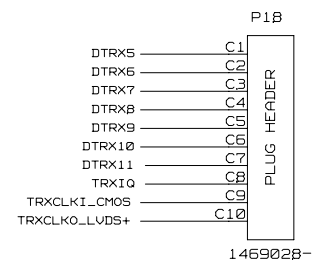
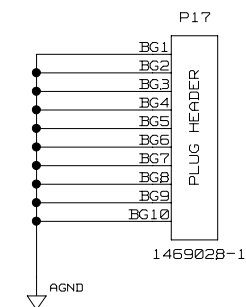
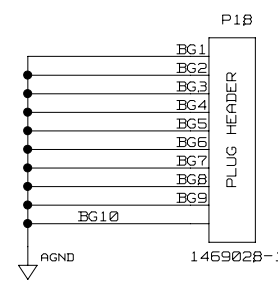
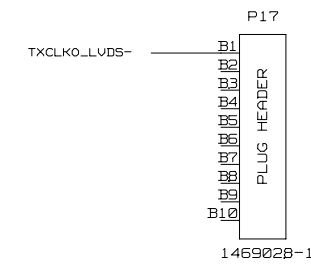
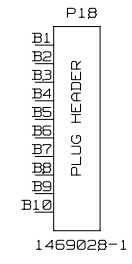
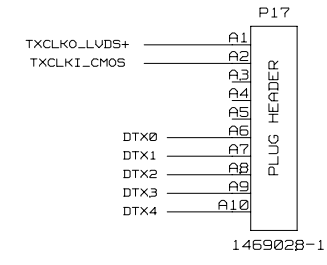
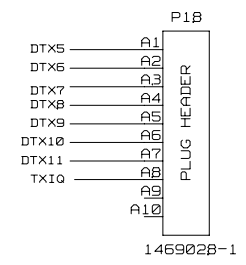
REVISIONS

REV	DESCRIPTION	DATE	APPROVED

TXRX INTERFACE
(TYCO PLUG CONN)

TXONLY INTERFACE
(TYCO PLUG CONN)

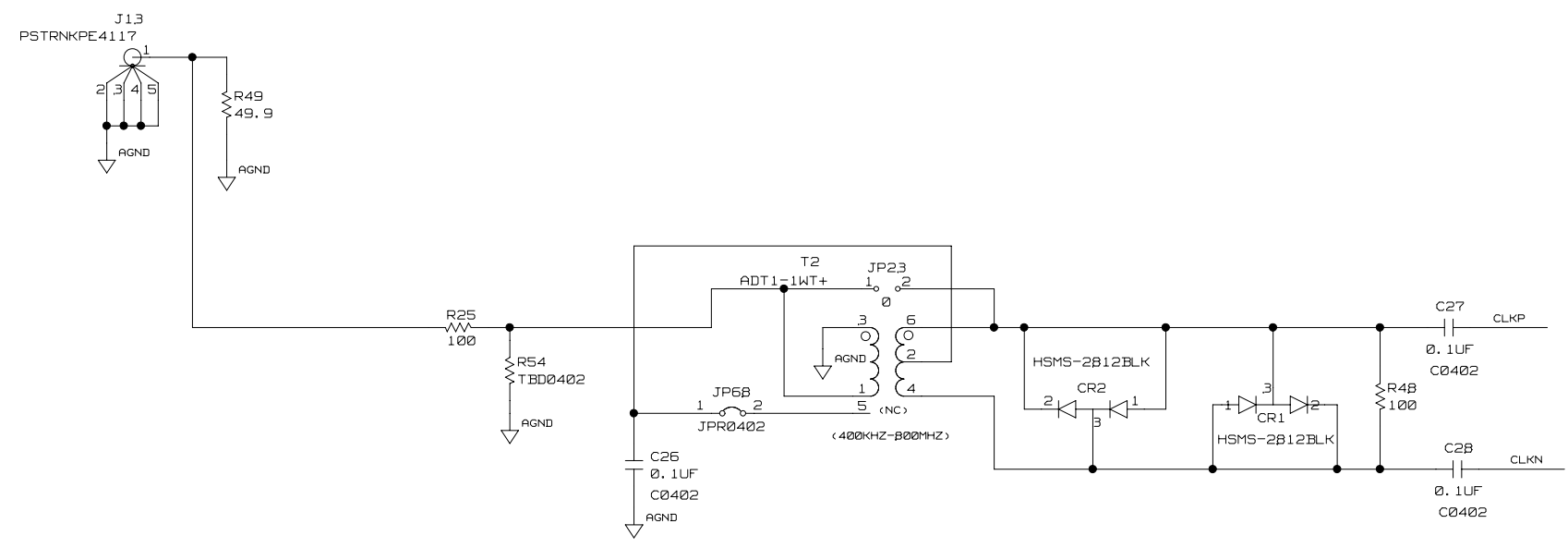
DATA INTERFACE/DATA CLOCKS



	SCHEMATIC		
	AD9963 SOLDER EVAL BRD AD9961/AD9963 <PRODUCT_1>		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 09010	REV B
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE <SCALE>	SHEET 5 OF 10

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

CLOCKING CIRCUITS



	SCHEMATIC		
	AD9963 SOLDER EVAL BRD AD9961/AD9963 <PRODUCT_1>		
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	<DESIGN_VIEW>	09010	B
PTD ENGINEER	SIZE	SCALE	SHEET 6 OF 10
<PTD_ENGINEER>	D	<SCALE>	E

ADC INPUT DRIVING CIRCUITS

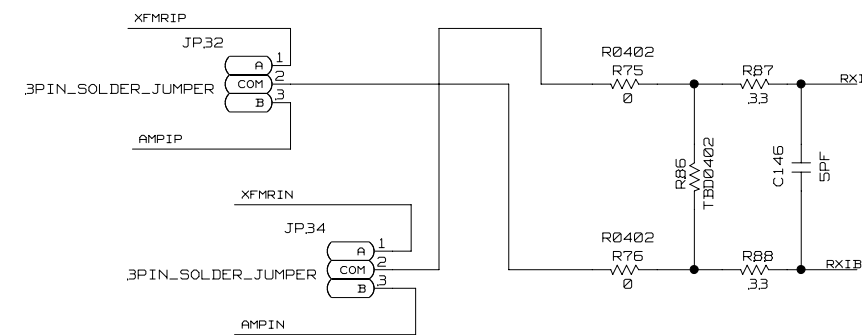
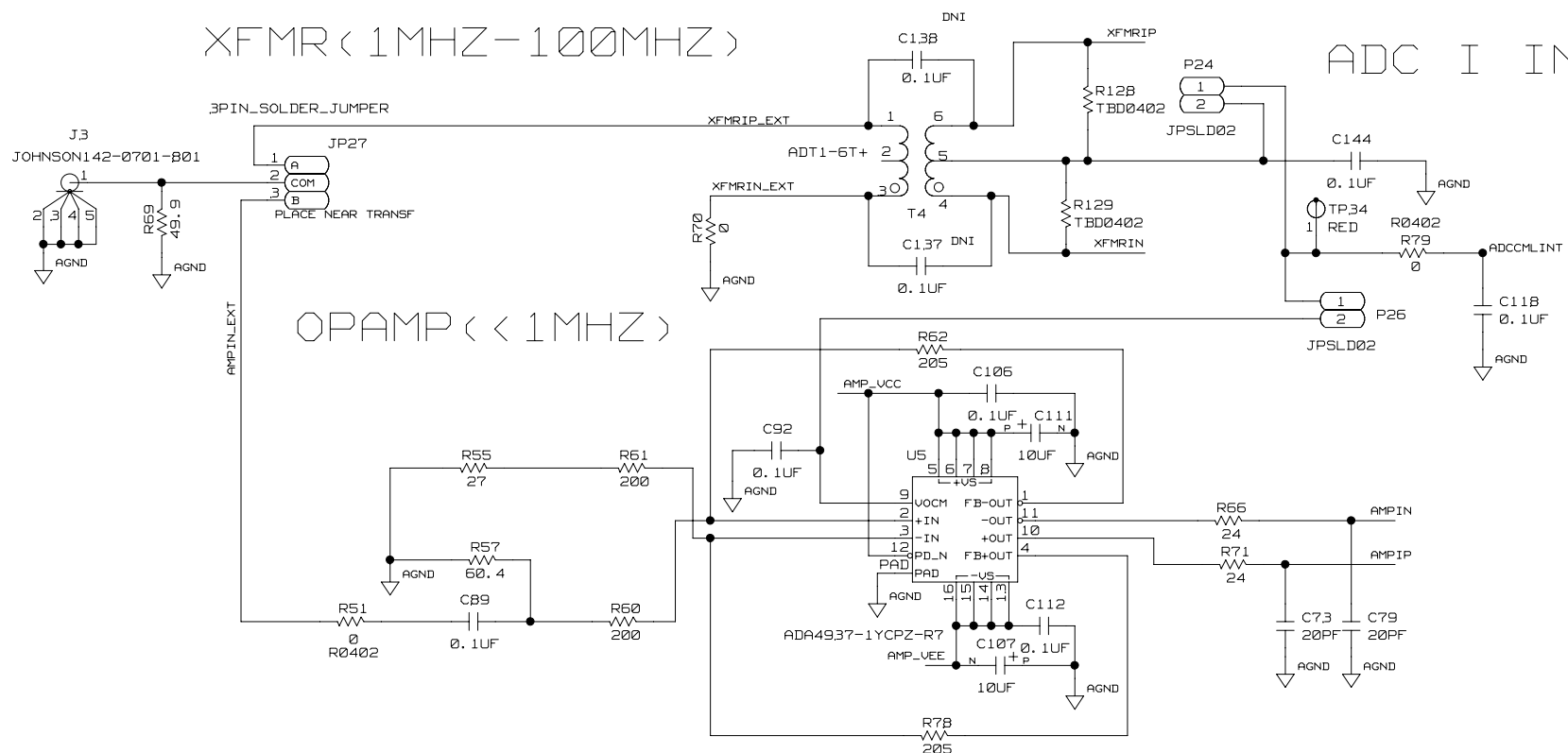
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

XFMR < 1MHZ - 100MHZ >

ADC I INPUT

OPAMP < < 1MHZ >

PCB TOP

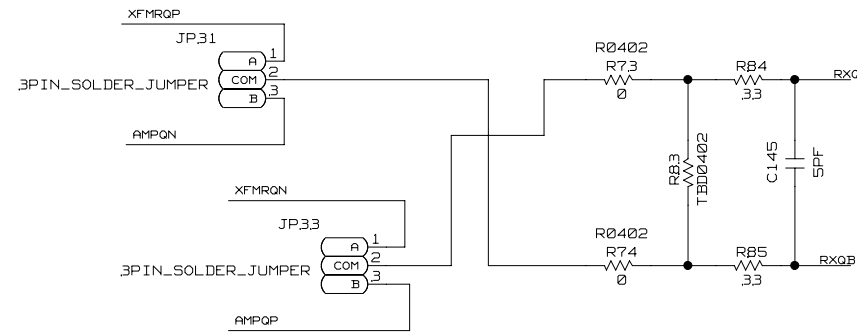
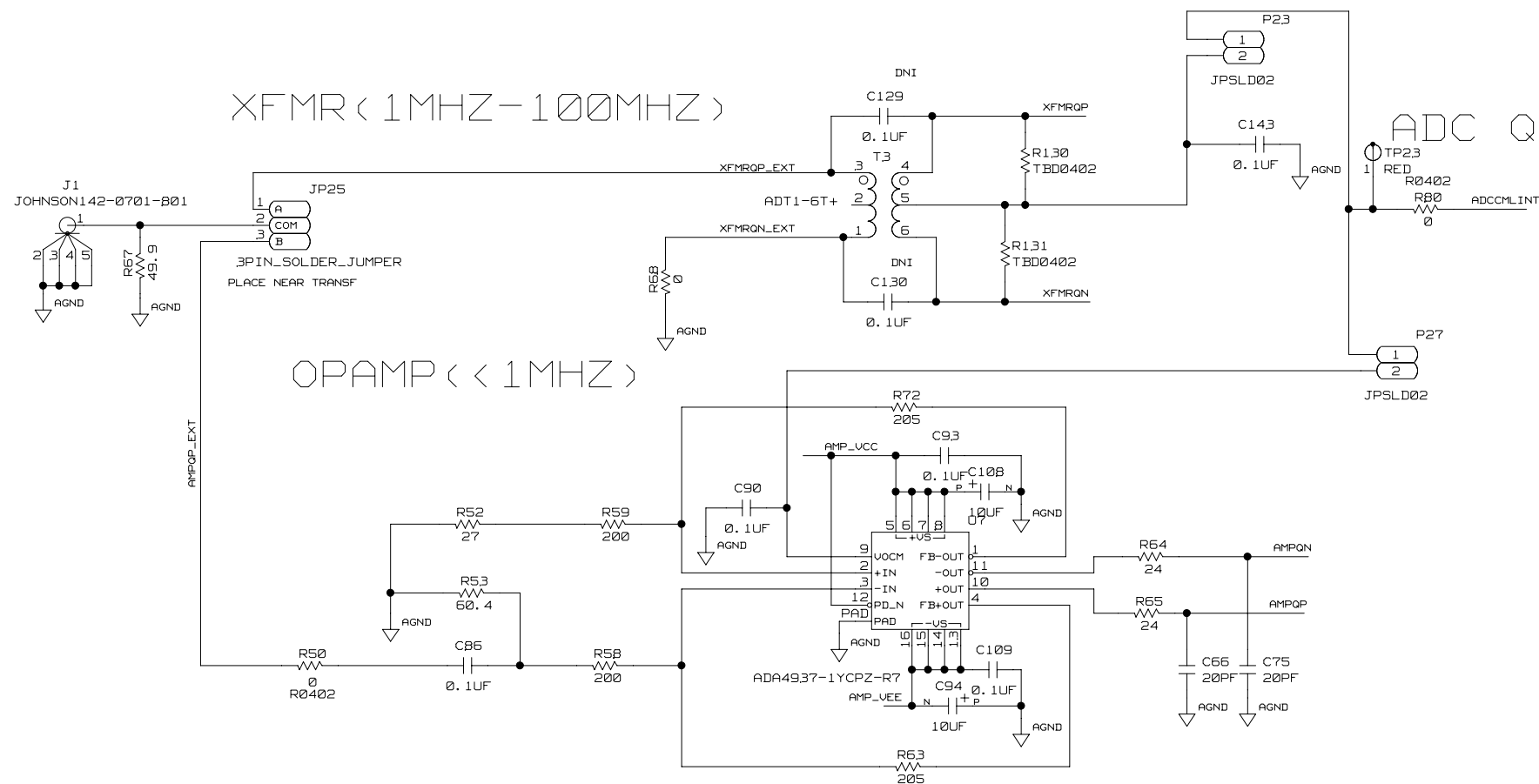


XFMR < 1MHZ - 100MHZ >

ADC Q INPUT

OPAMP < < 1MHZ >

PCB TOP

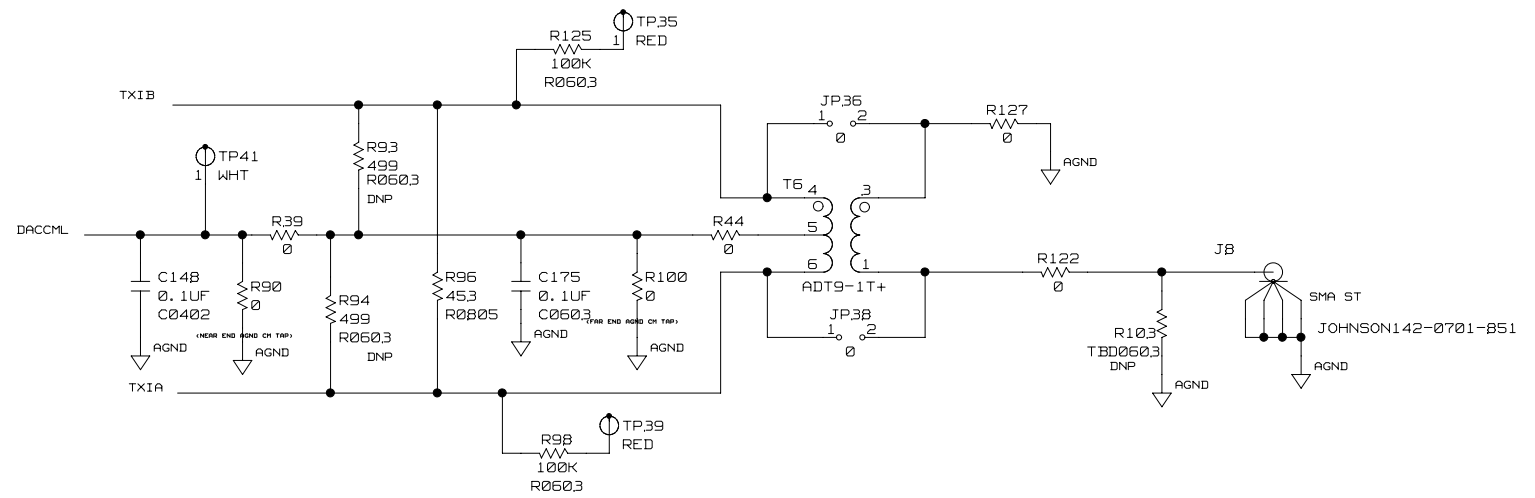


ANALOG DEVICES		SCHEMATIC	
AD9963 SOLDER EVAL BRD AD9961/AD9963 <PRODUCT_1>			
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 09010	REV B	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE <SCALE>	SHEET 7 OF 10

REVISIONS			
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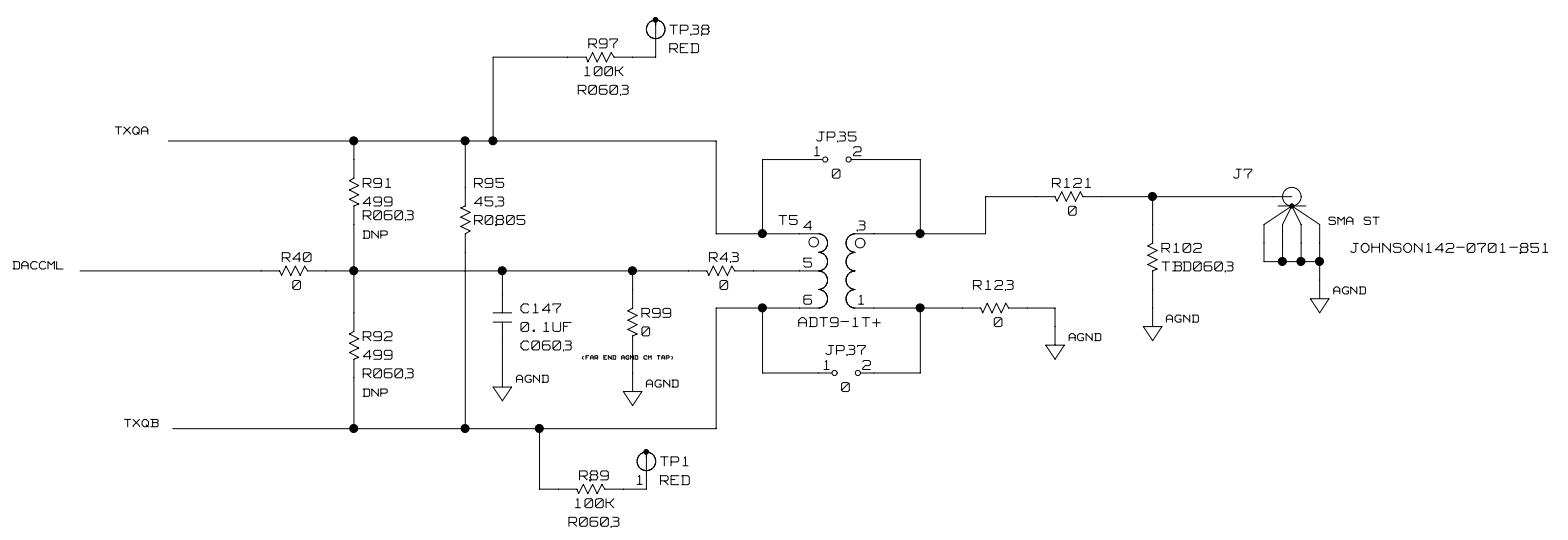
DAC OUTPUT CIRCUITS

IOUT NETWORK



XFMR PATH

QOUT NETWORK

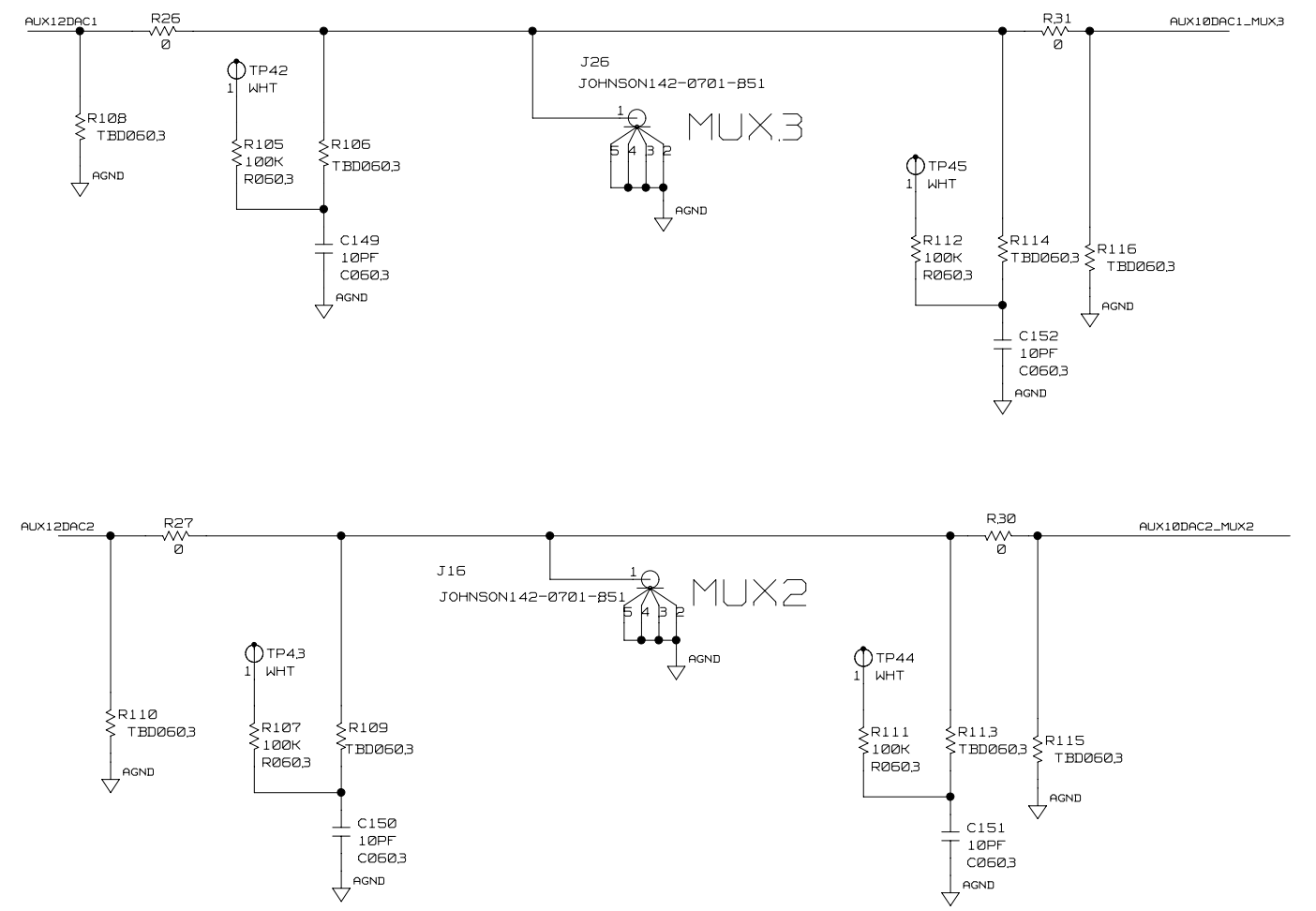


	SCHEMATIC		
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PTD ENGINEER	SIZE	SCALE	SHEET B OF 10
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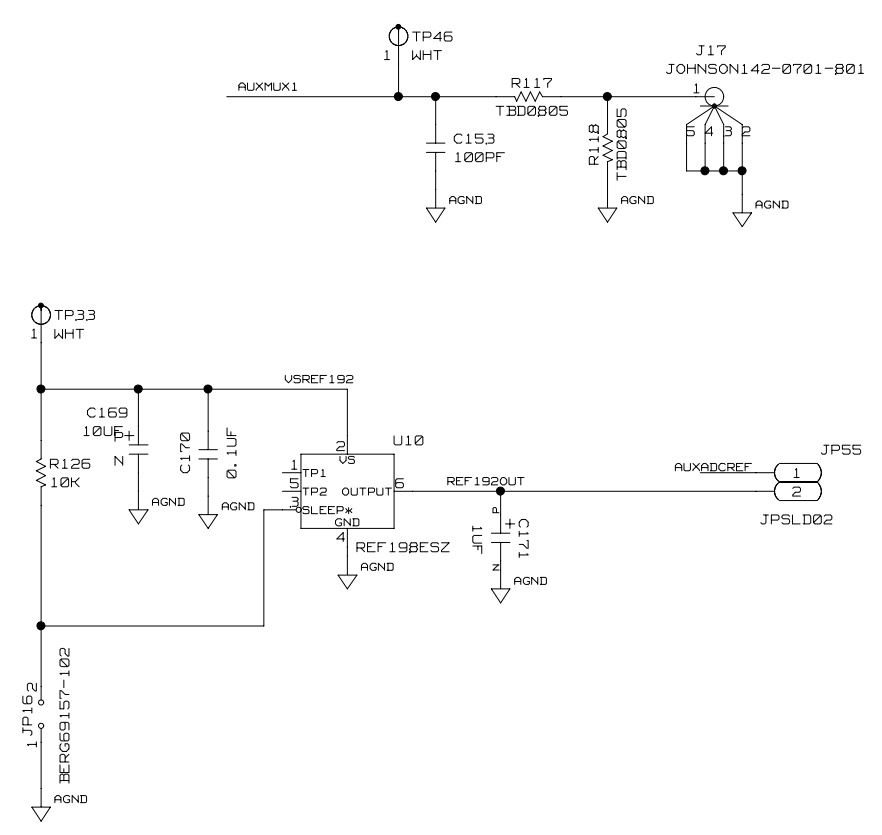
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

AUX DACS, AUX ADCS

AUX DACS



AUX ADCS

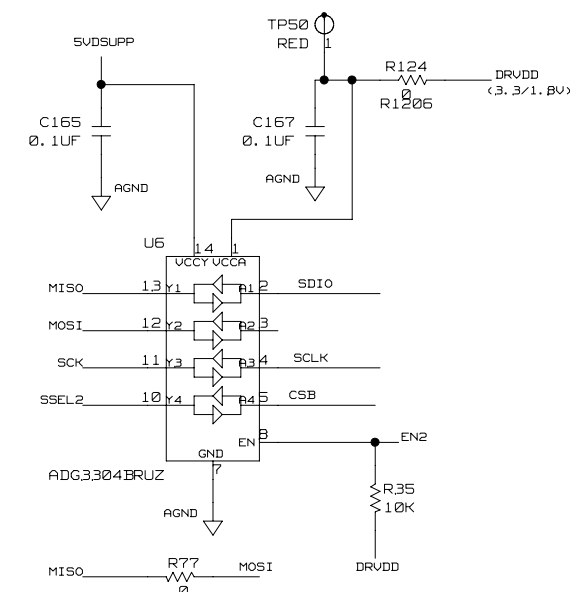
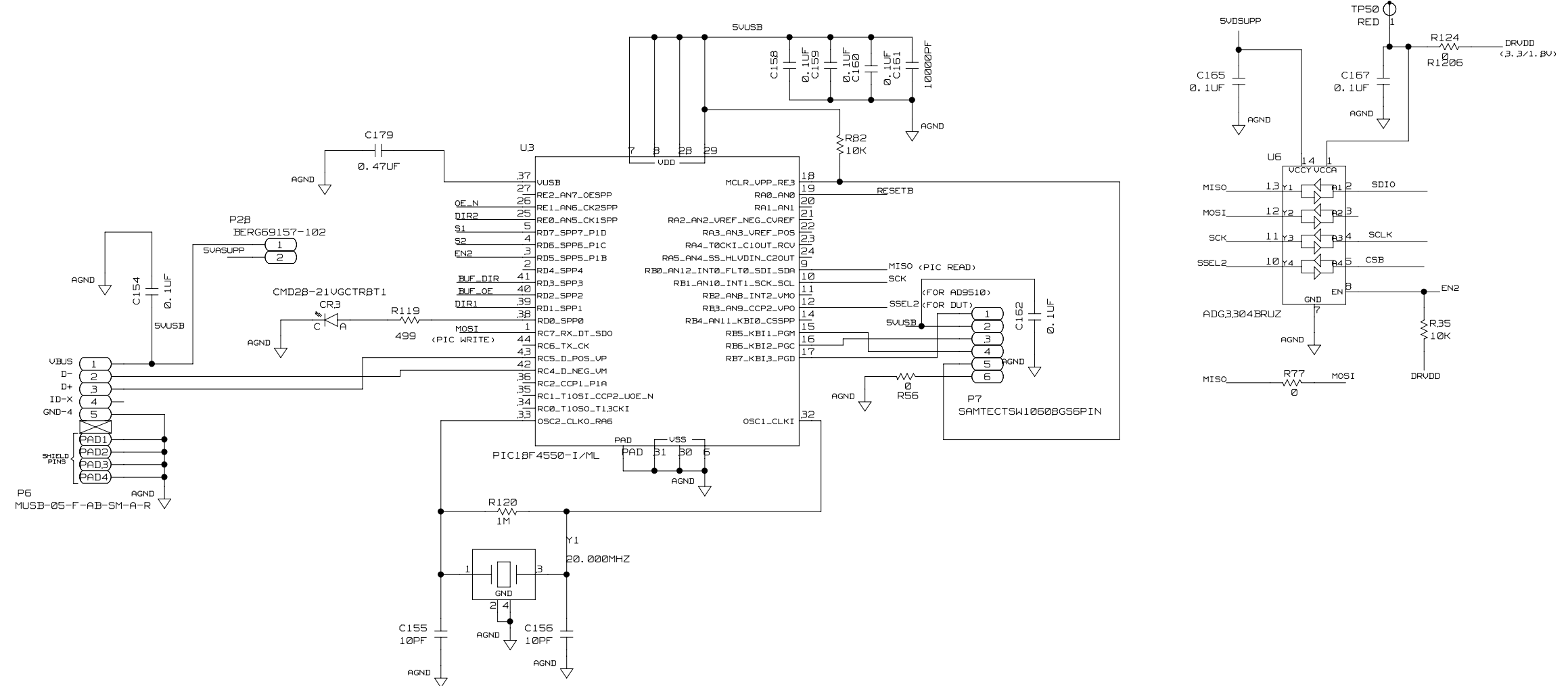


	SCHEMATIC		
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PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE <SCALE>	SHEET 9 OF 10

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

USB OPTION

SPI INTERFACES



		SCHEMATIC	
		AD9963 SOLDER EVAL BRD AD9961/AD9963 <PRODUCT_1>	
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	<DESIGN_VIEW>	09010	B
PTD ENGINEER	SIZE	SCALE	SHEET 10 OF 10
<PTD_ENGINEER>	D	<SCALE>	E