

THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.

JUMPER TABLE		
JP#	ON	OFF
1		
2		
3		
4		
5		

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

RELAY CONTROL CHART

CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR

* SEE ASSEMBLY INSTRUCTIONS

JULY 2, 2009

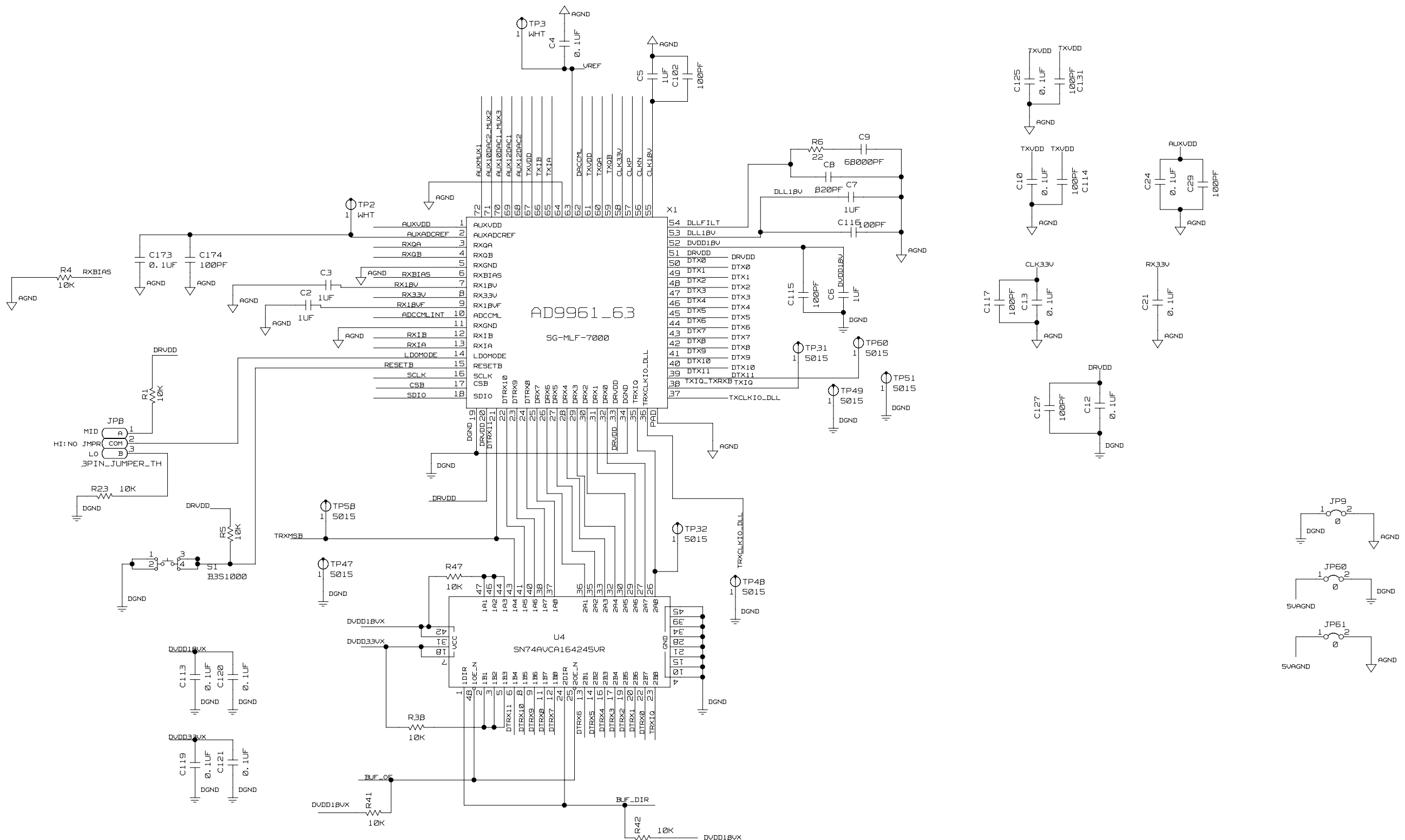
P. O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER

TEMPLATE ENGINEER P. ALONGI		DATE 09.25.2008	<h2>SCHEMATIC</h2>		
HARDWARE SERVICES <HARDWARE_SERVICES>			AD9963 EVAL BRD AD9961/AD9963		
HARDWARE SYSTEMS <HARDWARE_SYSTEMS>			<PRODUCT_1>		
TEST ENGINEER <TEST_ENGINEER>			<DRAWING_TITLE1>		
COMPONENT ENGINEER <COMPONENT_ENGINEER>			<DRAWING_TITLE2>		
TEST PROCESS <TEST_PROCESS>			<DRAWING_TITLE3>		
HARDWARE RELEASE <HARDWARE_RELEASE>			<DRAWING_TITLE4> <DRAWING_TITLE5>		
DESIGNER <DESIGNER>			MASTER PROJECT TEMPLATE <DRAWING_TITLE5>	TESTER TEMPLATE <TESTER>	DRAWING NO. 02-09007
PTD ENGINEER <PTD_ENGINEER>			UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES		
CHECKER <CHECKER>			TOLERANCES		
			DECIMALS X.XX +0.010 X.XXX +0.005	FRACTIONS +/-1/32	ANGLES +-2
			SIZE D	SCALE -	CODE ID NO. -
			SHEET 1 OF 10		

8
7
6
5
4
3
2
1

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

DUT PINOUT



	SCHEMATIC		
	AD9963 EVAL BRD AD9961/AD9963 <PRODUCT_1>		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02-09007	REV A
	PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE -

THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT DESIGN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.

SHEET 2 OF 10

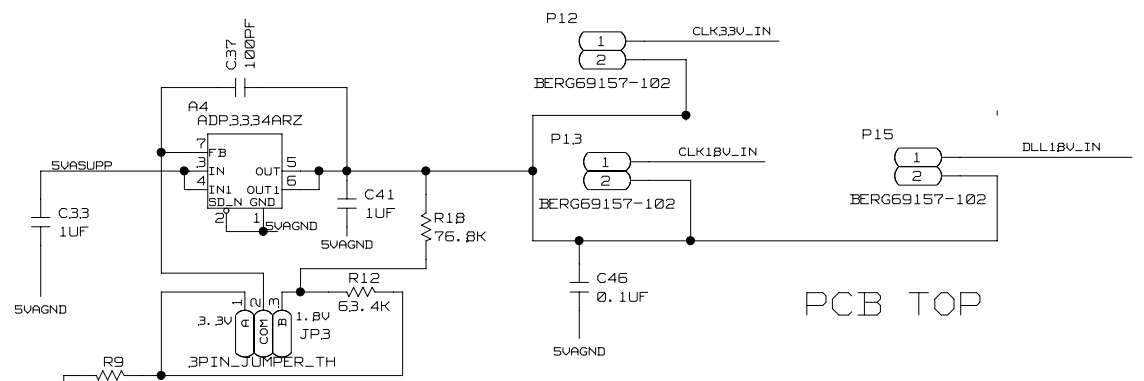
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

ANALOG POWER SUPPLIES

DIGITAL POWER SUPPLIES

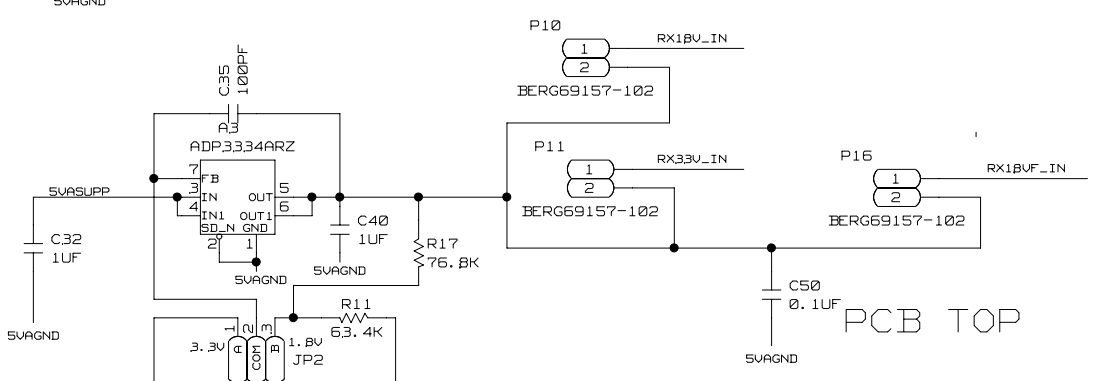
D

D



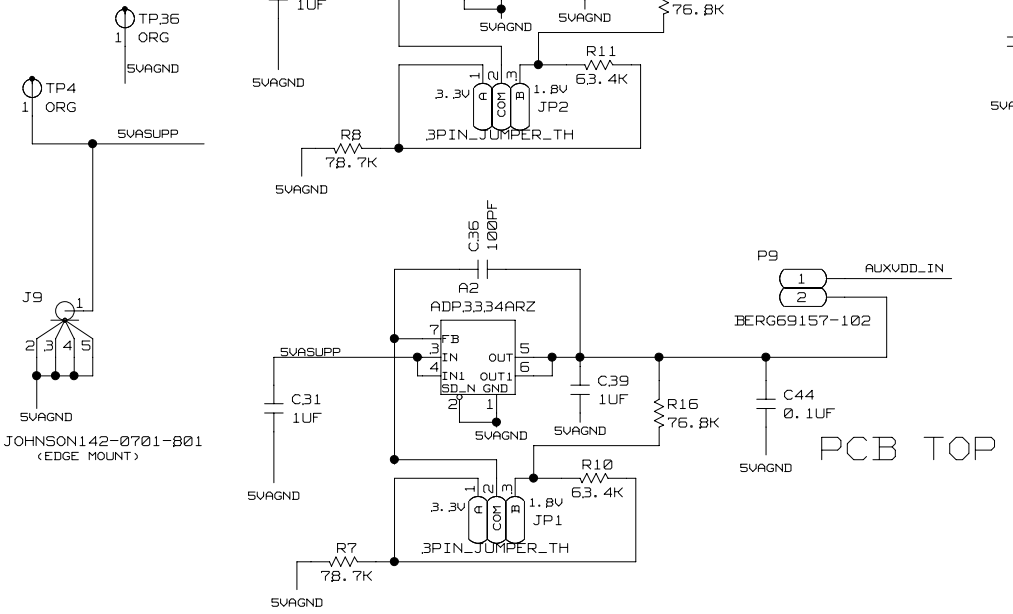
C

C



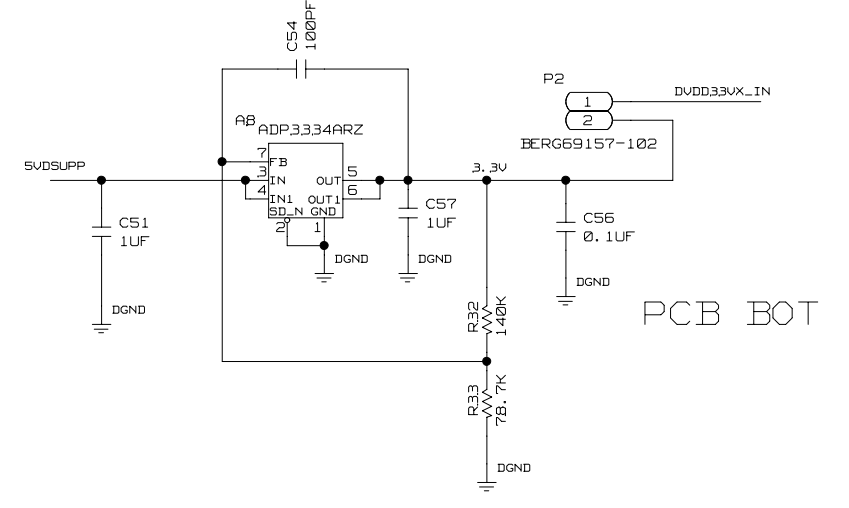
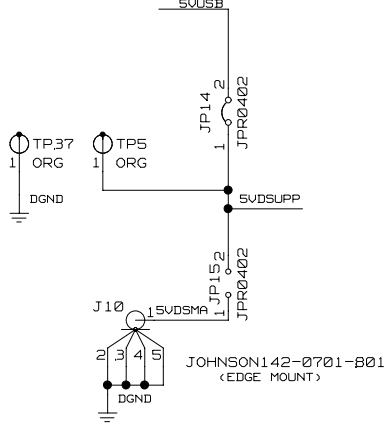
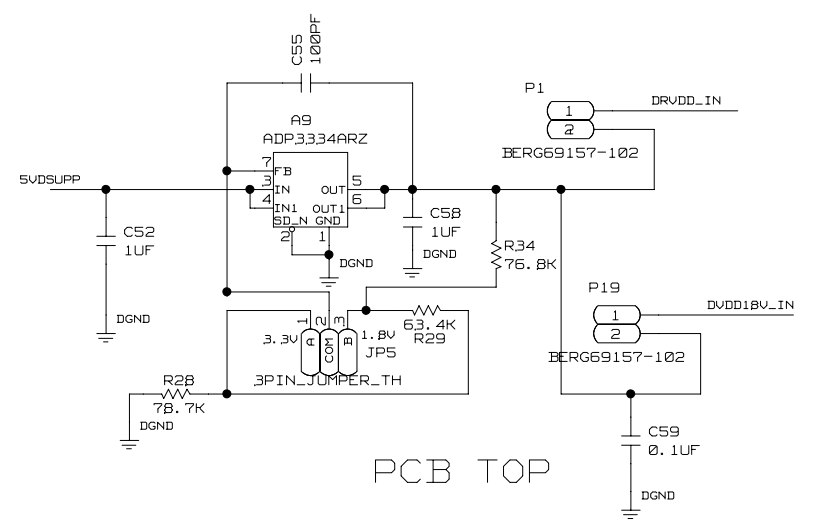
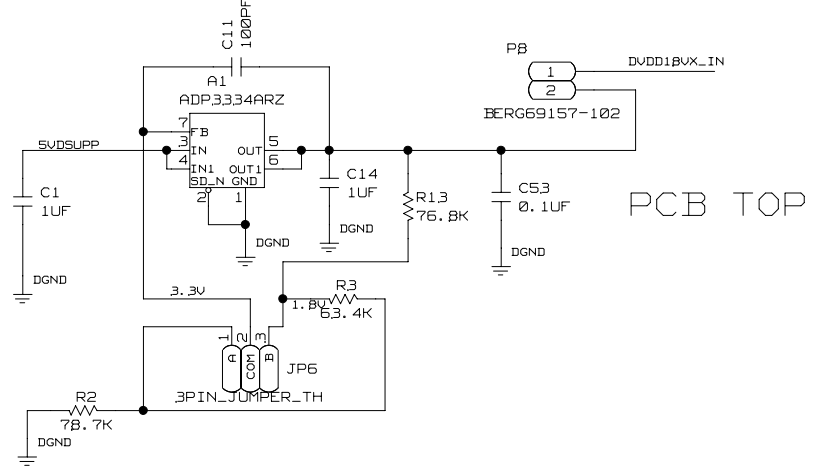
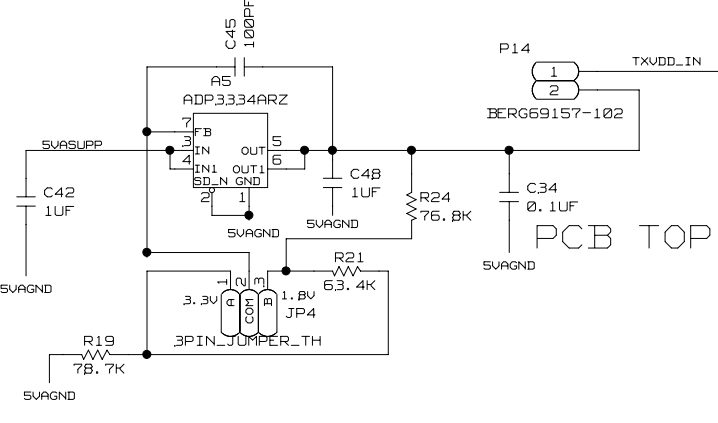
B

B



A

A

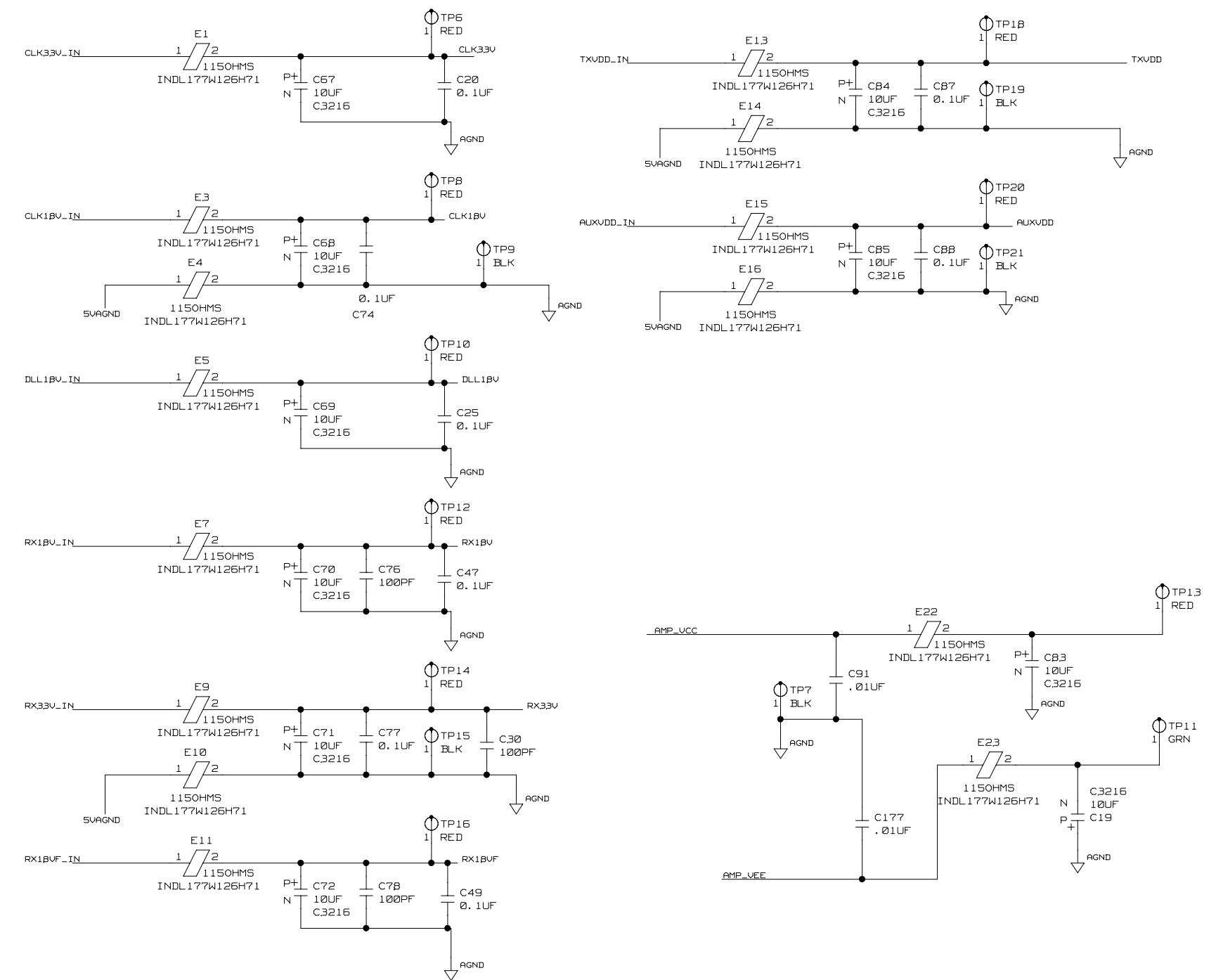


ANALOG DEVICES		SCHEMATIC		
AD9963 EVAL BRD AD9961/AD9963 <PRODUCT_1>		DESIGN NO. 02-09007	REV A	
DESIGN VIEW <DESIGN-VIEW>		SIZE D	SCALE -	SHEET 3 OF 10
PTD ENGINEER <PTD_ENGINEER>				

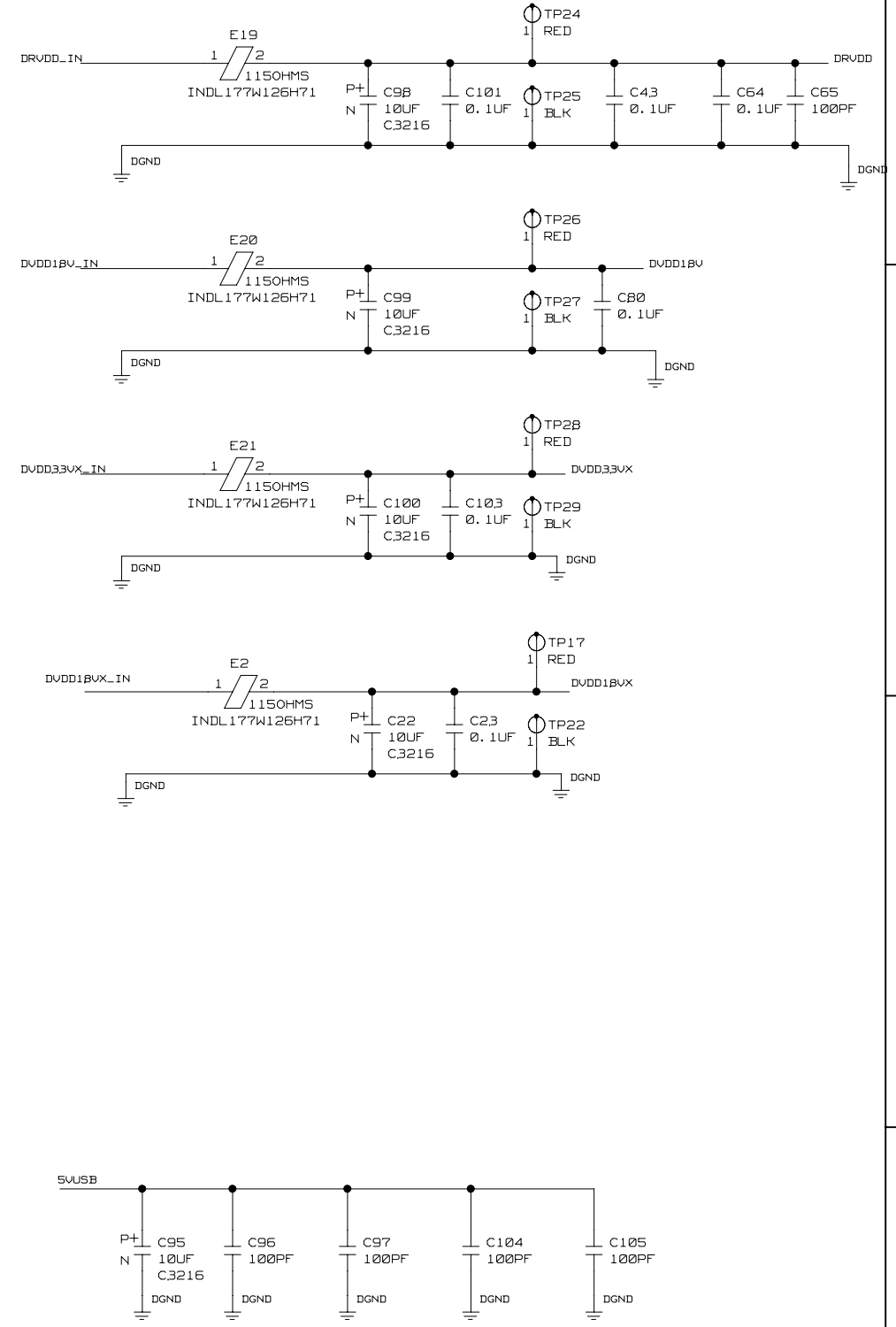
THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT DESIGN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

ANALOG SUPPLY FILTERS



DIGITAL SUPPLY FILTERS



	SCHEMATIC		
	AD9963 EVAL BRD AD9961/AD9963 <PRODUCT_1>		
<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT DESIGN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.</small>	DESIGN VIEW	DRAWING NO.	REV
	<DESIGN_VIEW>	02-09007	A
PTD ENGINEER	SIZE	SCALE	SHEET 4 OF 10
<PTD_ENGINEER>	D	-	

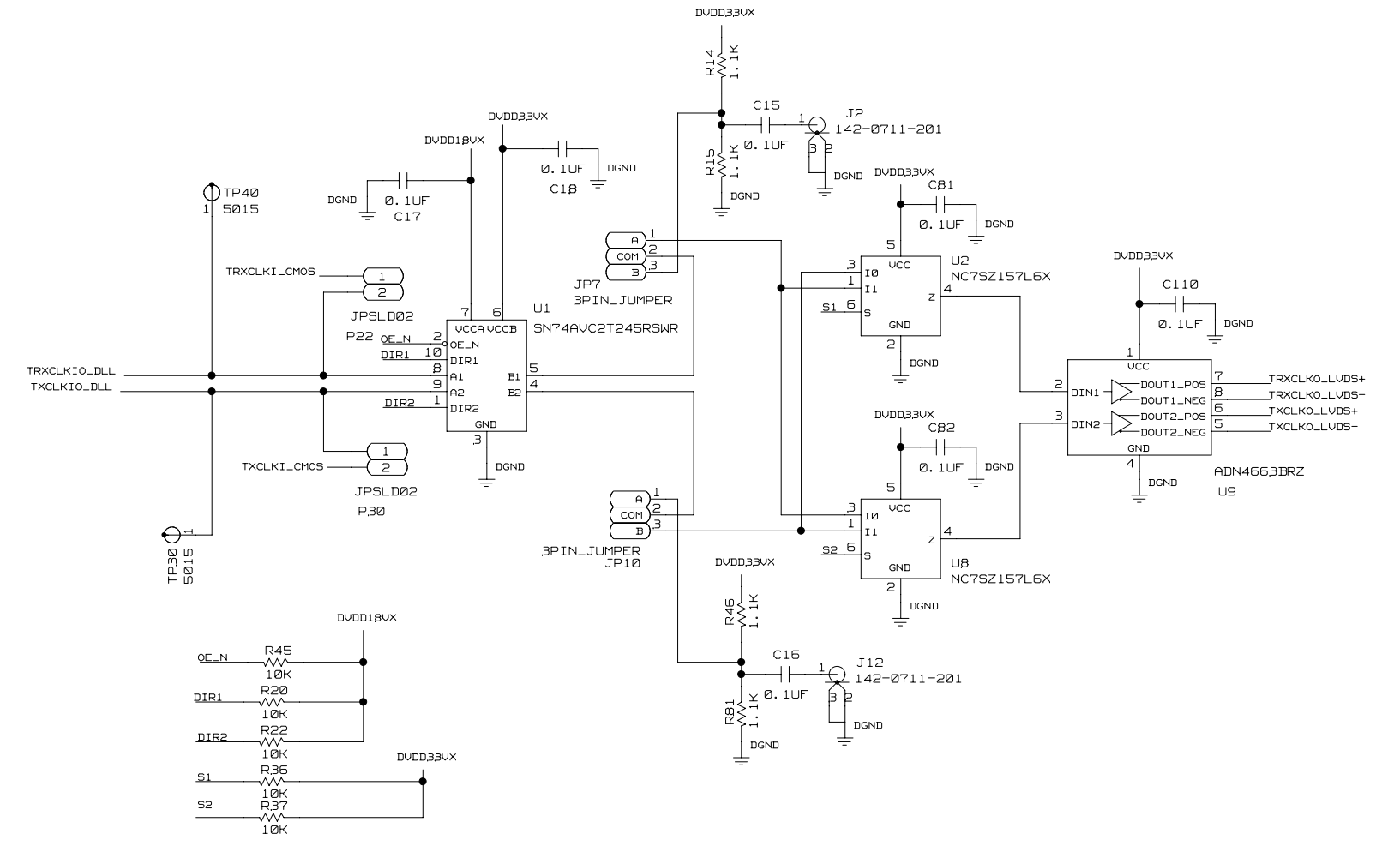
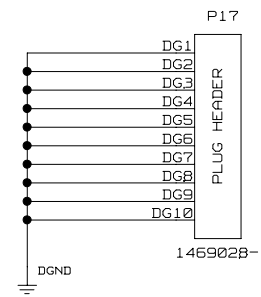
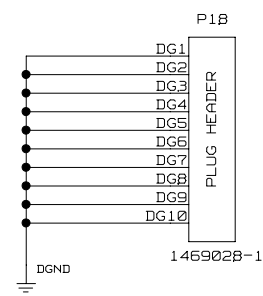
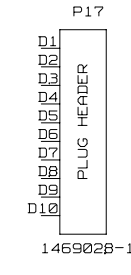
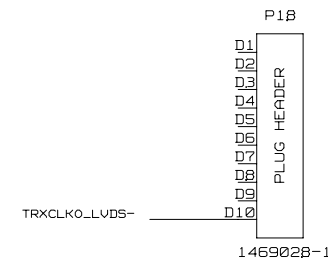
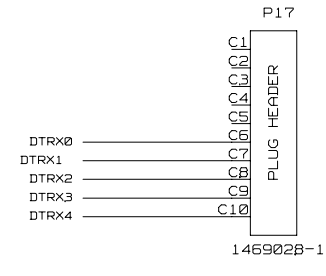
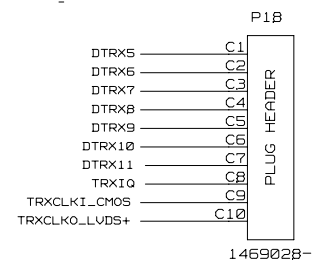
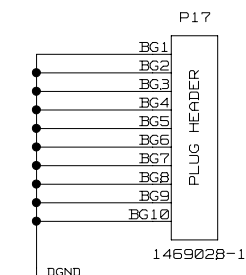
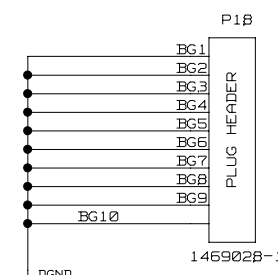
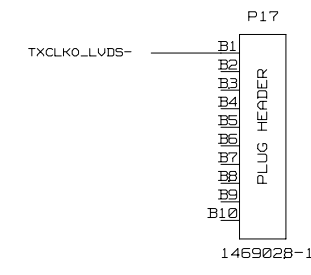
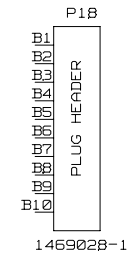
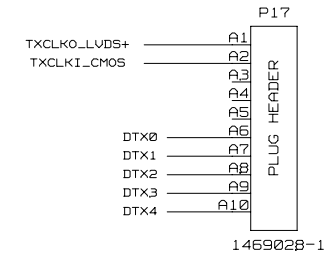
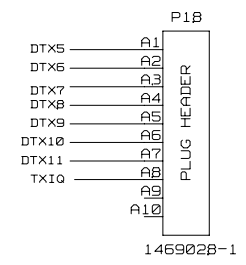
REVISIONS

REV	DESCRIPTION	DATE	APPROVED

TXRX INTERFACE
(TYCO PLUG CONN)

TXONLY INTERFACE
(TYCO PLUG CONN)

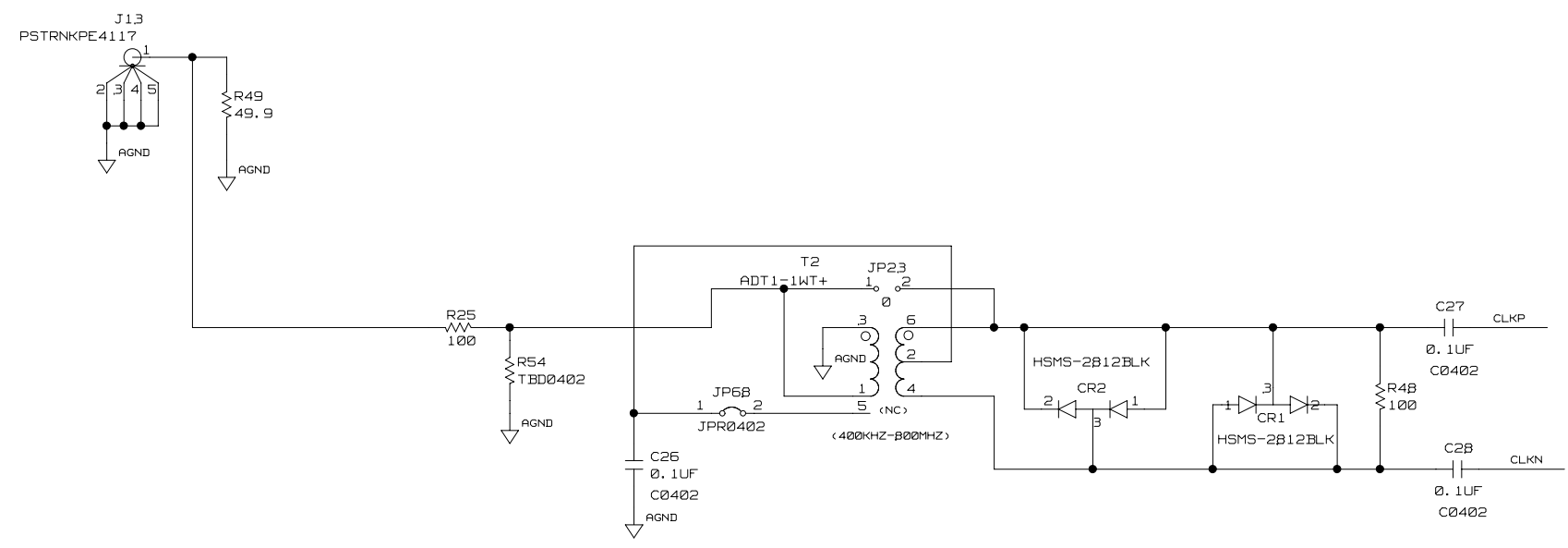
DATA INTERFACE/DATA CLOCKS



	SCHEMATIC		
	AD9963 EVAL BRD AD9961/AD9963 <PRODUCT_1>		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02-09007	REV A	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE -	SHEET 5 OF 10

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

CLOCKING CIRCUITS



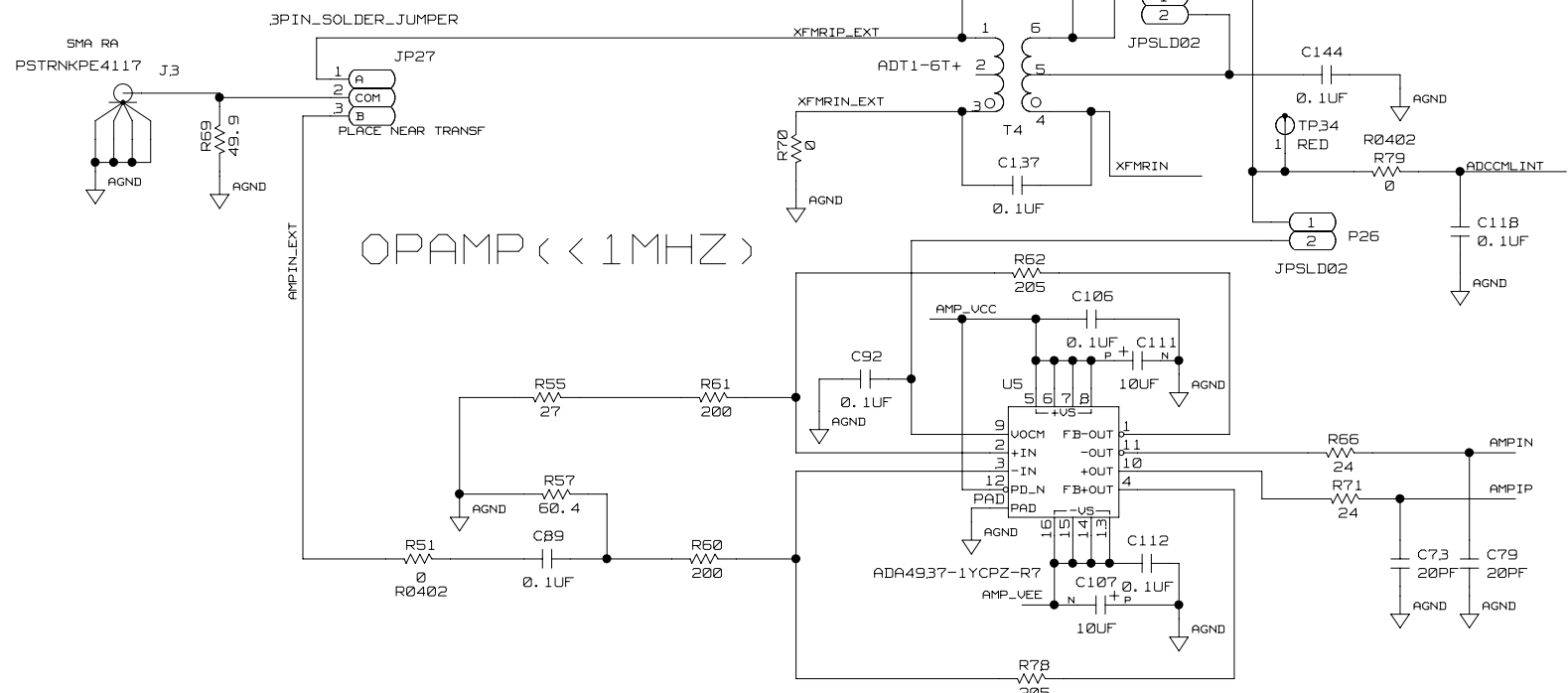
	SCHEMATIC		
	AD9963 EVAL BRD AD9961/AD9963 <PRODUCT_1>		
<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT DESIGN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.</small>	DESIGN VIEW	DRAWING NO.	REV
	<DESIGN_VIEW>	02-09007	A
PTD ENGINEER	SIZE	SCALE	SHEET 6 OF 10
<PTD_ENGINEER>	D	-	

ADC INPUT DRIVING CIRCUITS

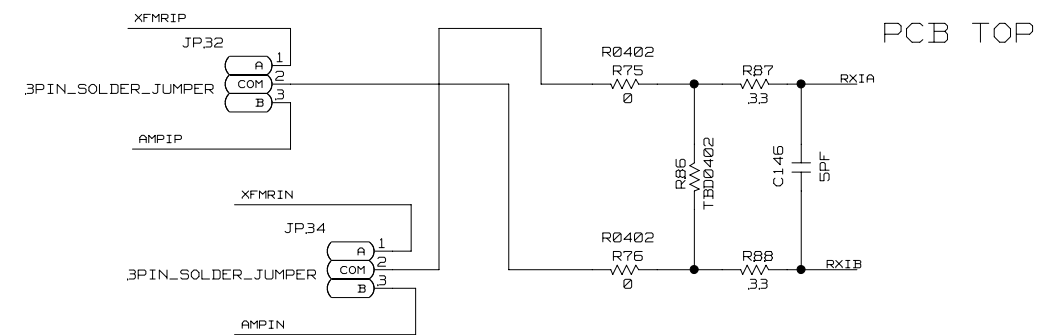
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

XFMR < 1MHZ - 100MHZ >

ADC I INPUT

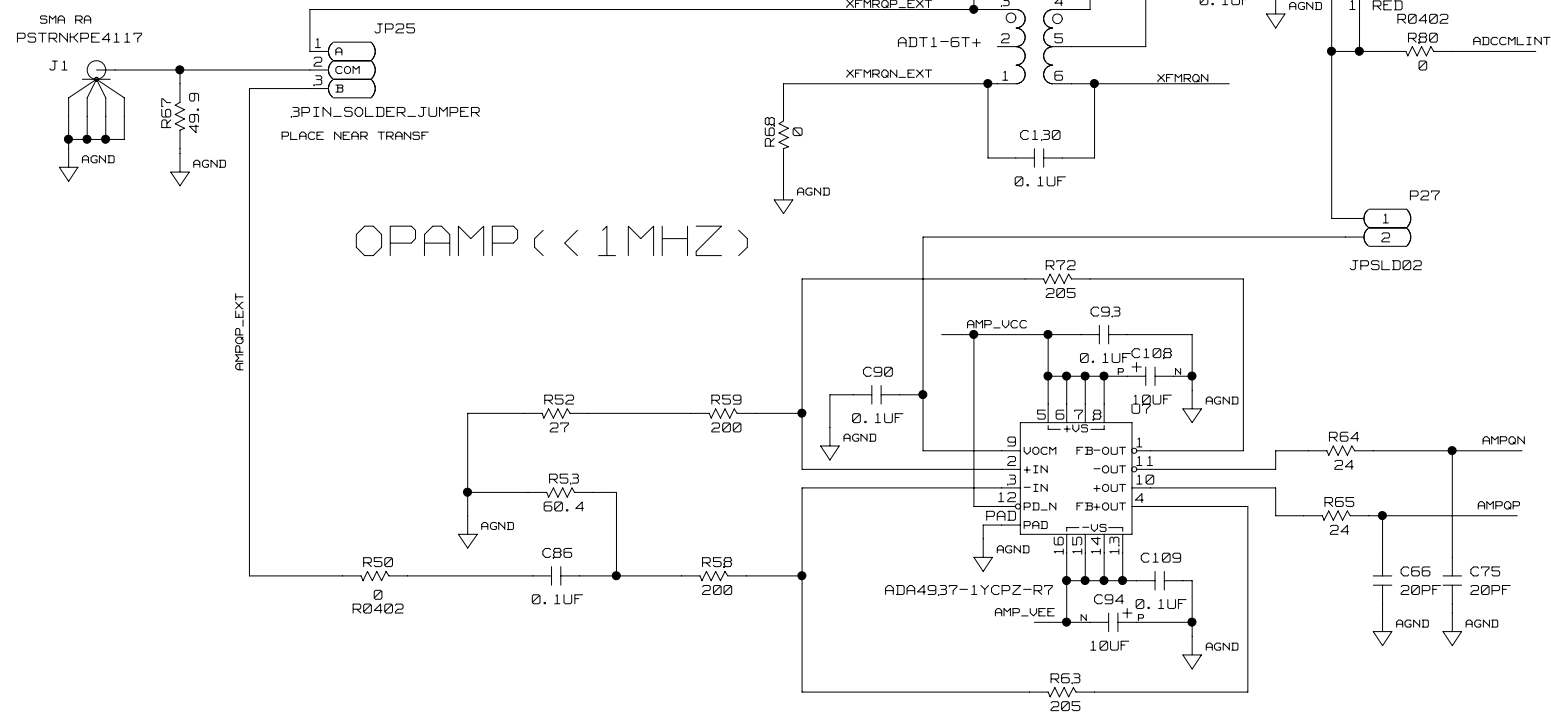


OPAMP < < 1MHZ >

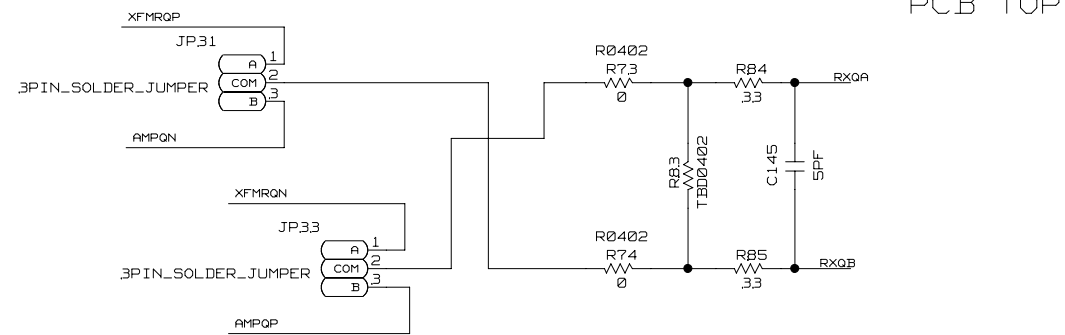


XFMR < 1MHZ - 100MHZ >

ADC Q INPUT



OPAMP < < 1MHZ >

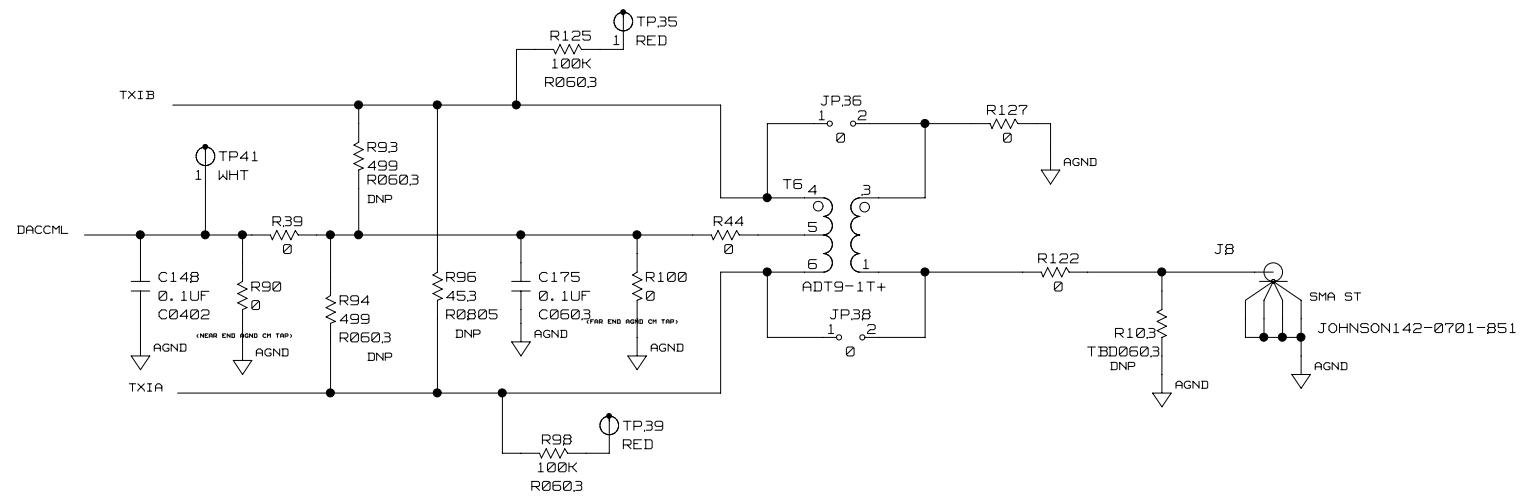


	SCHEMATIC		
	AD9963 EVAL BRD AD9961/AD9963 <PRODUCT_1>		
<small>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT DESIGN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.</small>	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02-09007	REV A
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE -	SHEET 7 OF 10

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

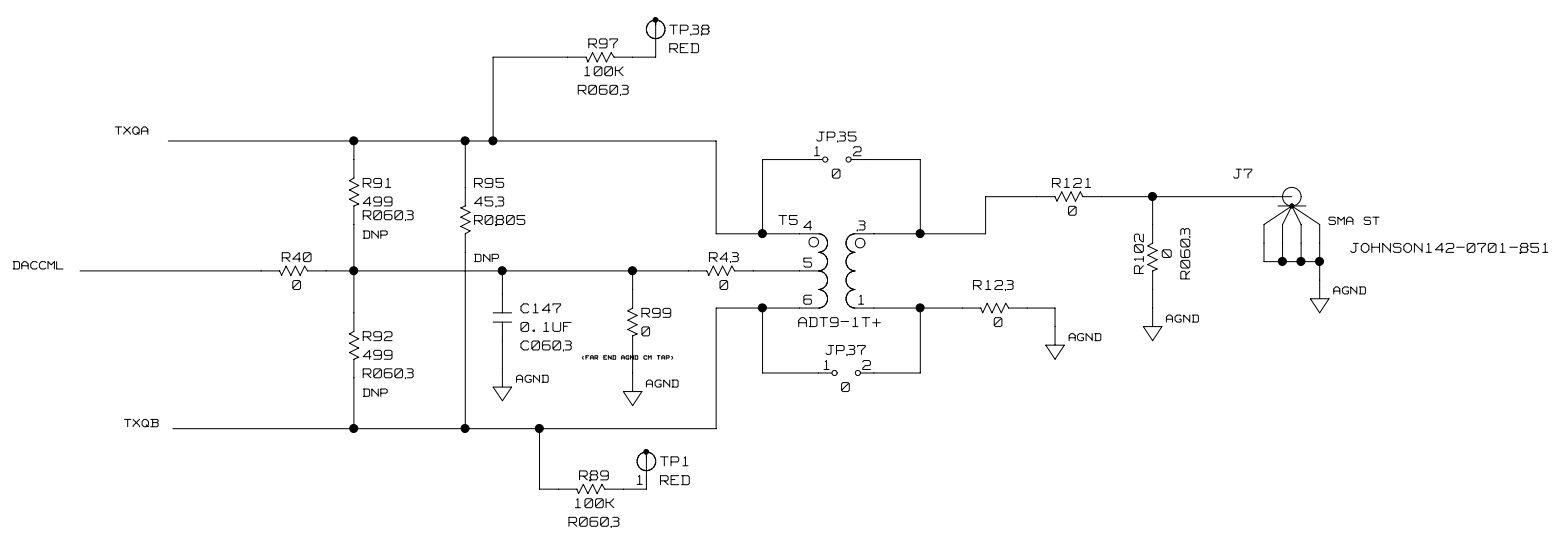
DAC OUTPUT CIRCUITS

IOUT NETWORK



XFMR PATH

QOUT NETWORK

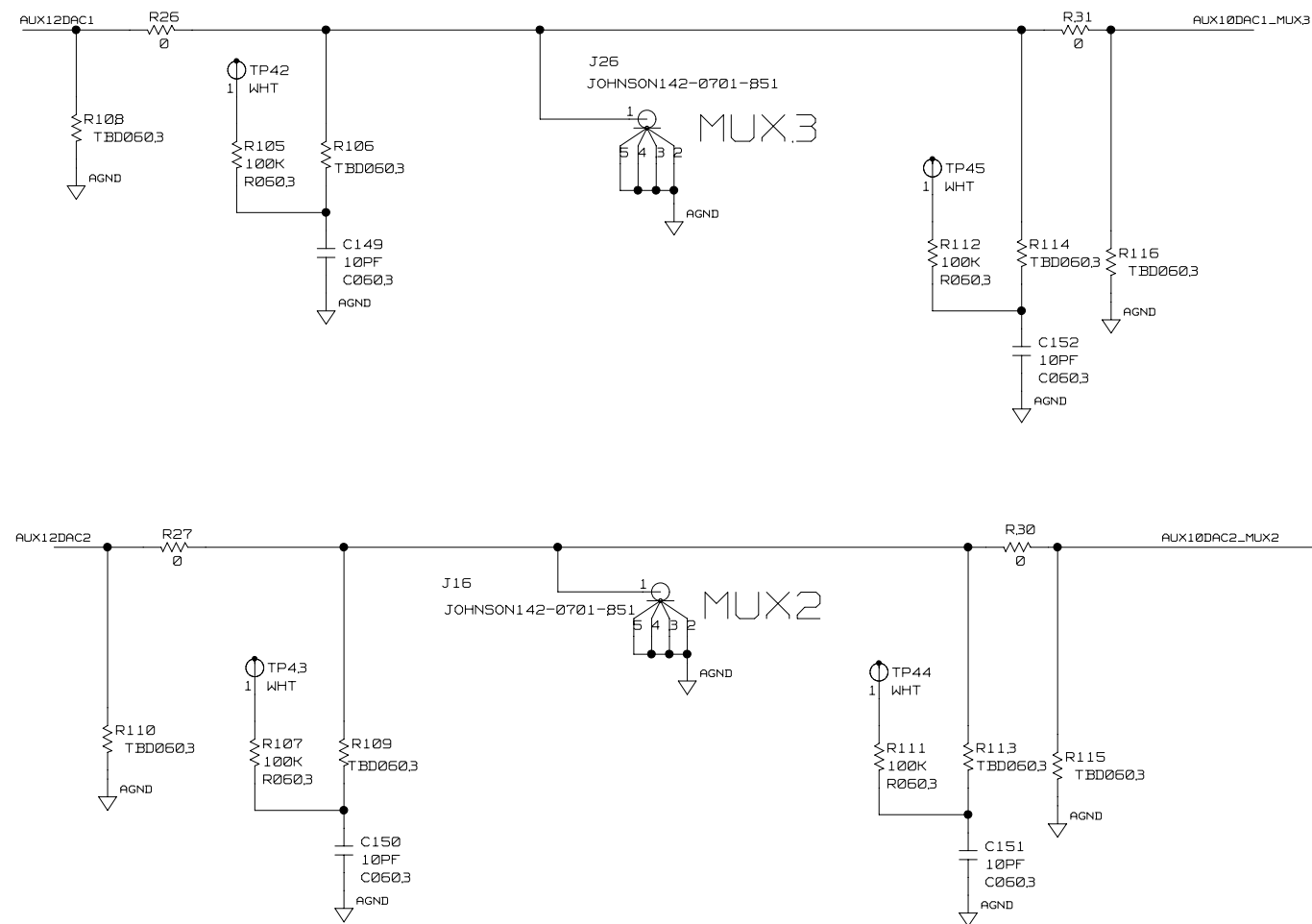


	SCHEMATIC		
	AD9963 EVAL BRD AD9961/AD9963 <PRODUCT_1>		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02-09007	REV A	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE -	SHEET 8 OF 10

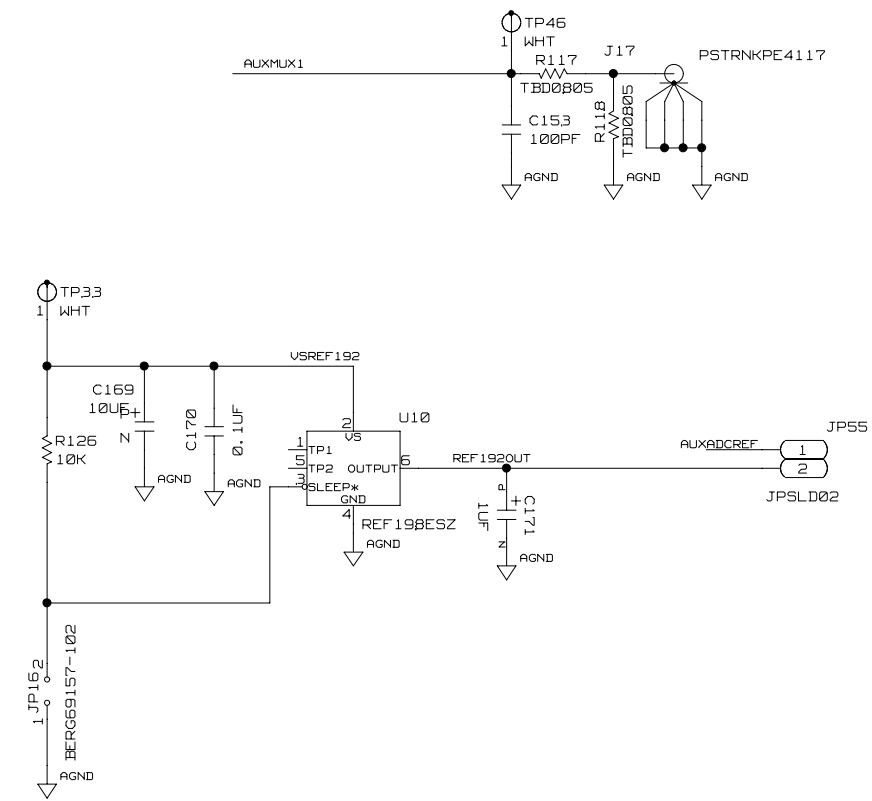
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

AUX DACS, AUX ADCS

AUX DACS



AUX ADCS

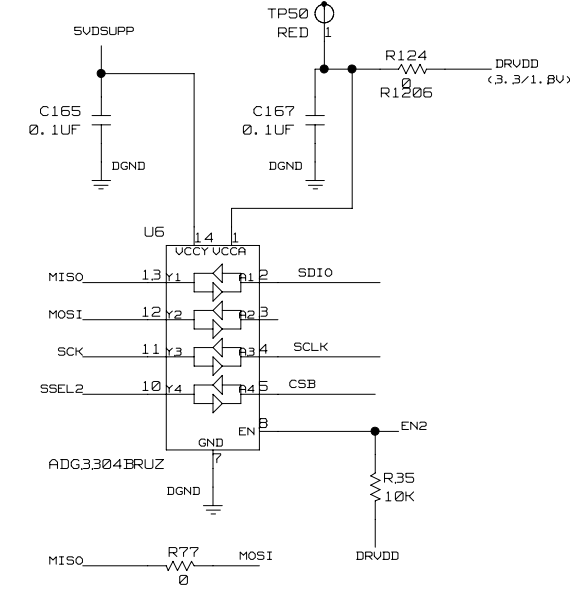
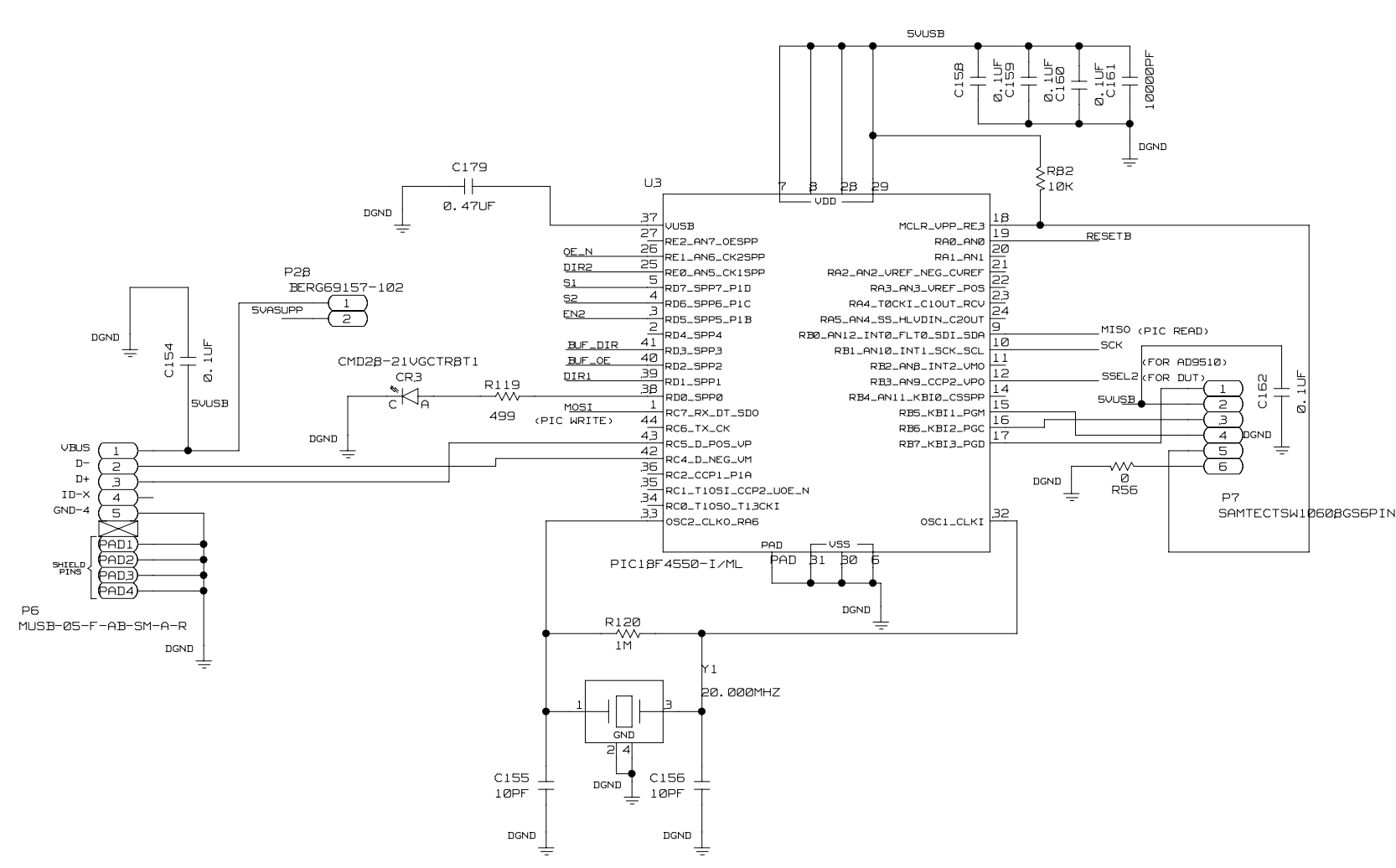


	SCHEMATIC		
	AD9963 EVAL BRD AD9961/AD9963 <PRODUCT_1>		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02-09007	REV A	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE -	
		SHEET 9 OF 10	

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

USB OPTION

SPI INTERFACES



	SCHEMATIC		
	AD9963 EVAL BRD AD9961/AD9963 <PRODUCT_1>		
DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02-09007	REV A	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE -	
		SHEET 10 OF 10	