

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	INITIAL RELEASE	30AUG2017	M.LYNCH

JUMPER TABLE		
JP#	ON	OFF
1		
2		
3		
4		
5		

\* SEE ASSEMBLY INSTRUCTIONS

**RELAY CONTROL CHART**

CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR

P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER

TEMPLATE ENGINEER G. Celedonio DATE 30AUG2017	HARDWARE SERVICES R. Macdonald DATE 30AUG2017	HARDWARE SYSTEMS N/A TEST ENGINEER N/A	COMPONENT ENGINEER A. Caulan DATE 30AUG2017	TEST PROCESS N/A HARDWARE RELEASE R. Amarille DATE 30AUG2017	DESIGNER J. Sayson DATE 30AUG2017	PTD ENGINEER M. Lynch DATE 30AUG2017	CHECKER N/A DATE N/A	<b>SCHEMATIC</b> HW TYPE : Customer Evaluation Product(s) : ADuM141E1S : N/A PACKAGE : 16-lead 6.86X10.16_MM CERPAK-family : Pitch-pitch StyleVendor Style		MASTER PROJECT TEMPLATE TBD TESTER TEMPLATE no_template	DRAWING NO. 02_047807	REV. A
<small>UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES</small> TOLERANCES DECIMALS            FRACTIONS            ANGLES X.XX +0.010            +1/32            +2 X.XXX +0.005								SIZE <b>D</b>	SCALE 1:1	CODE ID NO. CodeID	SHEET 1 OF 3	

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7

6

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1

D

D

C

C

B

B

A

A

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7

6

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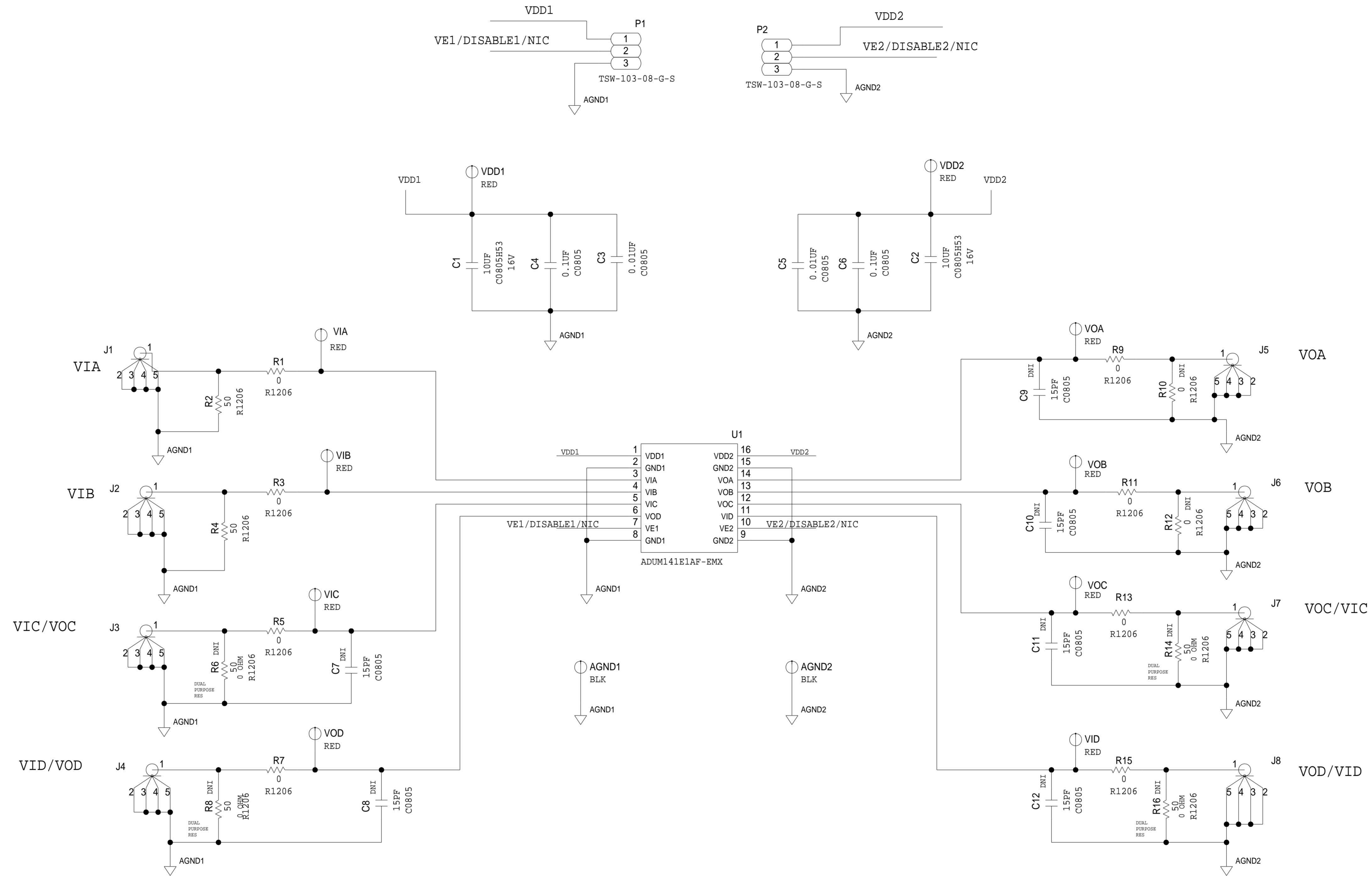
3

2

1

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\* SEE NOTES FOR ADUM14XD/E CONFIGURATIONS IN PAGE 3



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	HW TYPE : Customer Evaluation Product(s) : ADUM141E1S : N/A		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_047807	REV A
	PTD ENGINEER M. Lynch	SIZE D	SCALE 1:1
		SHEET 2	OF 3

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NOTES :

1. FOR ADUM140D:

A. PIN 7 IS DISABLE1. IT DISABLES THE INPUTS AND THE OUTPUTS WILL TAKE ON THE FAIL-SAFE OUTPUT STATE.

TO DISABLE INPUTS CONNECT TO P2. TO ENABLE INPUTS CONNECT TO P1.

B. PIN 10 IS NIC. NO INTERNAL CONNECTION. LEAVE THIS PIN FLOATING AND DO NOT CONNECT TO NEITHER P3 NOR P4.

2. FOR ADUM140E:

A. PIN 7 IS NIC. NO INTERNAL CONNECTION. LEAVE THIS PIN FLOATING AND DO NOT CONNECT TO NEITHER P1 NOR P2.

B. PIN 10 IS ENABLE2 (VE2). TO ENABLE OUTPUTS CONNECT TO P3. TO DISABLE OUTPUTS CONNECT TO P4.

3. FOR ADUM141D AND ADUM142D:

A. PIN 7 IS DISABLE1. IT DISABLES THE INPUTS AND THE OUTPUTS WILL TAKE ON THE FAIL-SAFE OUTPUT STATE.

TO DISABLE INPUTS CONNECT TO P2. TO ENABLE INPUTS CONNECT TO P1.


B. PIN 10 IS DISABLE2. IT DISABLES THE INPUTS AND THE OUTPUTS WILL TAKE ON THE FAIL-SAFE OUTPUT STATE.

TO DISABLE INPUTS CONNECT TO P4. TO ENABLE INPUTS CONNECT TO P3.

4. FOR ADUM141E AND ADUM142E:

A. PIN 7 IS ENABLE1 (VE1). TO ENABLE OUTPUTS CONNECT TO P1. TO DISABLE OUTPUTS CONNECT TO P2.

B. PIN 10 IS ENABLE2 (VE2). TO ENABLE OUTPUTS CONNECT TO P3. TO DISABLE OUTPUTS CONNECT TO P4.

	<b>SCHEMATIC</b>		
	HW TYPE : Customer Evaluation Product(s): ADuM141E1S : N/A		
	DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_047807	REV A
PTD ENGINEER M. Lynch	SIZE <b>D</b>	SCALE 1:1	SHEET 3 OF 3

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