### RELAY CONTROL CHART

<table>
<thead>
<tr>
<th>CONTROL</th>
<th>CODE</th>
<th>DEVICE</th>
<th>FUNCTION</th>
<th>CONNECTOR</th>
</tr>
</thead>
</table>

### JUMPER TABLE

<table>
<thead>
<tr>
<th>JUMPER</th>
<th>ON</th>
<th>OFF</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td></td>
<td></td>
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<tr>
<td>2</td>
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<td>3</td>
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<td>4</td>
<td></td>
<td></td>
</tr>
<tr>
<td>5</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*SEE ASSEMBLY INSTRUCTIONS*

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**SCHEMATIC**

*HW TYPE: EVAL-AD9246S*

*Product(s): AD9246S*

*Package: 48-LD 14.22x14.22MM_CQFP*

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VARIABLE POWER SUPPLY INPUTS

- V_DIG : +2.5V (SPI, CMOS BUFFER)
- 3P3V : +3.3V (SPI, OTR CIRCUIT)
- REST OF BOARD
  - AVDD : +1.8V
  - DRVDD : +2.5V

SUPPLY REGULATORS

- POWER SUPPLY INPUT
- SUPPLY REGULATORS
- VARIABLE POWER SUPPLY INPUTS

- AD9246S
- HW TYPE: EVAL-AD9246S
- A02_046595

- ADP1708ARDZ-R7
- 120OHM AT 100MEGHZ
- 24.3K
- 4.7UF
- 11.3K
- C207
- R206
- C208
- R205
- C209
- R204

- TSW-102-08-G-S
- VIN
- VIN
- VIN
- VIN
- VIN
- VIN
- VIN
- VIN
- VIN
- VIN

- ADP1708ARDZ-R7
- 5

- U202
- U203
- U201

- ANTENNA
- ANTENNA
- ANTENNA
- ANTENNA
- ANTENNA
- ANTENNA
- ANTENNA
- ANTENNA
- ANTENNA
- ANTENNA

- SML-310MTT86
- DS200
- SK33A
- A
- C
- D

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- REV DATE
- SHEET
- APPR
- DESCRIPTION
- REV
- DATE
- APPROVED

- AD9246S
- AVDD : +1.8V
- DRVDD : +2.5V

- REST OF BOARD
  - V_DIG : +2.5V (SPI, CMOS BUFFER)
  - 3P3V : +3.3V (SPI, OTR CIRCUIT)

- Schematic
- AD9246S