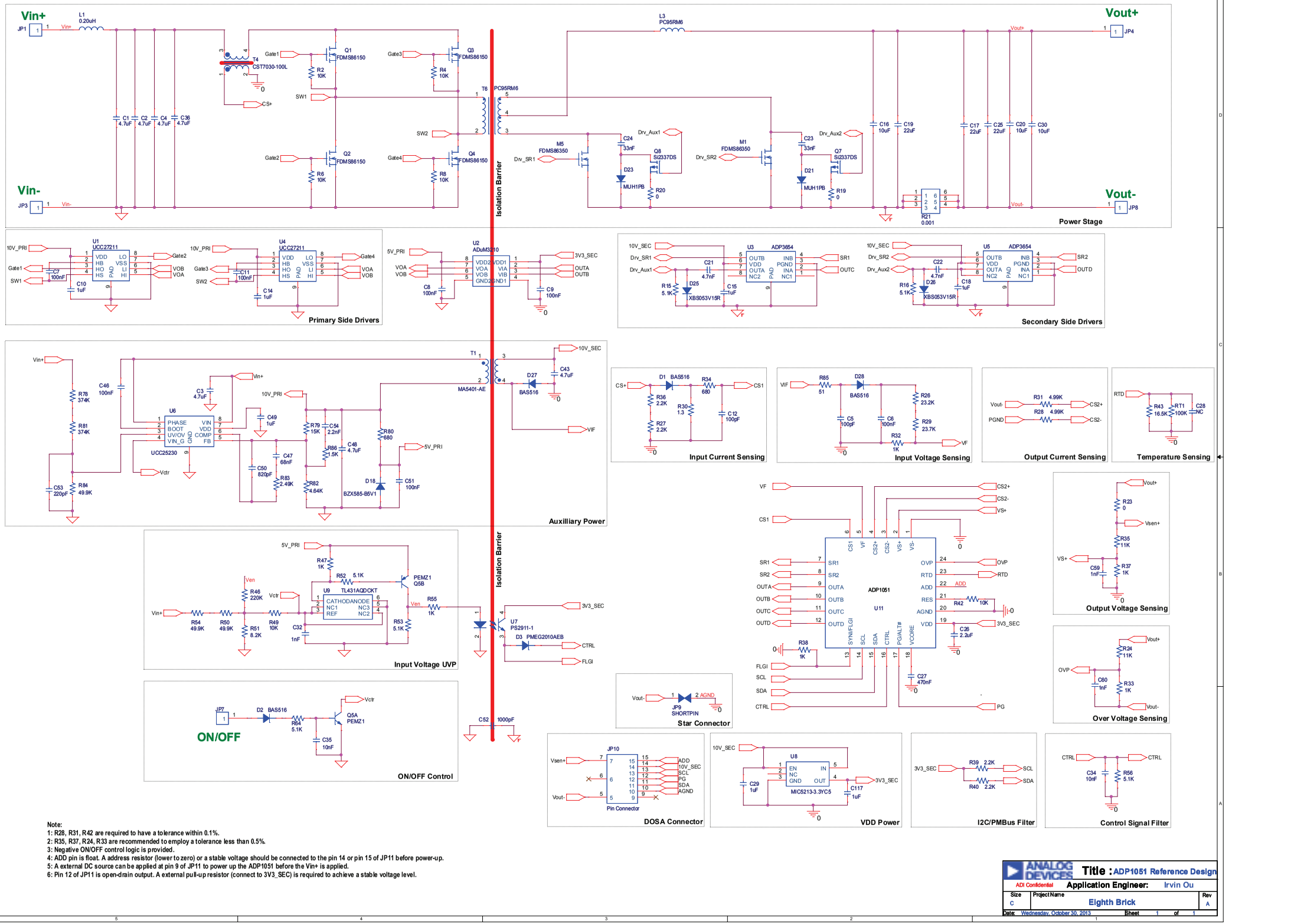

Note:
1: R28, R31, R42 are required to have a tolerance within 0.1%.
2: R35, R37, R24, R33 are recommended to employ a tolerance less than 0.5%.
3: Negative ON/OFF control logic is provided.
4: ADD pin is float. A address resistor (lower to zero) or a stable voltage should be connected to the pin 14 or pin 15 of JP11 before power-up.
5: A external DC source can be applied at pin 9 of JP11 to power up the ADP1051 before the Vin+ is applied.
6: Pin 12 of JP11 is open-drain output. A external pull-up resistor (connect to 3V3_SEC) is required to achieve a stable voltage level.

ANALOG DEVICES

Title : ADP1051 Reference Design

ADI Confidential

Application Engineer: Irvin Ou

Size	Project Name	Rev
C	Eighth Brick	A

Date: Wednesday, October 30, 2013 Sheet 1 of 1