

6

5

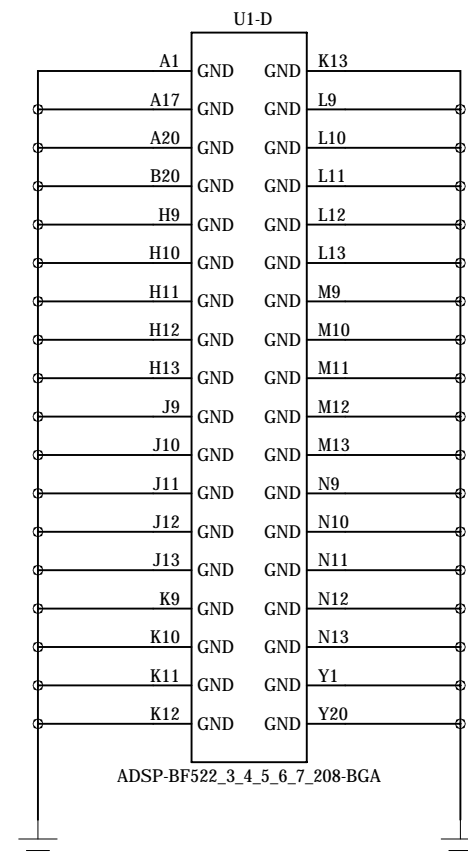
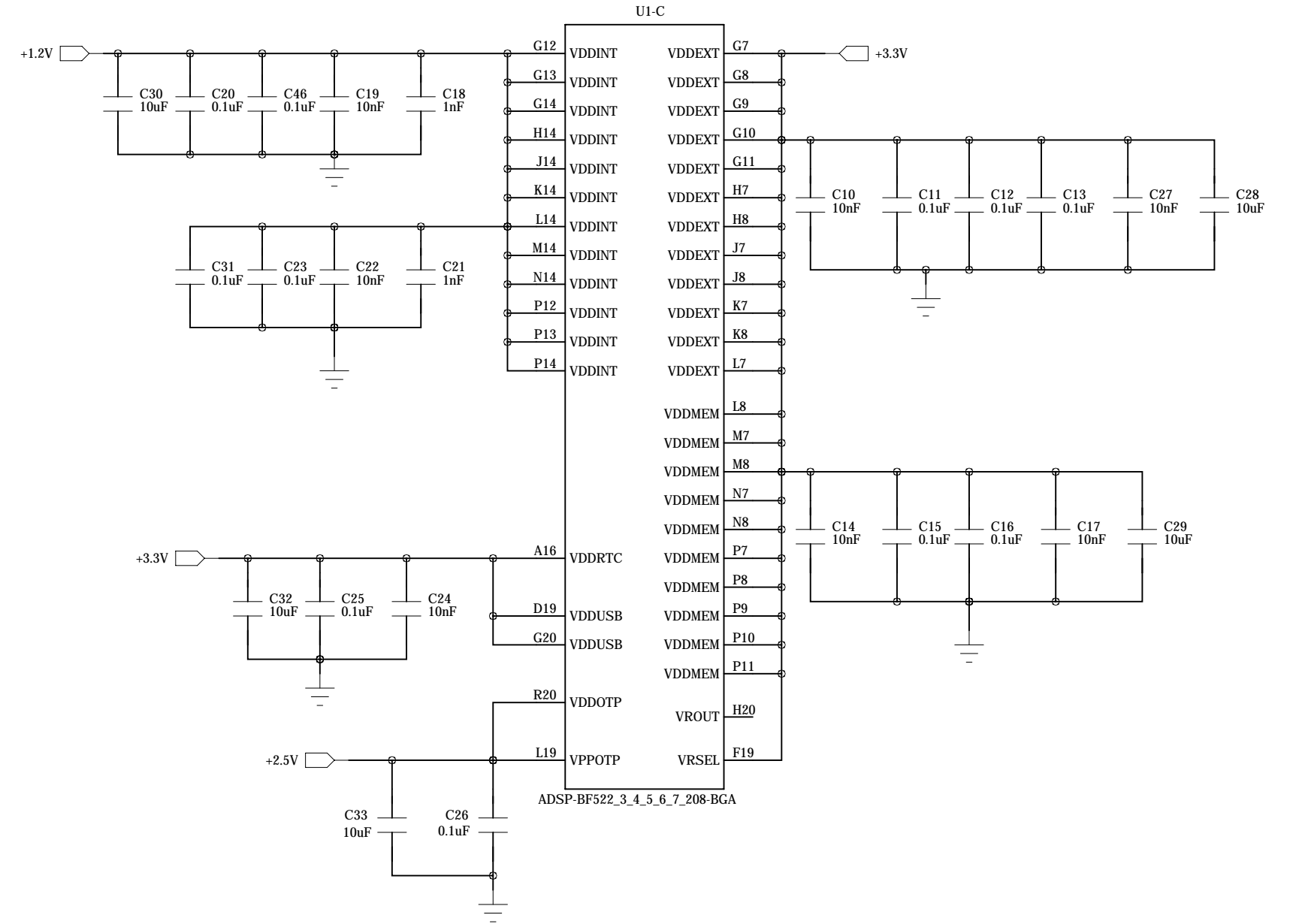
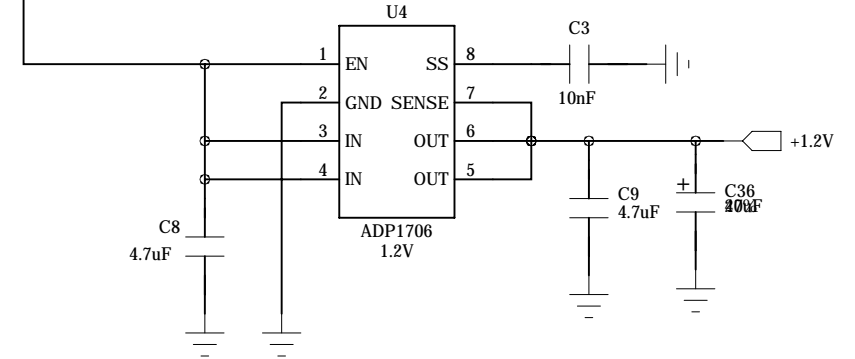
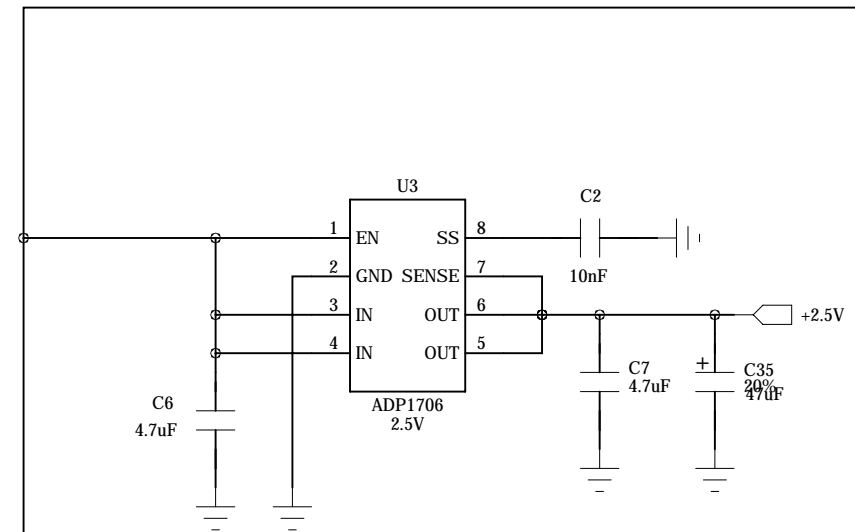
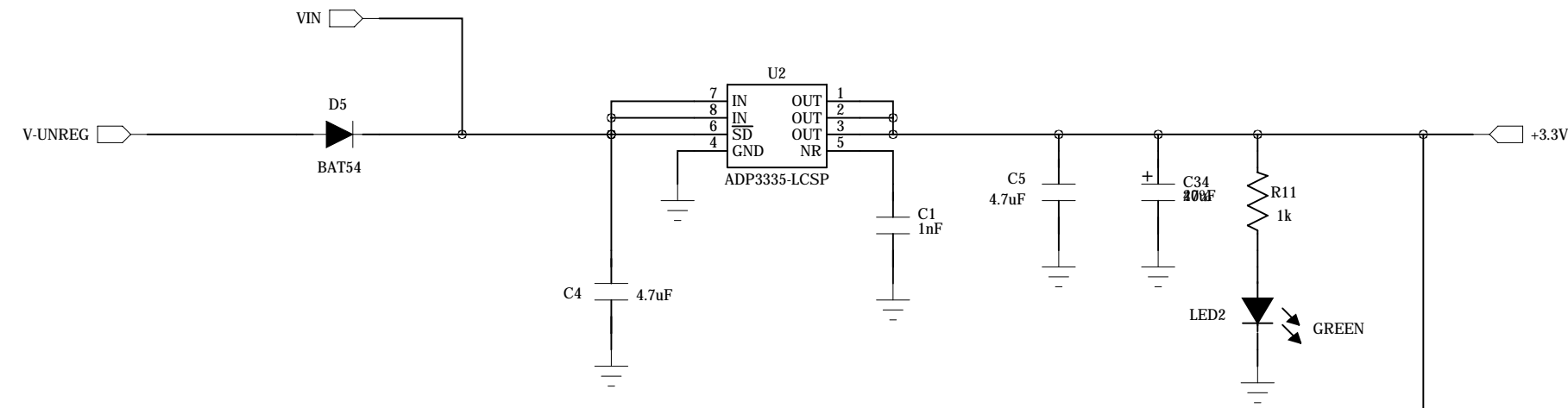
4

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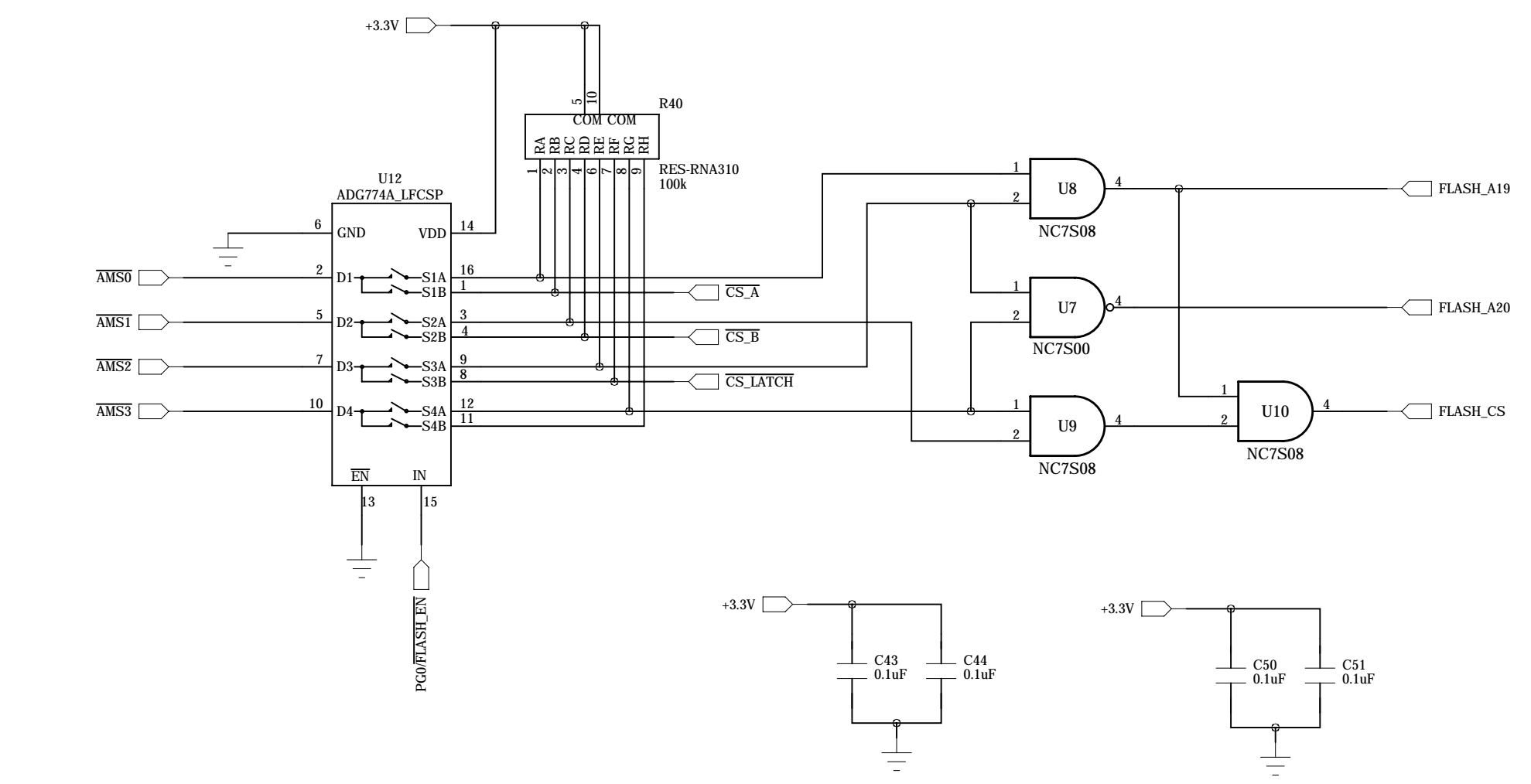
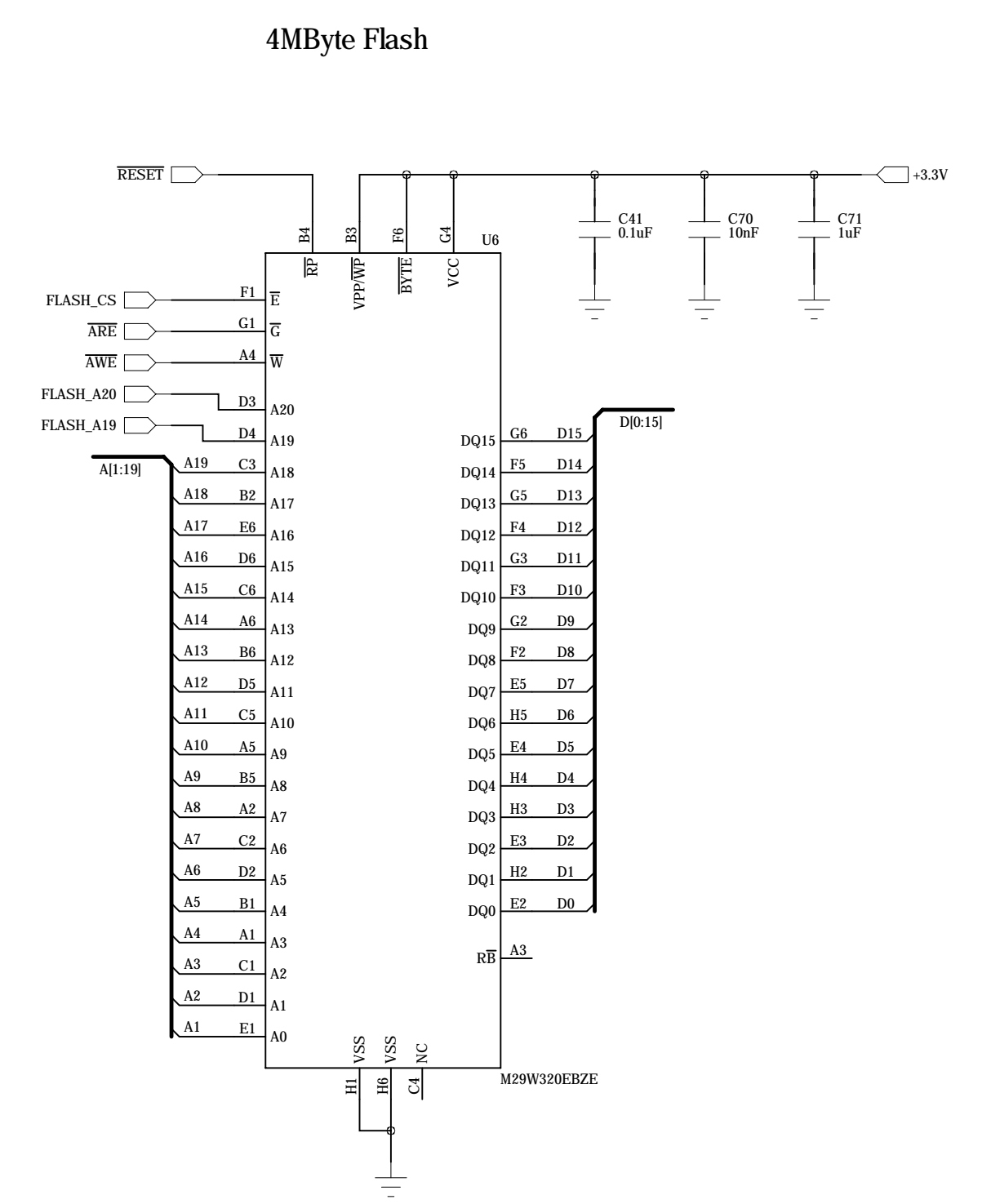
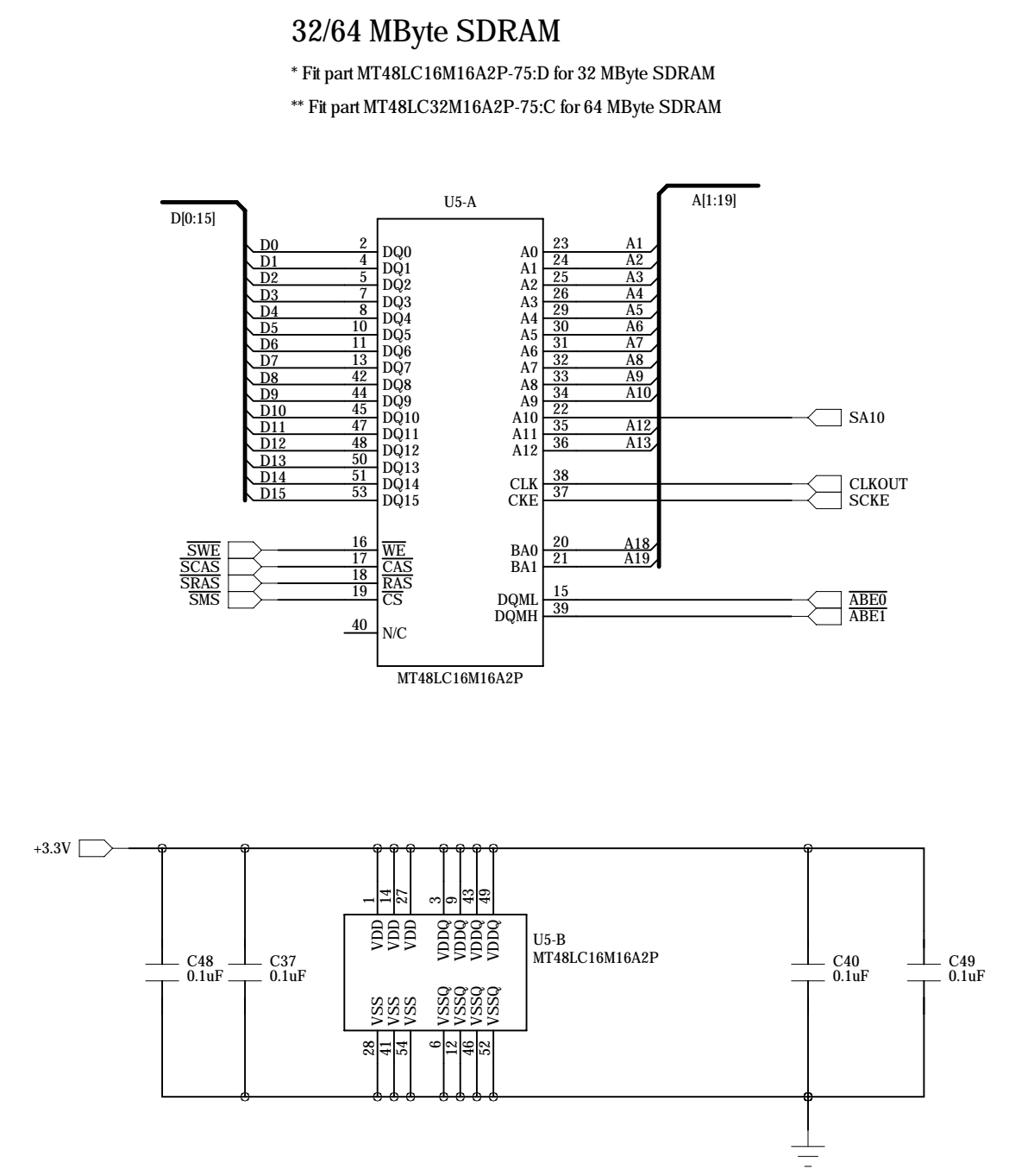
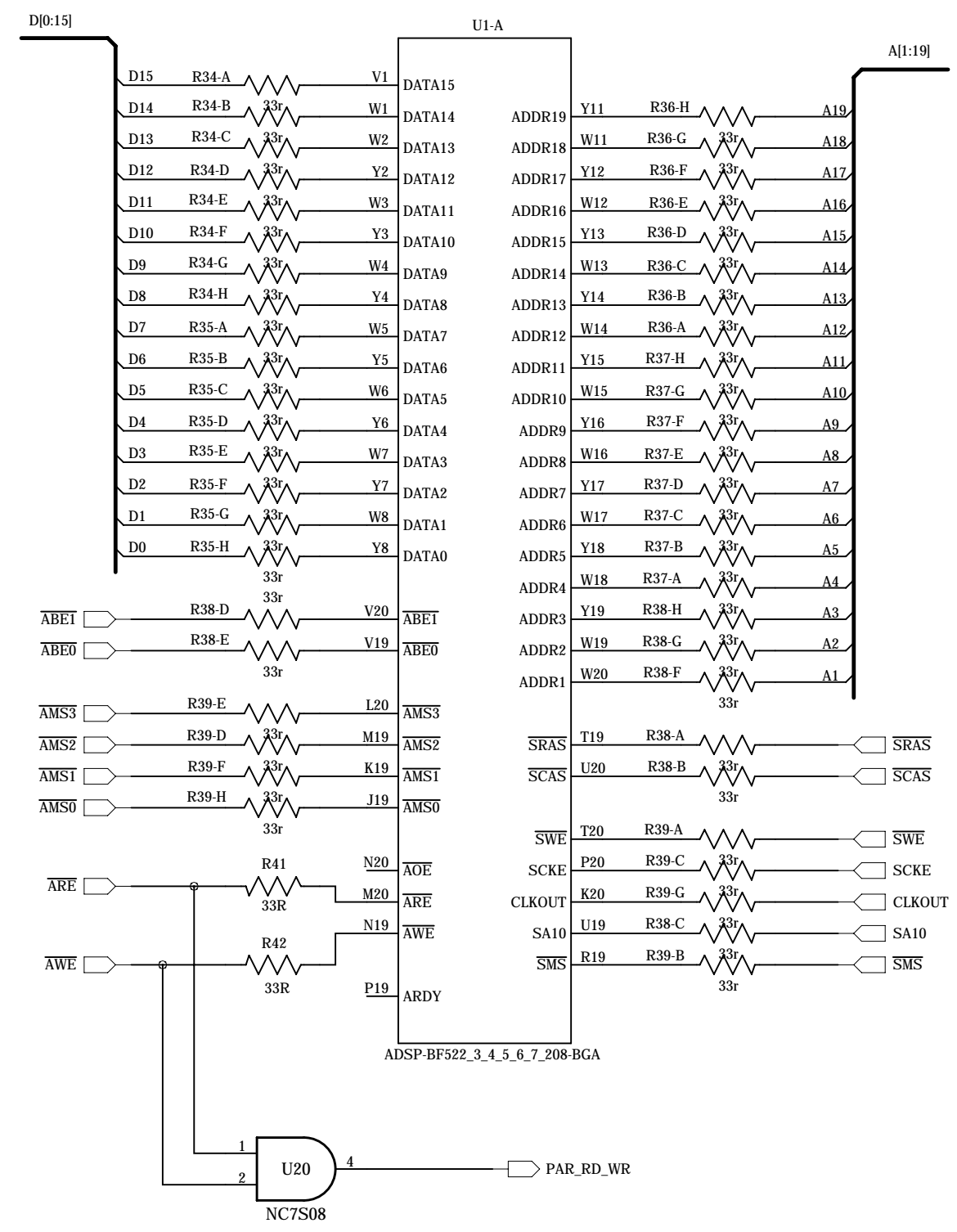
1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: ANALOG DEVICES			
TITLE: SYSTEM DEVELOPMENT PLATFORM POWER			
DRAWN: Paddy Duignan	DATED: 13-01-09	CODE:	REV: B
CHECKED: MICK McCARTHY	DATED: 14-01-10	SIZE:	DRAWING NO: SDP1Z
QUALITY CONTROL:	DATED:	SHEET: 1 OF 6	
RELEASED:	DATED:	SCALE:	

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: ANALOG DEVICES			
TITLE: SYSTEM DEVELOPMENT PLATFORM (MEMORY)			
CODE:	SIZE:	DRAWING NO:	REV:
		SDP1Z	B
SCALE:			SHEET: 2 OF 6

DRAWN: Paddy Duignan	DATED: 13-01-09
CHECKED: MICK McCARTHY	DATED: 14-01-10
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

6

5

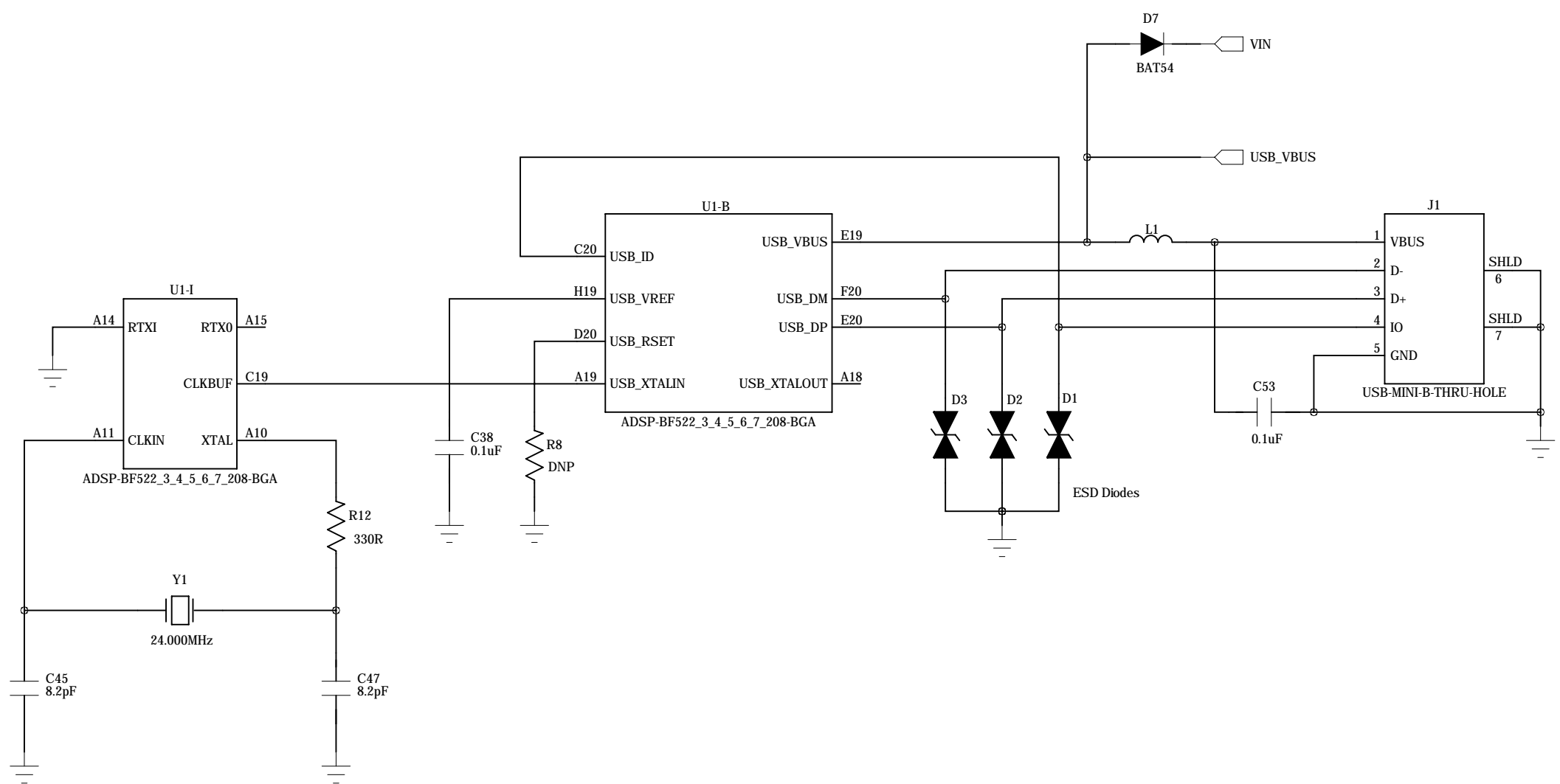
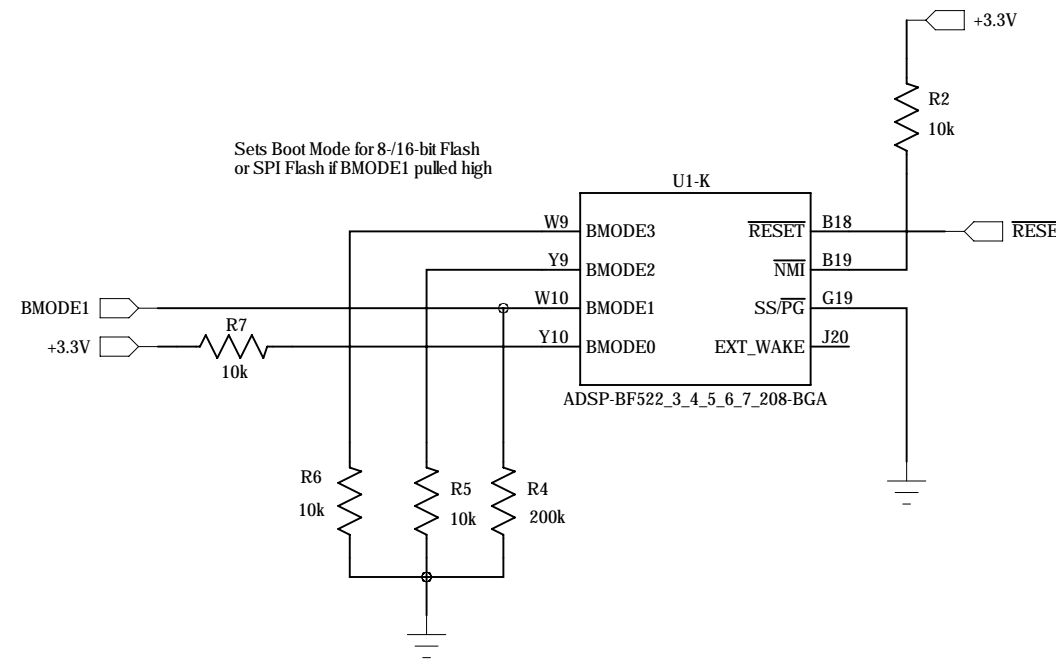
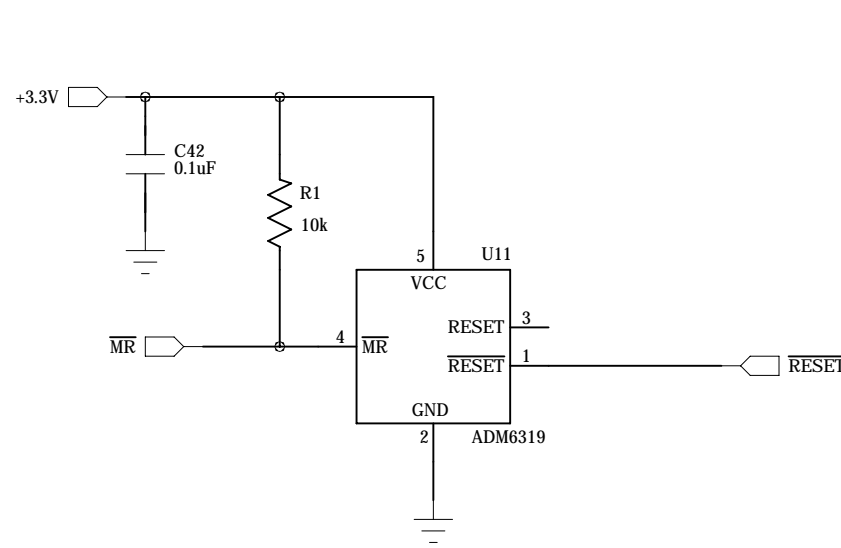
4

3

2

1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



COMPANY: ANALOG DEVICES			
TITLE: SYSTEM DEVELOPMENT PLATFORM (CLOCKS_USB)			
DRAWN: Paddy Duignan	DATED: 13-01-09	CODE:	REV: B
CHECKED: MICK McCARTHY	DATED: 14-01-10	SIZE:	DRAWING NO: SDP1Z
QUALITY CONTROL:	DATED:	SHEET: 3 OF 6	
RELEASED:	DATED:		

D

C

B

A

D

C

B

A

6

5

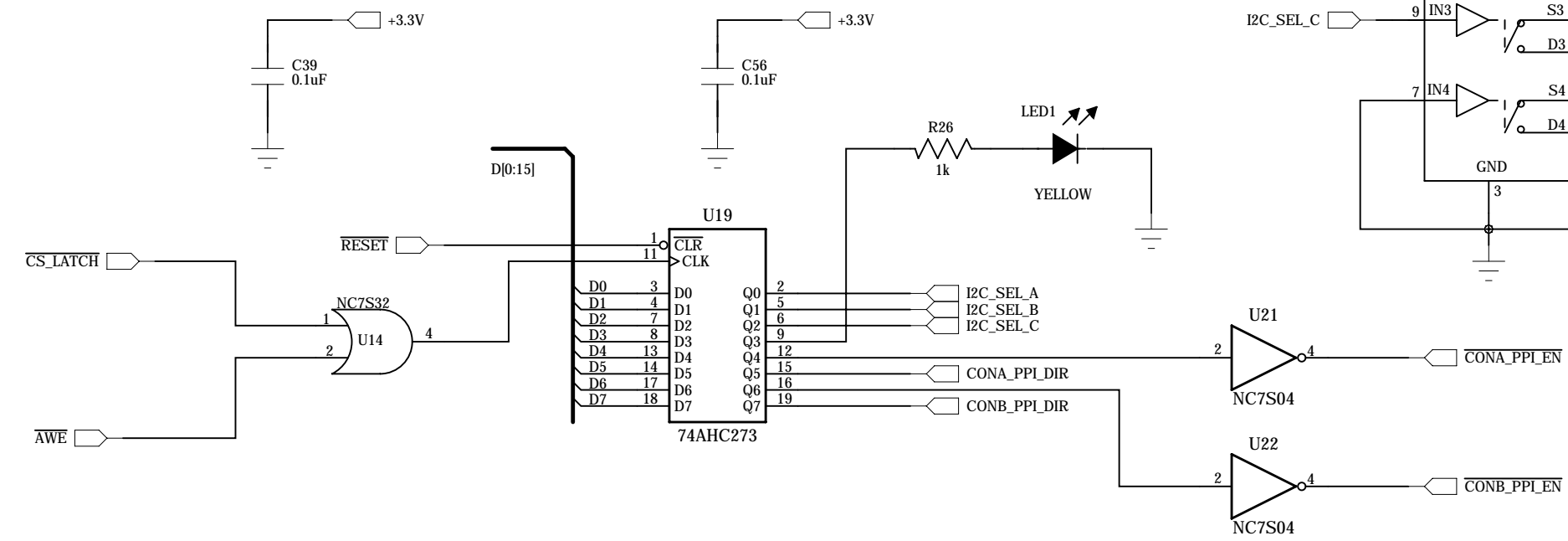
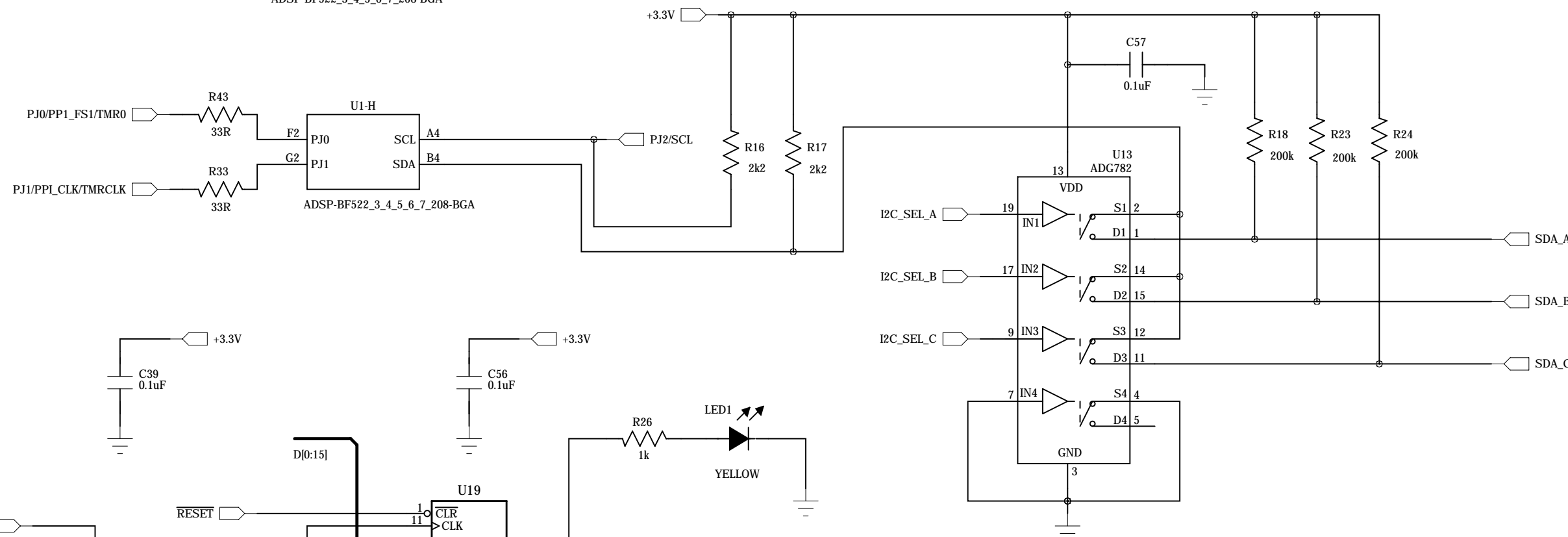
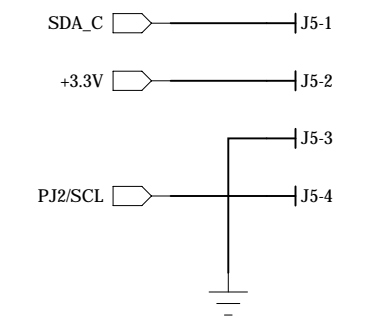
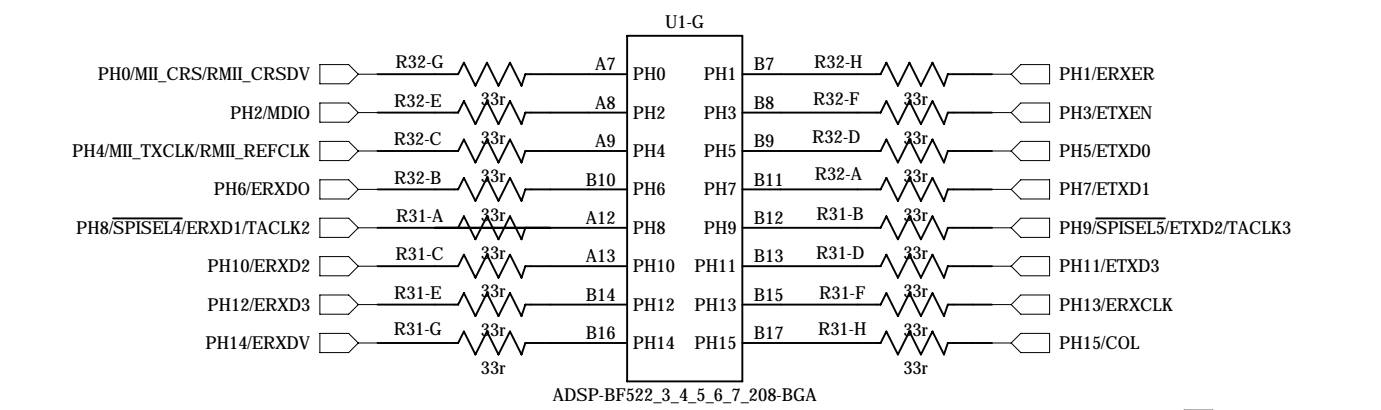
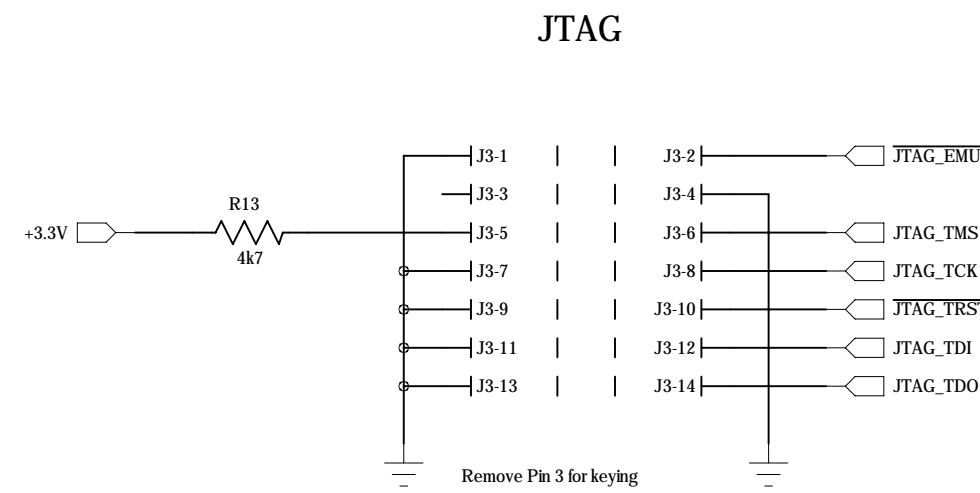
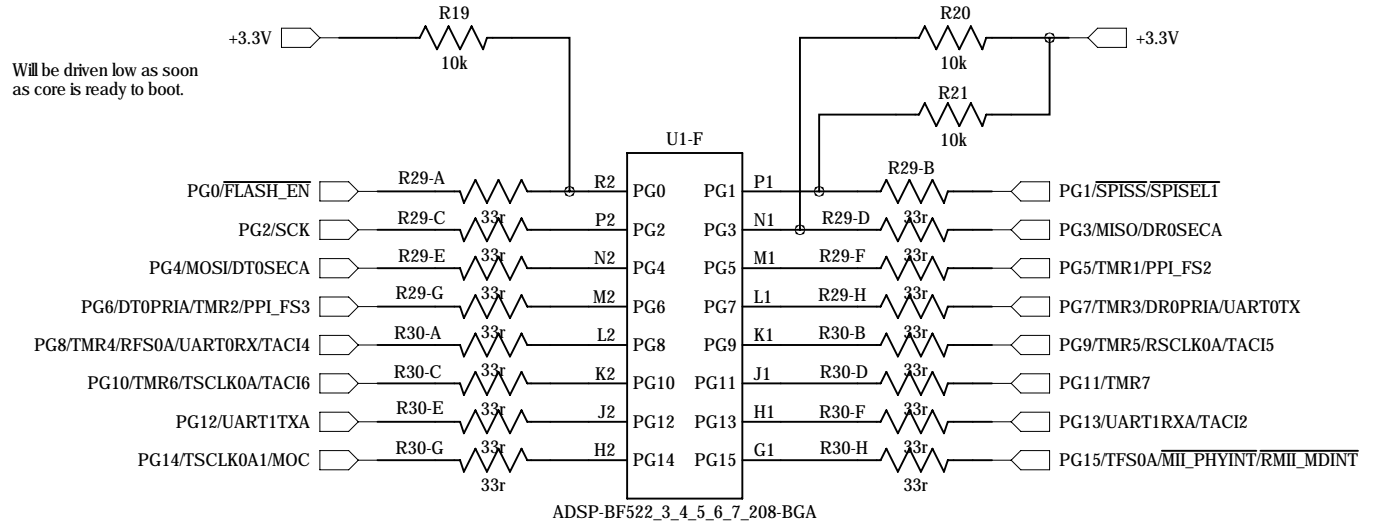
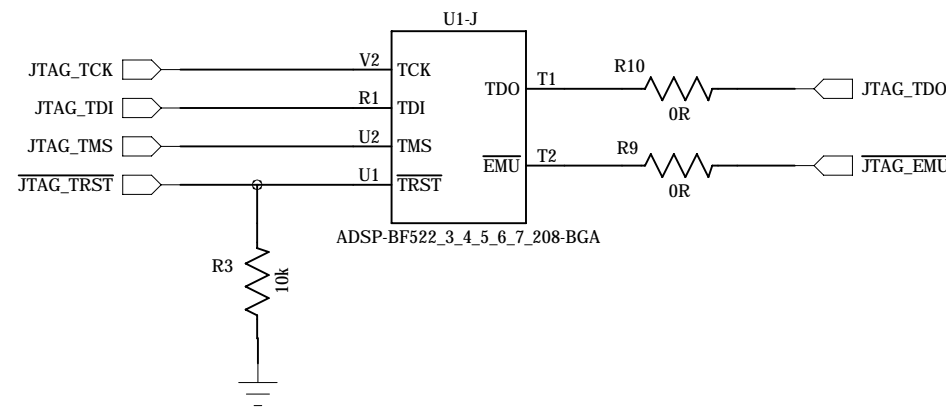
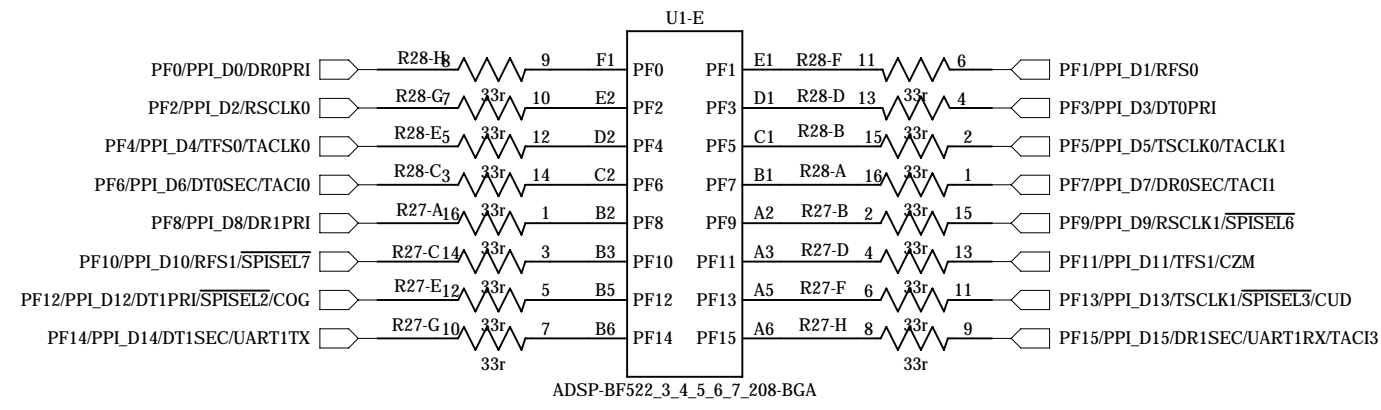
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3

2

1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DRAWN: Paddy Duignan	DATED: 13-01-09
CHECKED: MICK McCARTHY	DATED: 14-01-10
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

COMPANY: ANALOG DEVICES			
TITLE: SYSTEM DEVELOPMENT PLATFORM (BLACKFIN I/O)			
CODE:	SIZE:	DRAWING NO:	REV:
		SDP1Z	B
SCALE:			SHEET: 4 OF 6

6

5

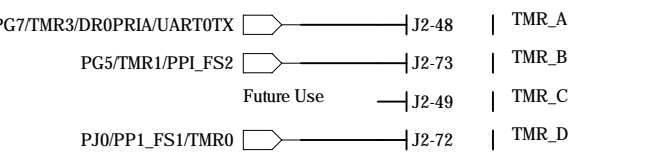
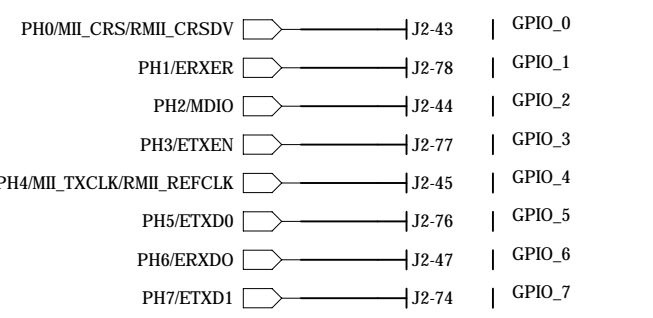
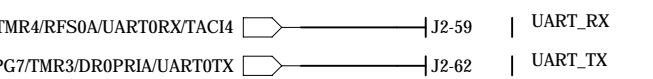
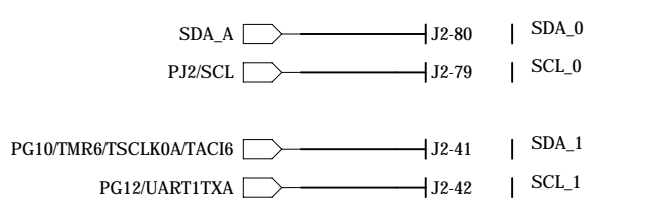
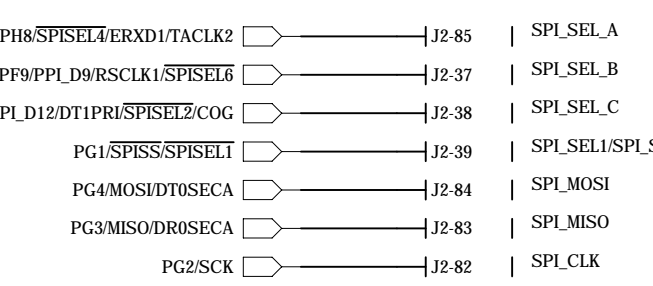
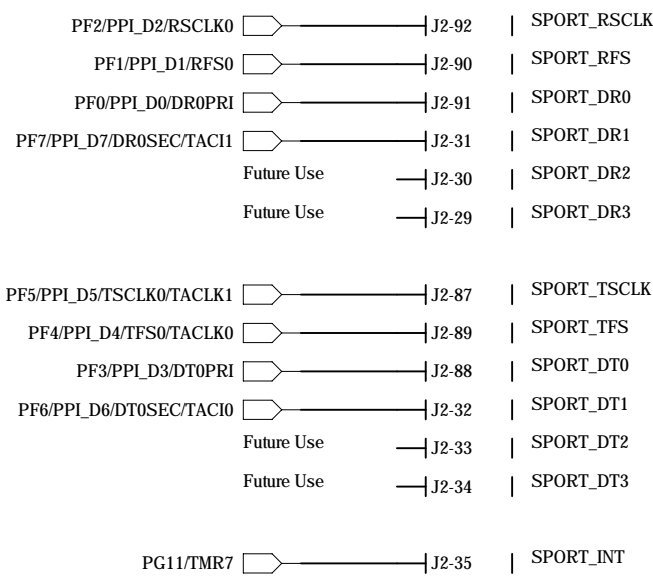
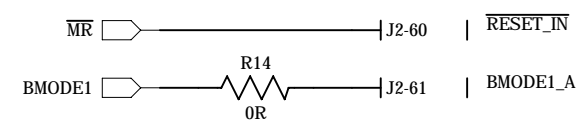
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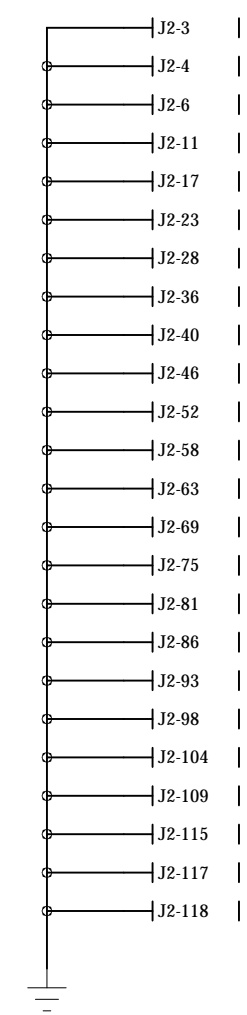
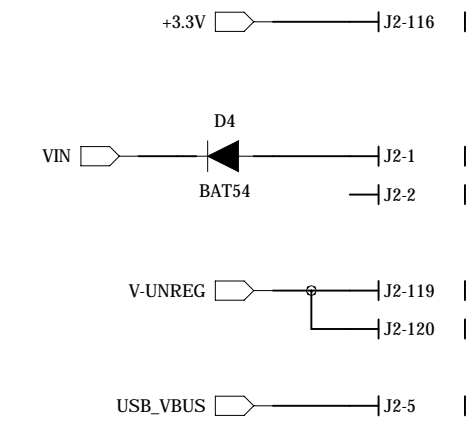
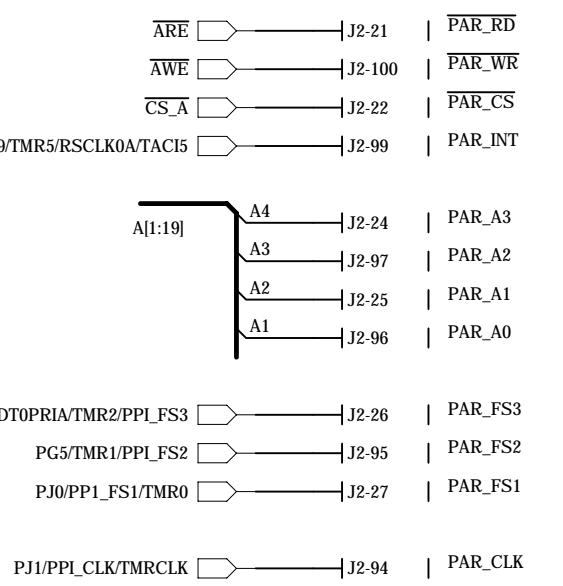
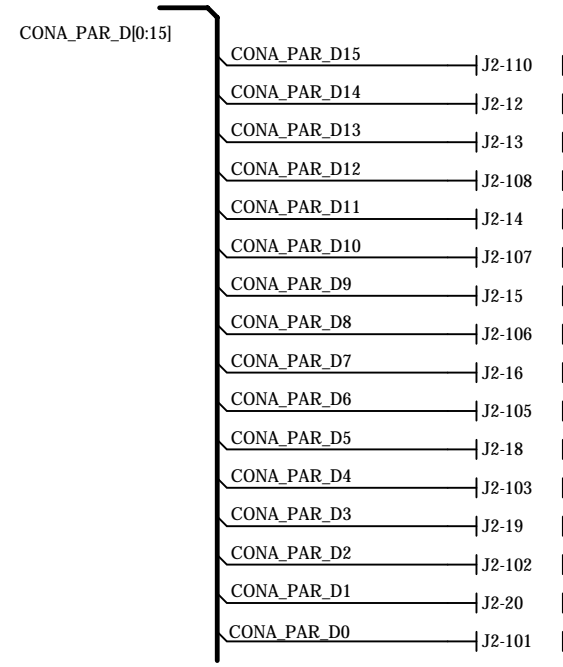
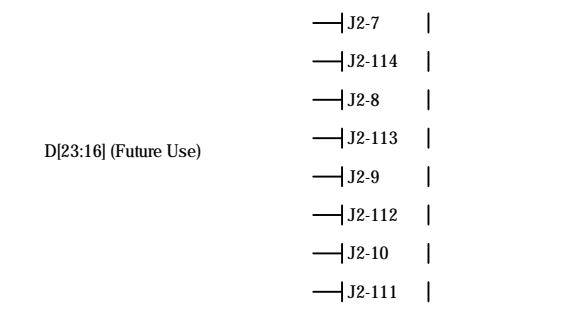
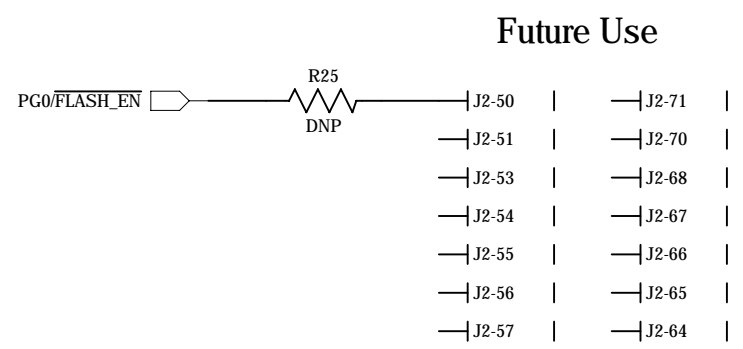
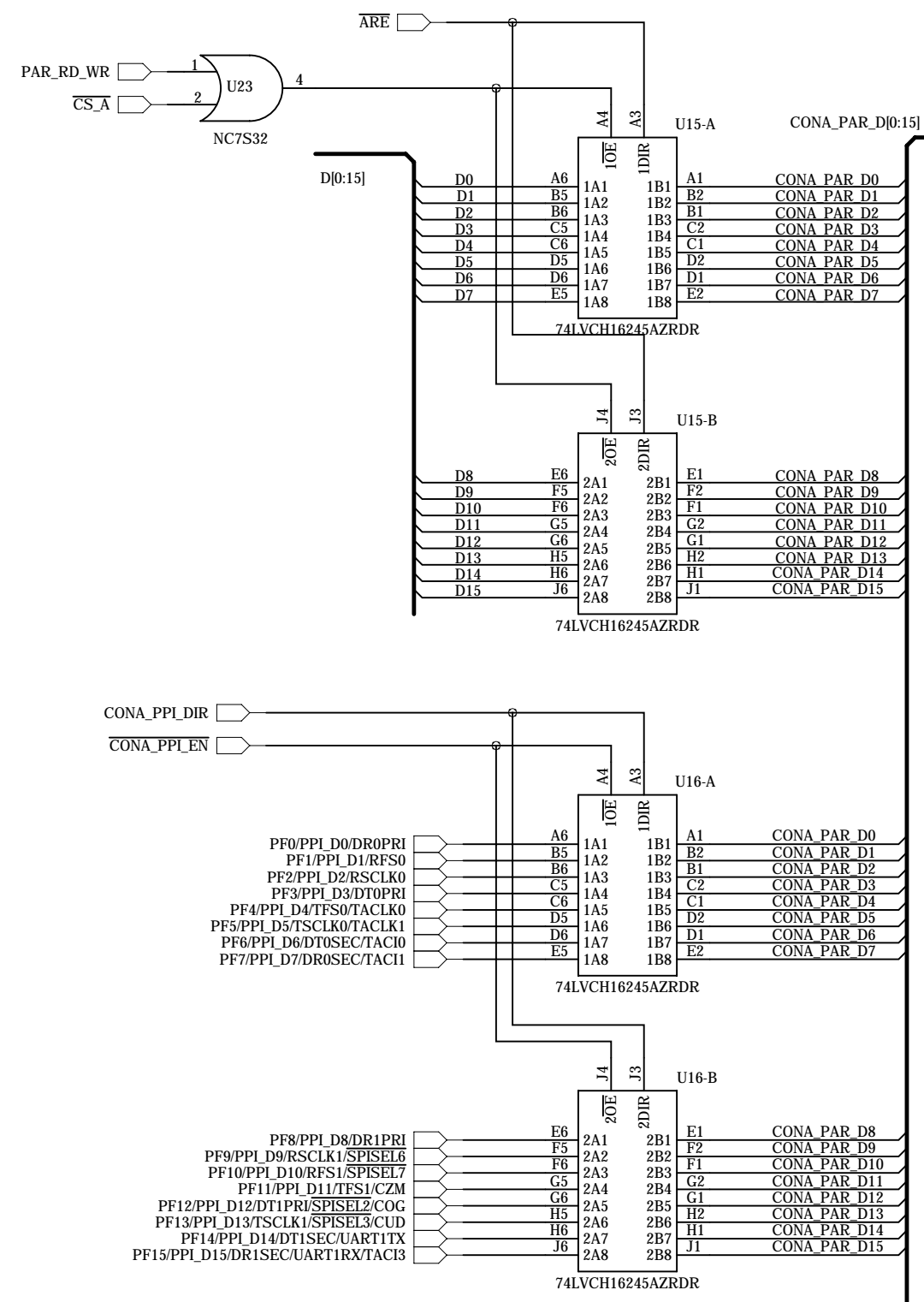
2

1

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DIR=1: A->B; DIR=0: B->A



COMPANY: ANALOG DEVICES			
TITLE: SYSTEM DEVELOPMENT PLATFORM (CONNECTOR A)			
CODE:	SIZE:	DRAWING NO:	REV: B
SCALE:		SHEET: 5 OF 6	

DRAWN: Paddy Duignan	DATED: 13-01-09
CHECKED: MICK McCARTHY	DATED: 14-01-10
QUALITY CONTROL:	DATED:
RELEASED:	DATED:

D
C
B
A

D
C
B
A

6

5

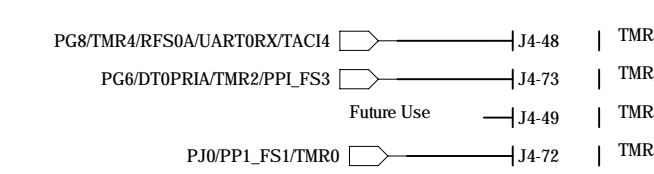
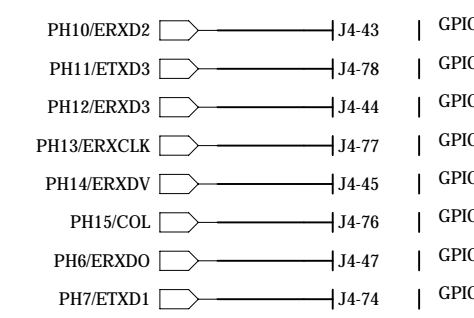
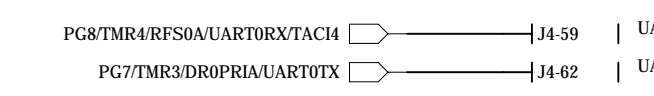
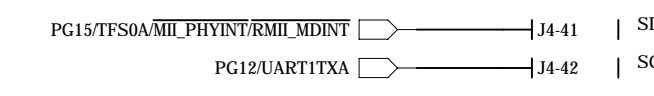
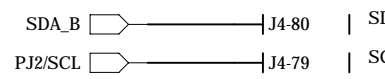
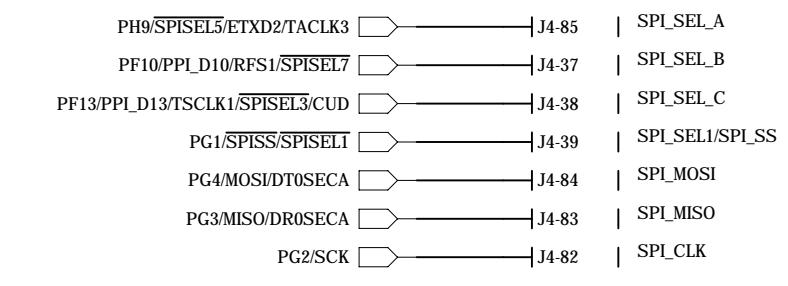
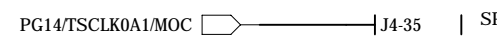
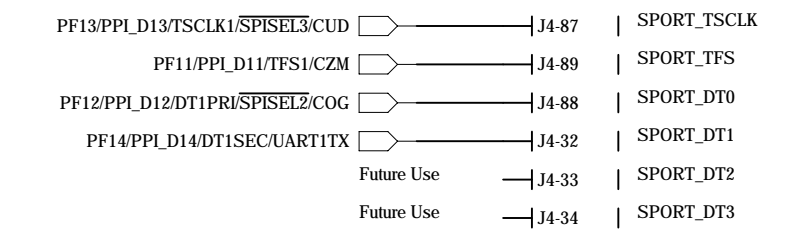
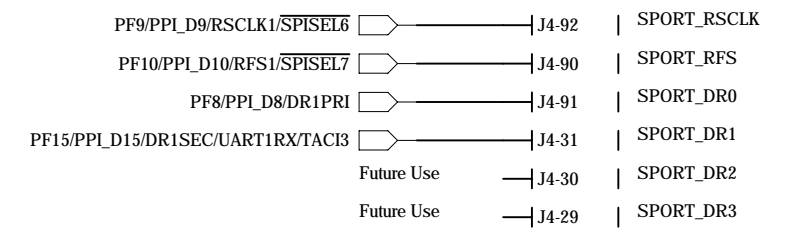
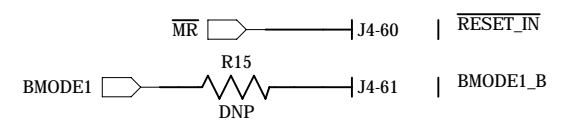
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3

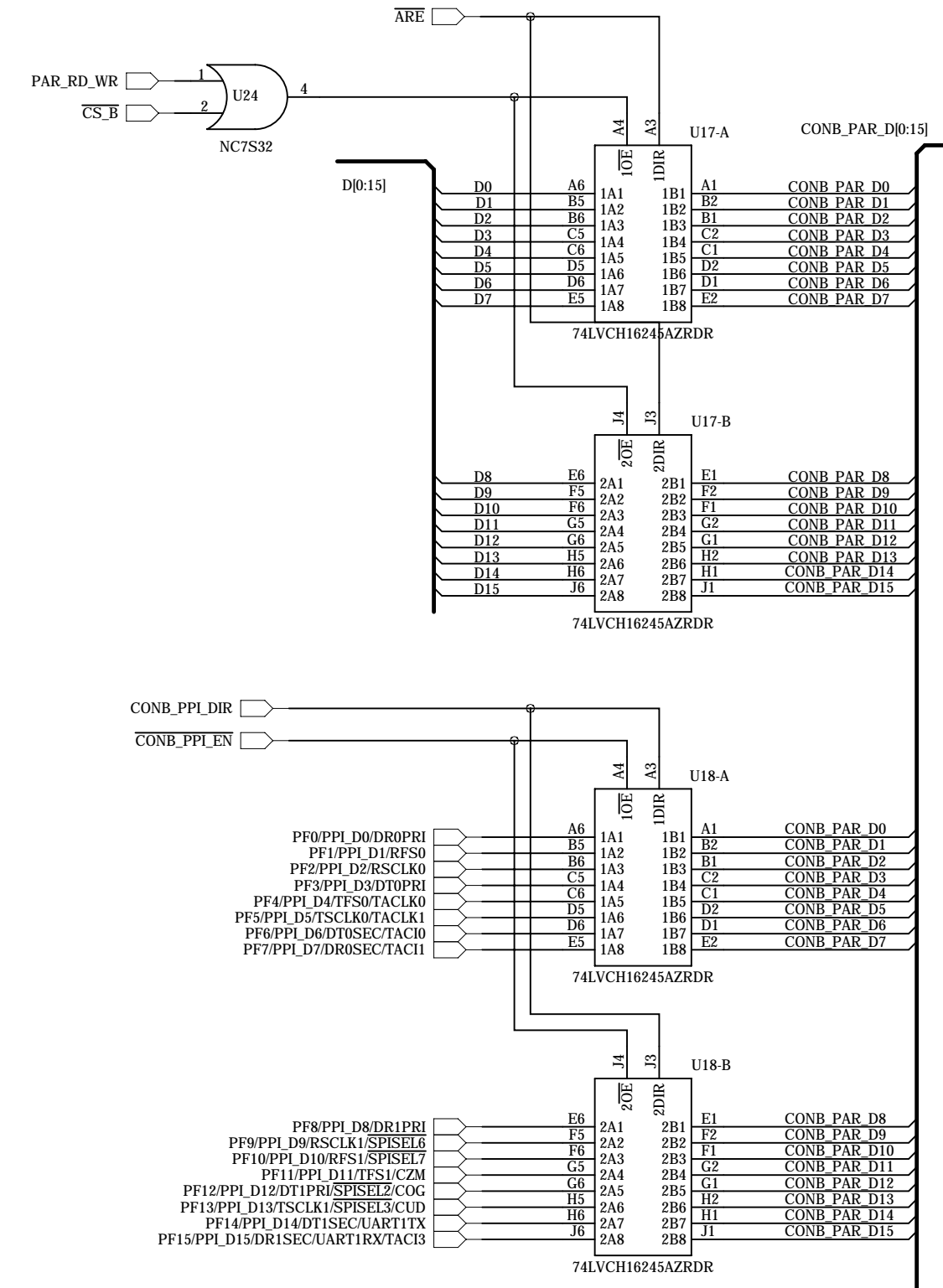
2

1

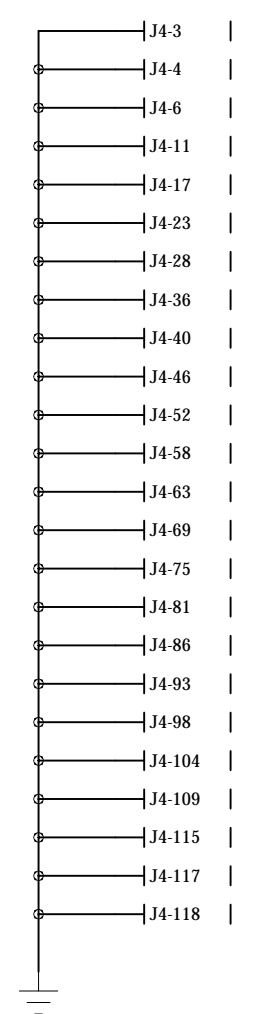
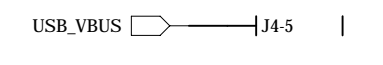
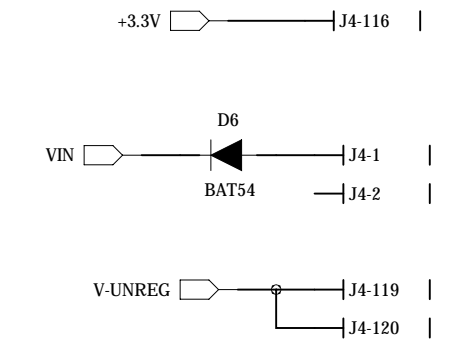
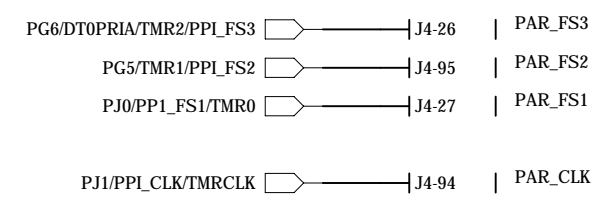
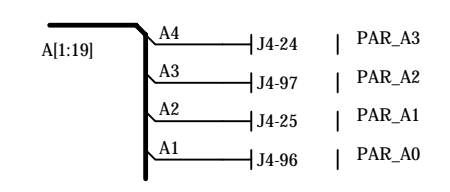
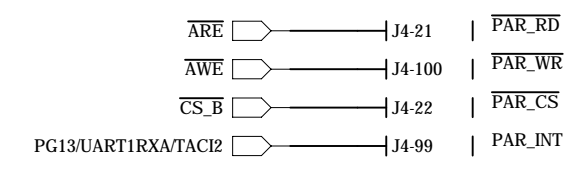
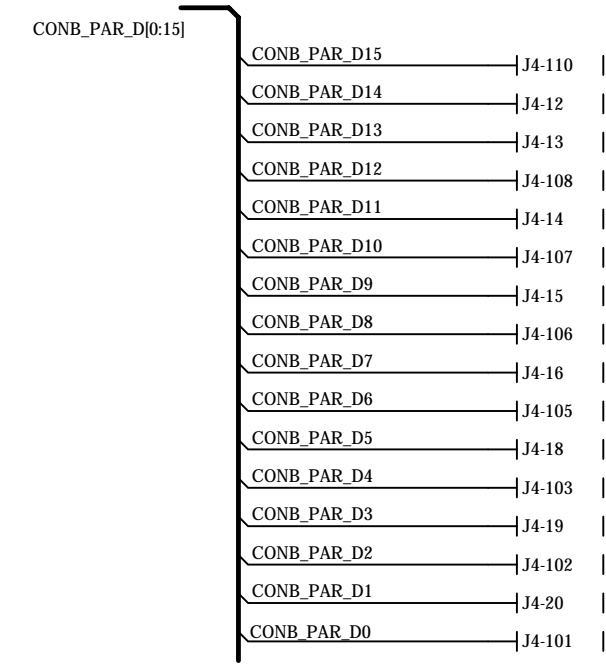
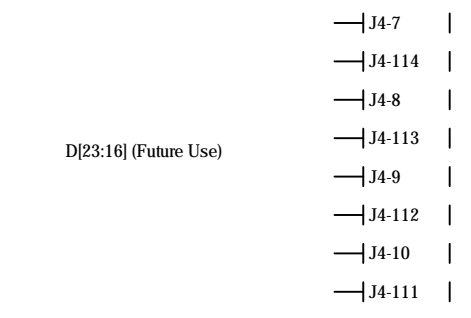
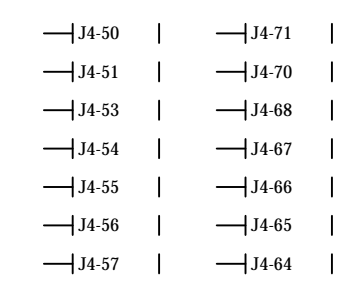
REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



DIR=1: A->B; DIR=0: B->A



Future Use



COMPANY: ANALOG DEVICES			
TITLE: SYSTEM DEVELOPMENT PLATFORM (CONNECTOR B)			
DRAWN: Paddy Duignan	DATED: 13-01-09	CODE:	SIZE:
CHECKED: MICK McCARTHY	DATED: 14-01-10	DRAWING NO:	REV: B
QUALITY CONTROL:	DATED:	SDP1Z	
RELEASED:	DATED:	SCALE:	SHEET: 6 OF 6