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RELAY CONTROL CHART

CONTROL	CODE	DEVICE	FUNCTION	CONNECTOR

JUMPER TABLE		
JP#	ON	OFF
1		
2		
3		
4		
5		

* SEE ASSEMBLY INSTRUCTIONS

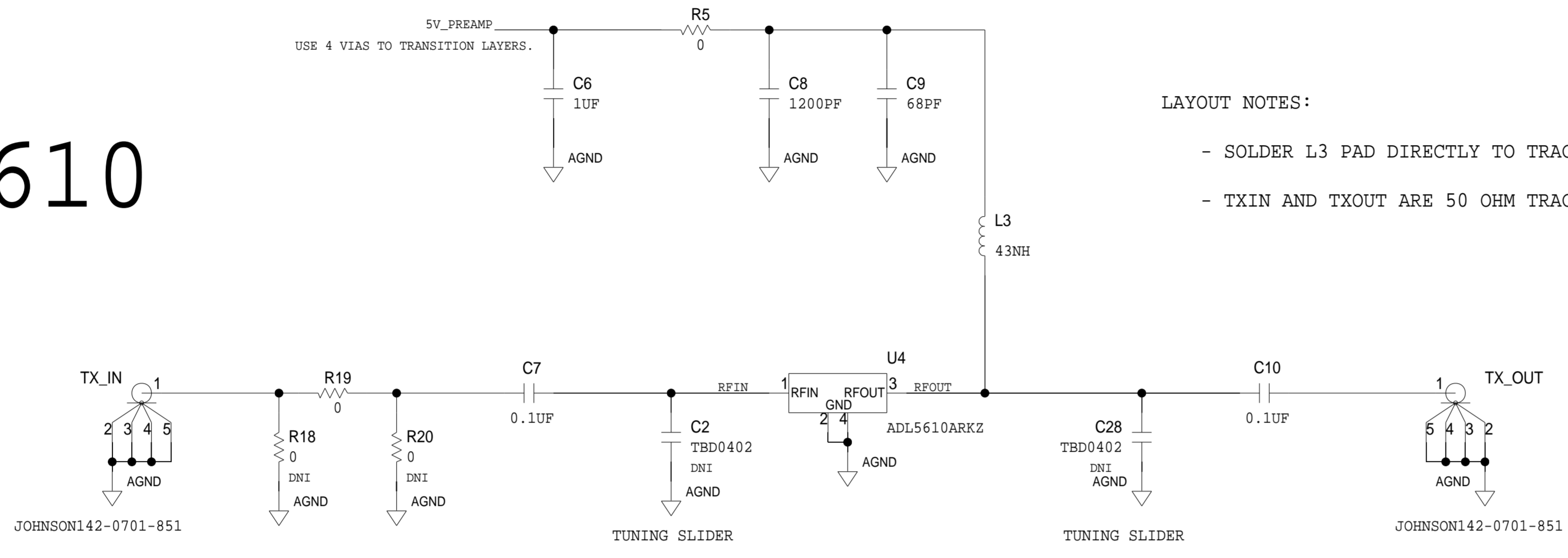
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED
A	ORIGINATION	03APR14	B.KAMAKOTI
B	AS PER ECR-048540	14OCT14	C.EL-KHOURY
B-1	AS PER ECR-052809	01OCT15	C.EL-KHOURY

TEMPLATE ENGINEER N/A HARDWARE SERVICES N/A HARDWARE SYSTEMS N/A TEST ENGINEER N/A COMPONENT ENGINEER N/A TEST PROCESS N/A HARDWARE RELEASE N/A DESIGNER M. LEWIN PTD ENGINEER B. KAMAKOTI CHECKER *	DATE 03APR14 03APR14	<h3>SCHEMATIC</h3>		
HW TYPE :ADL5521/5610 EVALZ Product(s) : ADL5521 (LFCSP), ADL5610 (SOT-89) : FMC COMMS Booster Board PACKAGE : 8-lead 3X3_MM LFCSP and SOT-89 : Pitch-pitch StyleVendor Style		TESTER : N/A CONTACTOR : N/A HANDLER : N/A		
MASTER PROJECT TEMPLATE : TBD TESTER TEMPLATE : no_template		DRAWING NO. 02_038228	REV. B	
<small>UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES</small> TOLERANCES DECIMALS : X.XX +0.010 FRACTIONS : +1/32 ANGLES : +2			SIZE : D SCALE : None CODE ID NO. : 24355 SHEET 1 OF 3	

P.O SPEC.	BK/BD SPEC.	SOCKET OEM	OEM PART#	HANDLER

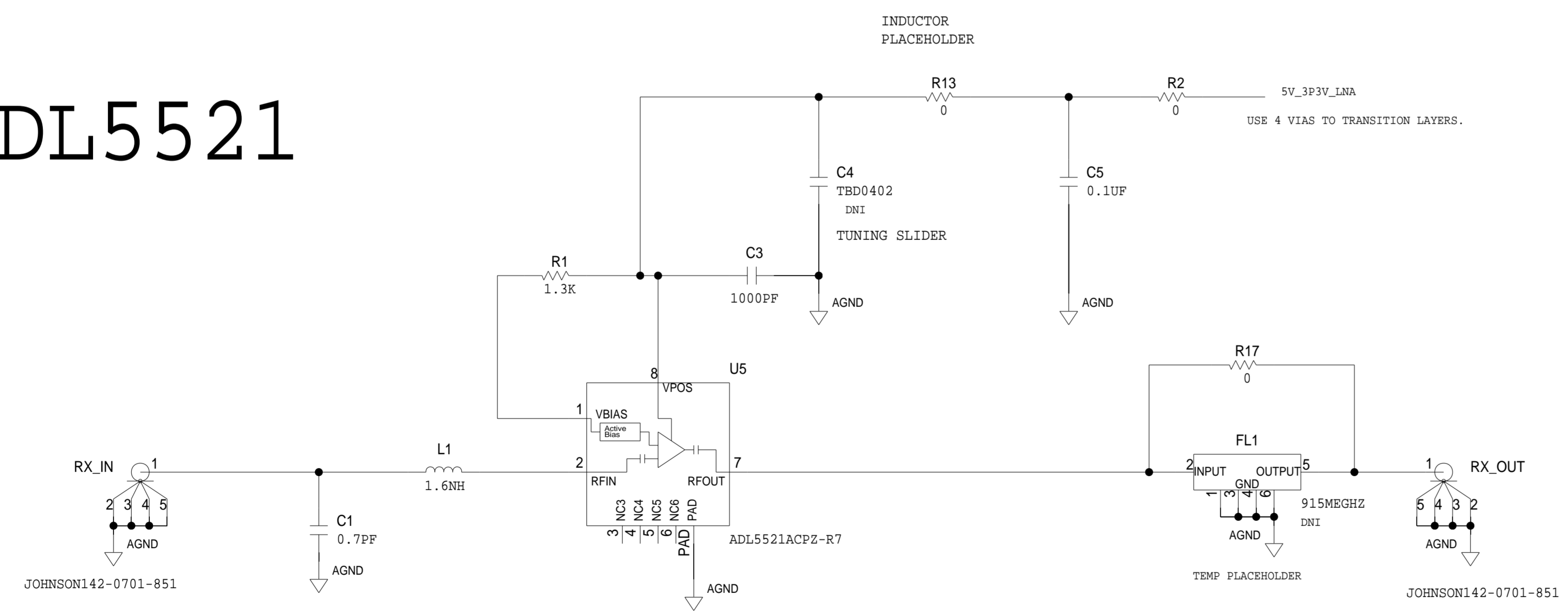
REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

ADL5610



LAYOUT NOTES:
 - SOLDER L3 PAD DIRECTLY TO TRACE.
 - TXIN AND TXOUT ARE 50 OHM TRACES WITH EXPOSED GROUND FOR TUNING.

ADL5521

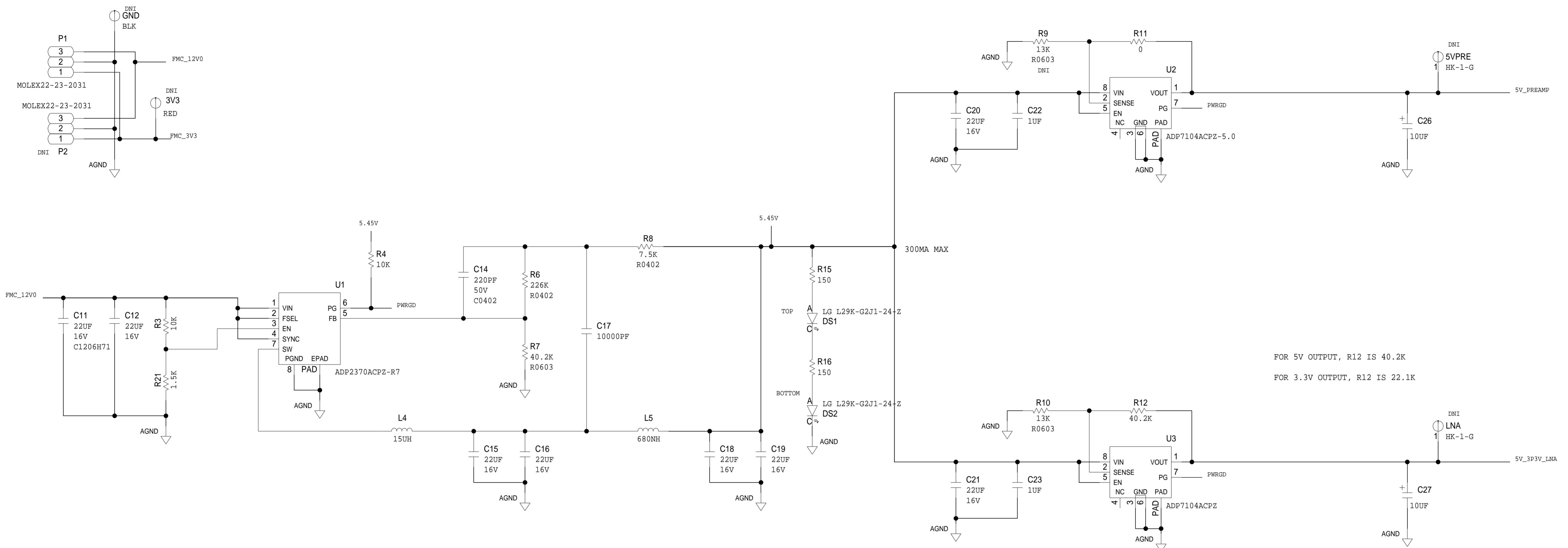


LAYOUT NOTES:
 - C3 AND C4 ARE "SLIDERS" FOR TUNING.
 - OPTIMUM TUNING DEFAULT: 2.4GHZ.

	SCHEMATIC		
	HW TYPE :ADL5521/5610 EVALZ Product(s) : ADL5521 (LFCSP), ADL5610 (SOT-89) : FMC COMMS Booster Board		
	DESIGN VIEW FLAT:	DRAWING NO. 02_038228	REV B
	PTD ENGINEER B. KAMAKOTI	SIZE D	SCALE None
SHEET 2		OF 3	

POWER SUPPLY

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



FOR 5V OUTPUT, R12 IS 40.2K
 FOR 3.3V OUTPUT, R12 IS 22.1K

	SCHEMATIC		
	HW TYPE :ADL5521/5610 EVALZ Product(s): ADL5521 (LFCSP), ADL5610 (SOT-89) : FMC COMMS Booster Board		
	DESIGN VIEW FLAT:	DRAWING NO. 02_038228	REV B
PTD ENGINEER B. KAMAKOTI	SIZE D	SCALE None	SHEET 3 OF 3