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Evaluation Boards & Kits

System Demonstration Platform

The System Demonstration Platform (SDP) is used as part of the evaluation system for many Analog Devices components. The SDP board is designed to be used in conjunction with various ADI component evaluation boards as part of a customer evaluation environment.

User Guides

Evaluation board user guides provide a quick reference to Analog Devices evaluation boards including instructions on how to install the board software and setup the hardware.

Data Pattern Generator

The Data Pattern Generator is a bench-top instrument for driving vectors into Analog Devices' high-speed Digital-to-Analog converters. The DPG connects to a PC over USB, and allows a user to download a vector from their PC into the DPG's internal memory. Once downloaded, the vector can be played out to an attached Evaluation Board for a specific DAC at full speed. This allows for rapid evaluation of the DAC with both generic and customer-generated test data.

ezLINX™ iCoupler® Isolated Interface Development environment

The *ez*LINX *i*Coupler Isolated Interface Development environment provides developers with a cost-effective, plug and play solution for evaluating eight physical Layer, digitally-isolated communication standards(USB, RS-422, RS-485, RS-232, CAN, SPI, I2C and LVDS). The Blackfin ADSP-BF548 processor is used to run the uCLinux operating system and allows for easy customization through the open source hardware and software platform. Development time is significantly reduced for embedded designers and system architects who are designing and evaluating isolated communication standards.The Interfaces on ezLINX use ADI's isolated transceivers with integrated *i* Coupler and *iso*Power® digital isolator technology.

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